

Hyunchul Chung

List of Publications by Year in descending order

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142
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#	ARTICLE	IF	CITATIONS
1	A 57.5â€“65.5 GHz Phased-Array Transmit Beamformer in 45 nm CMOS SOI With 5 dBm and 6.1% Linear PAE for 400 MBaud 64-QAM Waveforms. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 1772-1779.	4.6	7
2	A 37â€“42-GHz 8-Element Phased-Array With 48â€“51-dBm EIRP, 64â€“QAM 30-Gb/s Data Rates, and EVM Analysis Versus Channel RMS Errors. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 4753-4764.	4.6	45
3	A Packaged 0.01â€“26-GHz Single-Chip SiGe Reflectometer for Two-Port Vector Network Analyzers. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 1794-1808.	4.6	15
4	A Milliwatt-Level 70â€“110 GHz Frequency Quadrupler With >30 dBc Harmonic Rejection. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 1697-1705.	4.6	22
5	A 1 V 54-64 GHz 4-Channel Phased-Array Receiver in 45 nm RFSOI with 3.6/5.1 dB NF and -23 dBm IP1dB at 28/37 mW Per-Channel. , 2019, , .		3
6	A 5G 24-30 GHz 2x32 Element Dual-Polarized Dual-Beam Phased Array Base-Station for 2x2 MIMO System. , 2019, , .		4
7	A 4-Channel 10â€“40 GHz Wideband Receiver with Integrated Frequency Quadrupler for High Resolution Millimeter-Wave Imaging Systems. , 2018, , .		7
8	A 35â€“105 GHz High Image-Rejection-Ratio IQ Receiver with Integrated LO Doubler and > 40 dB IRR. , 2018, , .		7
9	A low EVM SiGe BiCMOS 40â€“100 GHz Direct Conversion IQ Modulator for Multi-Gbps Communications Systems. , 2018, , .		6
10	A 10â€“40 GHz frequency quadrupler source with switchable bandpass filters and > 30 dBc harmonic rejection. , 2017, , .		24
11	A 70â€“110 GHz single-chip SiGe reflectometer with integrated local oscillator quadrupler. , 2017, , .		12
12	A 0.01â€“26 GHz single-chip SiGe reflectometer for two-port vector network analyzers. , 2017, , .		9