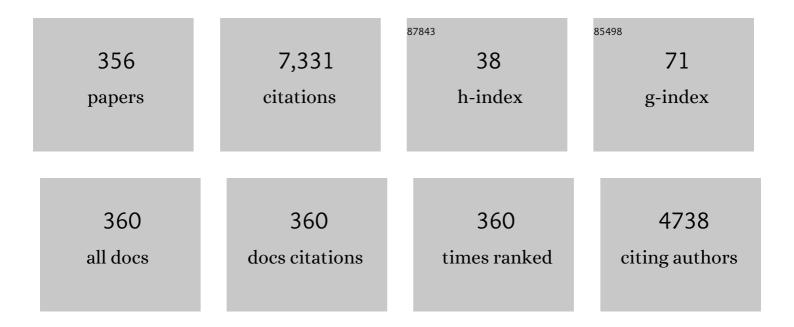
Ingrid De Wolf

List of Publications by Year in descending order

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#	Article	IF	CITATIONS
1	Thermal Modelling of Silicon Photonic Ring Modulator with Substrate Undercut. Journal of Lightwave Technology, 2022, 40, 4357-4363.	2.7	18
2	Gate-Induced-Drain-Leakage (GIDL) in CMOS Enhanced by Mechanical Stress. IEEE Transactions on Electron Devices, 2022, 69, 2214-2217.	1.6	6
3	Significant Enhancement of HCD and TDDB in CMOS FETs by Mechanical Stress. , 2022, , .		1
4	Electromigration Performance Improvement of Metal Heaters for Si Photonic Ring Modulators. IEEE Transactions on Device and Materials Reliability, 2022, , 1-1.	1.5	2
5	Properties of ultrathin molybdenum films for interconnect applications. Materialia, 2022, 24, 101511.	1.3	15
6	Investigation of the Impact of Externally Applied Out-of-Plane Stress on Ferroelectric FET. IEEE Electron Device Letters, 2021, 42, 264-267.	2.2	5
7	A multi-energy level agnostic simulation approach to defect generation. Solid-State Electronics, 2021, 184, 108056.	0.8	2
8	Hot-Electron-Induced Punch-Through (HEIP) Effect in p-MOSFET Enhanced by Mechanical Stress. IEEE Electron Device Letters, 2021, 42, 1424-1427.	2.2	9
9	A Novel Resistance Measurement Methodology for \$In~Situ\$ UBM/Solder Interfacial Reaction Monitoring. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 30-38.	1.4	3
10	Impact of Mechanical Stress on 3-D NAND Flash Current Conduction. IEEE Transactions on Electron Devices, 2020, 67, 4891-4896.	1.6	6
11	Nozzle scaling effects for the thermohydraulic performance of microjet impingement cooling with distributed returns. Applied Thermal Engineering, 2020, 180, 115767.	3.0	11
12	On the impact of mechanical stress on gate oxide trapping. , 2020, , .		5
13	Optical Beam-Based Defect Localization Methodologies for Open and Short Failures in Micrometer-Scale 3-D TSV Interconnects. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 1542-1551.	1.4	8
14	A novel iso-thermal intermetallic compound insertion bonding approach to improve throughput for 3D die to wafer stacking. , 2020, , .		4
15	Magnetic field imaging and light induced capacitance alteration for failure analysis of Cu-TSV interconnects. Microelectronics Reliability, 2020, 114, 113780.	0.9	1
16	Low-frequency noise and thermal equilibrium properties of vacancies. Applied Physics Letters, 2020, 116, 193501.	1.5	0
17	A Novel Intermetallic Compound Insertion Bonding to Improve Throughput for Sequential 3-D Stacking. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2020, 10, 669-678.	1.4	6
18	(Invited) Alternative Metals for Beyond-Cu Interconnects. ECS Meeting Abstracts, 2020, MA2020-01, 1293-1293.	0.0	1

#	Article	IF	CITATIONS
19	Fault Isolation of Resistive/Open 3-D Wafer Bonding Interconnects by Thermal Laser Stimulation and Light-Induced Capacitance Alteration. , 2020, , .		0
20	Effects of isothermal storage on grain structure of Cu/Sn/Cu microbump interconnects for 3D stacking. Microelectronics Reliability, 2019, 102, 113296.	0.9	12
21	Impact of Mechanical Stress on the Electrical Performance of 3D NAND. , 2019, , .		5
22	Thermal Analysis of Polymer 3D Printed Jet Impingement Coolers for High Performance 2.5D Si Interposer Packages. , 2019, , .		5
23	Study of the effect of Sn grain boundaries on IMC morphology in solid state inter-diffusion soldering. Scientific Reports, 2019, 9, 14862.	1.6	11
24	Study of out-of-plane mechanical stress impact on Si BJT and diffusion resistor using in-situ nanoindentation probing. Microelectronics Reliability, 2019, 100-101, 113367.	0.9	1
25	First Demonstration of a Low Cost/Customizable Chip Level 3D Printed Microjet Hotspot-Targeted Cooler for High Power Applications. , 2019, , .		5
26	Low-Frequency Noise Measurements to Characterize Cu-Electromigration Down to 44nm Metal Pitch. , 2019, , .		2
27	New Access to Soft Breakdown Parameters of Low-k Dielectrics Through Localisation-Based Analysis. , 2019, , .		Ο
28	Study of the Mechanical Stress Impact on Silicide Contact Resistance by 4-Point Bending. , 2019, , .		0
29	Low-frequency noise and defects in copper and ruthenium resistors. Applied Physics Letters, 2019, 114, .	1.5	13
30	A novel electromigration characterization method based on low-frequency noise measurements. Semiconductor Science and Technology, 2019, 34, 075002.	1.0	8
31	Experimental Characterization of a Chip-Level 3-D Printed Microjet Liquid Impingement Cooler for High-Performance Systems. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2019, 9, 1815-1824.	1.4	13
32	Experimental characterization and model validation of liquid jet impingement cooling using a high spatial resolution and programmable thermal test chip. Applied Thermal Engineering, 2019, 152, 308-318.	3.0	31
33	Low-Frequency Noise Measurements for Electromigration Characterization in BEOL Interconnects. , 2019, , .		Ο
34	Characterization of Impact of Vertical Stress on FinFETs. , 2019, , .		3
35	Electromigration Activation Energies in Alternative Metal Interconnects. IEEE Transactions on Electron Devices, 2019, 66, 5278-5283.	1.6	22
36	High-Efficiency Polymer-Based Direct Multi-Jet Impingement Cooling Solution for High-Power Devices. IEEE Transactions on Power Electronics, 2019, 34, 6601-6612.	5.4	40

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37	Non-invasive soft breakdown localisation in low k dielectrics using photon emission microscopy and thermal laser stimulation. Microelectronics Reliability, 2019, 92, 73-78.	0.9	4
38	Defect localization of metal interconnection lines in 3-dimensional through-silicon-via structures by differential scanning photocapacitance microscopy. Applied Physics Letters, 2018, 112, 071904.	1.5	4
39	Expected Failures in 3-D Technology and Related Failure Analysis Challenges. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2018, 8, 711-718.	1.4	18
40	Physics of self-aligned assembly at room temperature. Physics of Fluids, 2018, 30, .	1.6	2
41	Anisotropic stress in narrow sGe fin field-effect transistor channels measured using nano-focused Raman spectroscopy. APL Materials, 2018, 6, .	2.2	9
42	Fast and Distributed Thermal Model for Thermal Modeling of GaN Power Devices. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2018, 8, 1747-1755.	1.4	21
43	Method to assess the impact of LER and spacing variation on BEOL dielectric reliability using 2D-field simulations for <20nm spacing. , 2018, , .		Ο
44	In-situ Investigation of the Impact of Externally Applied Vertical Stress on III-V Bipolar Transistor. , 2018, , .		7
45	An in-situ resistance measurement to extract IMC resistivity and kinetic parameter of alternative metallurgies for 3D stacking. , 2018, , .		2
46	Stress Induced Densification of Thin Porous Low-K Films During Nanoindentation. , 2018, , .		0
47	(Invited) Raman Stress Measurements at the Nanoscale. ECS Transactions, 2018, 86, 311-320.	0.3	2
48	(Invited) Determining Si Composition in SiGe Alloys with < 1% Si Concentrations Using Raman Spectroscopy. ECS Transactions, 2018, 86, 397-407.	0.3	1
49	Detection of failure mechanisms in 24–40†nm FinFETs with (spectral) photon emission techniques using InGaAs camera. Microelectronics Reliability, 2018, 88-90, 334-338.	0.9	Ο
50	Photon emission as a characterization tool for bipolar parasitics in FinFET technology. Microelectronics Reliability, 2018, 88-90, 273-276.	0.9	3
51	Contactless Fault Isolation of Ultra Low k Dielectrics in Soft Breakdown Condition. , 2018, , .		1
52	Stress mitigation of 3D-stacking/packaging induced stresses. , 2018, , .		1
53	Edge Trimming Induced Defects on Direct Bonded Wafers. Journal of Electronic Packaging, Transactions of the ASME, 2018, 140, .	1.2	6
54	Novel Failure Analysis Techniques for 1.8 µm Pitch Wafer-to-Wafer Bonding. , 2018, , .		1

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55	3D Printed Liquid Jet Impingement Cooler: Demonstration, Opportunities and Challenges. , 2018, , .		10
56	The first observation of p-type electromigration failure in full ruthenium interconnects. , 2018, , .		9
57	Novel technology for microlenses for imaging applications. Applied Optics, 2018, 57, 9296.	0.9	4
58	(Invited) Raman Stress Measurements at the Nanoscale. ECS Meeting Abstracts, 2018, , .	0.0	0
59	(Invited) Determining Si Composition in SiGe Alloys with < 1% Si concentrations using Raman Spectroscopy. ECS Meeting Abstracts, 2018, , .	0.0	0
60	Detection of Local Cu-to-Cu Bonding Defects in Wafer-to-Wafer Hybrid Bonding Using GHz-SAM. , 2018, , .		0
61	Advanced Raman Spectroscopy Using Nanofocusing of Light. Advanced Engineering Materials, 2017, 19, 1600612.	1.6	10
62	Convolution-Based Fast Thermal Model for 3-D-ICs: Transient Experimental Validation. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2017, , 1-16.	1.4	1
63	Substrate Independent Elastic Modulus of Thin Low Dielectric Constant Materials. Advanced Engineering Materials, 2017, 19, 1600653.	1.6	12
64	Correlation between temperature dependence of Raman shifts and in-plane strains in an AlGaN/GaN stack. Journal of Applied Physics, 2017, 121, .	1.1	8
65	Direct correlation between low-frequency noise measurements and electromigration lifetimes. , 2017,		4
66	Study of the enhanced electromigration performance of Cu(Mn) by low-frequency noise measurements and atom probe tomography. Applied Physics Letters, 2017, 111, .	1.5	8
67	Study of GHz-SAM sensitivity to delamination in BEOL layers. Microelectronics Reliability, 2017, 76-77, 238-242.	0.9	1
68	LER and spacing variability on BEOL TDDB using E-field mapping: Impact of field acceleration. Microelectronics Reliability, 2017, 76-77, 131-135.	0.9	3
69	Impact of via density and passivation thickness on the mechanical integrity of advanced Back-End-Of-Line interconnects. Microelectronics Reliability, 2017, 79, 297-305.	0.9	1
70	Study of electromigration mechanisms in 22nm half-pitch Cu interconnects by 1/f noise measurements. , 2017, , .		3
71	Lock-in thermal laser stimulation for non-destructive failure localization in 3-D devices. Microelectronics Reliability, 2017, 76-77, 188-193.	0.9	3
72	Distributed electro-thermal model based on fast and scalable algorithm for GaN power devices and		3

circuit simulations. , 2017, , .

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73	Statistical Distribution of Through-Silicon via Cu Pumping. IEEE Transactions on Device and Materials Reliability, 2017, 17, 549-559.	1.5	13
74	Demonstration of low-frequency noise measurements for studying electromigration mechanisms in advanced nano-scaled interconnects. , 2017, , .		2
75	A novel in-situ resistance measurement to extract IMC resistivity and kinetic parameter for CoSn 3D stacks. , 2017, , .		2
76	High efficiency direct liquid jet impingement cooling of high power devices using a 3D-shaped polymer cooler. , 2017, , .		12
77	Use of 3D X-Ray Microscopy for BEOL and Advanced Packaging Failure Analysis. , 2017, , .		Ο
78	A photoâ€elastic microscopy study of the temperature dependency of stress induced by through silicon vias in silicon. Physica Status Solidi C: Current Topics in Solid State Physics, 2017, 14, 1700028.	0.8	0
79	Impact of ELD layers in mechanical properties of microbumps for 3D stacking. , 2016, , .		1
80	Liquid mediated direct bonding and bond propagation. , 2016, , .		1
81	A spectroscopic study of the chromatic properties of GafChromicâ,,¢EBT3 films. Medical Physics, 2016, 43, 1156-1166.	1.6	29
82	Extraction of elastic modulus of porous ultra-thin low-k films by two-dimensional finite-element simulations of nanoindentation. Journal of Applied Physics, 2016, 119, .	1.1	14
83	1/f Noise measurements for faster electromigration characterization. , 2016, , .		7
84	Nanofocusing of light into semiconducting fin photonic crystals. Applied Physics Letters, 2016, 108, .	1.5	9
85	1/f noise measurements for faster evaluation of electromigration in advanced microelectronics interconnections. Journal of Applied Physics, 2016, 119, .	1.1	29
86	Nondestructive Monitoring of Die Warpage in Encapsulated Chip Packages. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2016, 6, 653-662.	1.4	6
87	Fine Pitch Rapid Heat Self-Aligned Assembly and Liquid-Mediated Direct Bonding of Si Chips. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2016, 6, 946-953.	1.4	4
88	Copper Through Silicon Vias Studied by Photo-elastic Scanning Infrared Microscopy. Microelectronics Reliability, 2016, 64, 330-335.	0.9	1
89	Investigating stress measurement capabilities of GHz Scanning Acoustic Microscopy for 3D failure analysis. Microelectronics Reliability, 2016, 64, 336-340.	0.9	11
90	Surface Treatment to Enable Low Temperature and Pressure Copper Direct Bonding. , 2016, , .		3

Surface Treatment to Enable Low Temperature and Pressure Copper Direct Bonding. , 2016, , . 90

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91	Investigation of Advanced Dicing Technologies for Ultra Low-k and 3D Integration. , 2016, , .		9
92	New fast distributed thermal model for analysis of GaN based power devices. , 2016, , .		7
93	New breakdown mechanism investigation: Barrier metal penetration induced soft breakdown in low-k dielectrics. , 2016, , .		5
94	Impact of Via Density on the Mechanical Integrity of Advanced Back-End-of-Line During Packaging. , 2016, , .		9
95	Assessment of dielectric charging in capacitive MEMS switches fabricated on Si substrate with thin oxide film. Microelectronic Engineering, 2016, 159, 209-214.	1.1	5
96	Experimental Benchmarking of Electrical Methods and <inline-formula> <tex-math notation="LaTeX">\$mu \$ </tex-math </inline-formula> -Raman Spectroscopy for Channel Temperature Detection in AlGaN/GaN HEMTs. IEEE Transactions on Electron Devices, 2016, 63, 2321-2327.	1.6	16
97	Reliability Challenges Related to TSV Integration and 3-D Stacking. IEEE Design and Test, 2016, 33, 37-45.	1.1	29
98	Evaluation of via density and low-k Young's modulus influence on mechanical performance of advanced node multi-level Back-End-Of-Line. Microelectronics Reliability, 2016, 56, 93-100.	0.9	8
99	Fast Transient Convolution-Based Thermal Modeling Methodology for Including the Package Thermal Impact in 3D ICs. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2016, 6, 424-431.	1.4	10
100	B-Spline X-Ray Diffraction Imaging — Rapid non-destructive measurement of die warpage in ball grid array packages. Microelectronics Reliability, 2016, 59, 108-116.	0.9	6
101	50Gb/s C-band GeSi Waveguide Electro-Absorption Modulator. , 2016, , .		14
102	Relation between Raman frequency and triaxial stress in Si for surface and cross-sectional experiments in microelectronics components. Journal of Applied Physics, 2015, 118, .	1.1	40
103	Correlation between stress-induced leakage current and dielectric degradation in ultra-porous SiOCH low-k materials. Journal of Applied Physics, 2015, 118, .	1.1	12
104	Through silicon via to FinFET noise coupling in 3-D integrated circuits. , 2015, , .		2
105	Materials characterization and device analysis for evaluation of semiconductor processes by highlyâ€sophisticated photoelastic stress measurement technique. Physica Status Solidi C: Current Topics in Solid State Physics, 2015, 12, 1085-1089.	0.8	4
106	Effect of test structure on electromigration characteristics in three-dimensional through silicon via stacked devices. Japanese Journal of Applied Physics, 2015, 54, 05EE01.	0.8	16
107	Edge-enhanced Raman scattering in narrow sGe fin field-effect transistor channels. Applied Physics Letters, 2015, 106, .	1.5	12
108	3D-convolution based fast transient thermal model for 3D integrated circuits: methodology and		6

applications., 2015,,.

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109	Microstructure simulation of grain growth in Cu through silicon vias using phase-field modeling. Microelectronics Reliability, 2015, 55, 765-770.	0.9	13
110	Towards understanding intrinsic degradation and breakdown mechanisms in SiOCH low-k dielectrics. Journal of Applied Physics, 2015, 117, .	1.1	6
111	Effect of 4-point bending test procedure on crack propagation in thin film stacks. Microelectronic Engineering, 2015, 137, 59-63.	1.1	4
112	Advanced experimental back-end-of-line (BEOL) stability test: Measurements and simulations. Microelectronic Engineering, 2015, 137, 54-58.	1.1	7
113	Formation, processing and characterization of Co–Sn intermetallic compounds for potential integration in 3D interconnects. Microelectronic Engineering, 2015, 140, 72-80.	1.1	39
114	Three-dimensional micro-Raman spectroscopy mapping of stress induced in Si by Cu-filled through-Si vias. Applied Physics Letters, 2015, 106, 191901.	1.5	14
115	Influence of Field-Plate Configuration on Power Dissipation and Temperature Profiles in AlGaN/GaN on Silicon HEMTs. IEEE Transactions on Electron Devices, 2015, 62, 2416-2422.	1.6	26
116	Noise coupling between TSVs and active devices: Planar nMOSFETs vs. nFinFETs. , 2015, , .		6
117	Effects of packaging on mechanical stress in 3D-ICs. , 2015, , .		9
118	Constant voltage electromigration for advanced BEOL copper interconnects. , 2015, , .		1
119	Impact of oxide liner properties on TSV Cu pumping and TSV stress. , 2015, , .		13
120	A modeling and experimental method for accurate thermal analysis of AlGaN/GaN powerbars. , 2015, , .		4
121	SUâ€Eâ€Tâ€44: A Microâ€Raman Spectroscopy Study of the Doseâ€Dependence of EBT3 GafChromicTM Films fo Quantifying the Degree of Molecular Polymerization. Medical Physics, 2015, 42, 3340-3341.	or 1.6	0
122	3D stacking induced mechanical stress effects. , 2014, , .		26
123	<i>In-situ</i> scanning electron microscopy study of fracture events during back-end-of-line microbeam bending tests. Applied Physics Letters, 2014, 105, .	1.5	17
124	Gas leak rate study of MEMS thin film packages in different environments. , 2014, , .		1
125	Raman spectroscopy study of stress in 3D-stacked chips and correlation with FEM and electrical measurements. , 2014, , .		6
126	Room temperature and zero pressure high quality oxide direct bonding for 3D self-aligned assembly. , 2014, , .		2

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127	Hydrogen outgassing induced liner/barrier reliability degradation in through silicon via's. Applied Physics Letters, 2014, 104, 142906.	1.5	9
128	Correlation between Cu microstructure and TSV Cu pumping. , 2014, , .		39
129	Impact of Cu TSVs on BEOL metal and dielectric reliability. , 2014, , .		9
130	In-situ scanning electron microscope observation of electromigration-induced void growth in 30 nm ¼ pitch Cu interconnect structures. Journal of Applied Physics, 2014, 115, 074305.	1.1	9
131	Towards the understanding of intrinsic degradation and breakdown mechanisms of a SiOCH low-k dielectric. , 2014, , .		6
132	Development of B-spline X-ray Diffraction Imaging techniques for die warpage and stress monitoring inside fully encapsulated packaged chips. , 2014, , .		5
133	Degradation of Cu6Sn5 intermetallic compound by pore formation in solid–liquid interdiffusion Cu/Sn microbump interconnects. Microelectronic Engineering, 2014, 117, 26-34.	1.1	56
134	Chip-Package Interaction in 3D stacked IC packages using Finite Element Modelling. Microelectronics Reliability, 2014, 54, 1200-1205.	0.9	16
135	A Systematical Method to Determine the Internal Pressure and Hermeticity of MEMS Packages. Journal of Microelectromechanical Systems, 2014, 23, 862-870.	1.7	2
136	Defect detection in Through Silicon Vias by GHz Scanning Acoustic Microscopy: Key ultrasonic characteristics. , 2014, , .		7
137	Design aspects for CPI robust BEOL. , 2014, , .		3
138	Convolution based compact thermal model application to the evaluation of the thermal impact of die to die to die interface including interconnections. , 2014, , .		3
139	Reliability challenges for barrier/liner system in high aspect ratio through silicon vias. Microelectronics Reliability, 2014, 54, 1949-1952.	0.9	9
140	Sn whisker evaluations in 3D microbumped structures. Microelectronics Reliability, 2014, 54, 1982-1987.	0.9	6
141	As-grown donor-like traps in low-k dielectrics and their impact on intrinsic TDDB reliability. Microelectronics Reliability, 2014, 54, 1675-1679.	0.9	9
142	The underfill-microbump interaction mechanism in 3D ICs: Impact and mitigation of induced stresses. , 2014, , .		6
143	Mechanical stability of Cu/low-k BEOL interconnects. , 2014, , .		8
144	Microstructure simulation of grain growth in Cu Through Silicon Via using phase-field modeling. , 2014, , .		0

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145	Fast convolution based thermal model for 3D-ICs: Methodology, accuracy analysis and package impact. Microelectronics Journal, 2014, 45, 1746-1752.	1.1	13
146	Correlation between field dependent electrical conduction and dielectric breakdown in a SiCOH based low-k (k = 2.0) dielectric. Applied Physics Letters, 2013, 103, .	1.5	11
147	Copper through silicon via induced keep out zone for 10nm node bulk FinFET CMOS technology. , 2013, ,		19
148	A novel method to measure the internal pressure of MEMS thin-film packages. Microelectronics Reliability, 2013, 53, 1663-1666.	0.9	1
149	High frequency scanning acoustic microscopy applied to 3D integrated process: Void detection in Through Silicon Vias. , 2013, , .		14
150	Convolution based compact thermal model for 3D-ICs: Methodology and accuracy analysis. , 2013, , .		6
151	Impact of post-plating anneal and through-silicon via dimensions on Cu pumping. , 2013, , .		46
152	Metrology and inspection challenges for manufacturing 3D stacked IC's. , 2013, , .		4
153	IC-Package Interaction. , 2013, , .		Ο
154	Reliability of RF MEMS. , 2013, , 291-342.		2
155	Residual Stress in Silicon Caused by Cu-Sn Wafer-Level Packaging. , 2013, , .		3
156	Simulation of Cu pumping during TSV fabrication. , 2013, , .		8
157	Impact of barrier integrity on liner reliability in 3D through silicon vias. , 2013, , .		5
158	Printed circuit board technology inspired stretchable circuits. MRS Bulletin, 2012, 37, 254-260.	1.7	130
159	In-depth Raman spectroscopy analysis of various parameters affecting the mechanical stress near the surface and bulk of Cu-TSVs. , 2012, , .		30
160	Reliability test methodology for MEMS and MOEMS under electrical overstress and electrostatic discharge stress. Journal of Micro/ Nanolithography, MEMS, and MOEMS, 2012, 11, 021204-1.	1.0	0
161	SCB and SMI: two stretchable circuit technologies, based on standard printed circuit board processes. Circuit World, 2012, 38, 232-242.	0.7	22
162	Analysis of microbump induced stress effects in 3D stacked IC technologies. , 2012, , .		16

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163	Impact of through silicon vias on front-end-of-line performance after thermal cycling and thermal storage. , 2012, , .		17
164	FET arrays as CPI sensors for 3D stacking and packaging characterization. , 2012, , .		15
165	Reliability concerns in copper TSV's: Methods and results. , 2012, , .		8
166	3D chip package interaction thermo-mechanical challenges: Proximity effects of Through Silicon vias and μ-bumps. , 2012, , .		8
167	Comparison of x-ray diffraction, wafer curvature and Raman spectroscopy to evaluate the stress evolution in Copper TSV's. , 2012, , .		6
168	A study of blister formation in ALD Al <inf>2</inf> 0 <inf>3</inf> grown on silicon. , 2012, , .		11
169	Comparison of three methods to measure the internal pressure of empty MEMS packages. , 2012, , .		3
170	Impact of through silicon via induced mechanical stress on fully depleted Bulk FinFET technology. , 2012, , .		28
171	Design and fabrication of SiGe MEMS structures with high intrinsic ESD robustness. , 2012, , .		1
172	Investigation of temporary stiction in poly-SiGe micromirror arrays. Sensors and Actuators A: Physical, 2012, 188, 320-328.	2.0	3
173	Study of glass frit induced stiction using a micromirror array. Microelectronics Reliability, 2012, 52, 2256-2260.	0.9	1
174	Effect of the functionalization process on the performance of SiGe MEM resonators used for bio-molecular sensing. Microelectronics Reliability, 2012, 52, 2272-2277.	0.9	4
175	Diffusion growth of Cu3Sn phase in the bump and thin film Cu/Sn structures. Microelectronics Reliability, 2012, 52, 1971-1974.	0.9	13
176	MEMS packaging and reliability: An undividable couple. Microelectronics Reliability, 2012, 52, 2228-2234.	0.9	28
177	Chip package interaction (CPI): Thermo mechanical challenges in 3D technologies. , 2012, , .		18
178	Device and method for the measurement of anchor shear strength in MEMS devices. , 2012, , .		3
179	Poly-SiGe-Based MEMS Thin-Film Encapsulation. Journal of Microelectromechanical Systems, 2012, 21, 110-120.	1.7	16
180	Developing an Advanced Module for Back-Contact Solar Cells. IEEE Transactions on Components, Packaging and Manufacturing Technology, 2011, 1, 1319-1327.	1.4	17

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181	Above-IC generic poly-SiGe thin film wafer level packaging and MEM device technology: Application to accelerometers. , 2011, , .		15
182	Si Trench Around Drain (STAD) technology of GaN-DHFETs on Si substrate for boosting power performance. , 2011, , .		12
183	Design Issues and Considerations for Low-Cost 3-D TSV IC Technology. IEEE Journal of Solid-State Circuits, 2011, 46, 293-307.	3.5	236
184	Raman Spectroscopy Analysis Of Mechanical Stress Near Cu-TSVs. AIP Conference Proceedings, 2011, , .	0.3	16
185	Cu pumping in TSVs: Effect of pre-CMP thermal budget. Microelectronics Reliability, 2011, 51, 1856-1859.	0.9	122
186	Outgassing study of thin films used for poly-SiGe based vacuum packaging of MEMS. Microelectronics Reliability, 2011, 51, 1878-1881.	0.9	9
187	Thermal optimization of GaN-on-Si HEMTs with plastic package. Microelectronics Reliability, 2011, 51, 1788-1791.	0.9	8
188	Electrical characterization and reliability study of integrated GaN power amplifier in multi-layer thin-film technology. Microelectronics Reliability, 2011, 51, 1721-1724.	0.9	2
189	Influence of test structure design on stress-induced-voiding using an experimentally validated Finite Element Modeling approach. Microelectronics Reliability, 2011, 51, 1578-1581.	0.9	0
190	Polyimide-Enhanced Stretchable Interconnects: Design, Fabrication, and Characterization. IEEE Transactions on Electron Devices, 2011, 58, 2680-2688.	1.6	91
191	The effects of encapsulation on deformation behavior and failure mechanisms of stretchable interconnects. Thin Solid Films, 2011, 519, 2225-2234.	0.8	71
192	An investigation of stiction in poly-SiGe micromirror. , 2011, , .		1
193	Metrology and inspection for process control during bonding and thinning of stacked wafers for manufacturing 3D SIC's. , 2011, , .		10
194	Investigation of temporary stiction in poly-SiGe micromirror arrays. , 2011, , .		3
195	The Impact of Back-Side Cu Contamination on 3D Stacking Architecture. Electrochemical and Solid-State Letters, 2010, 13, H39.	2.2	5
196	Processing assessment and adhesion evaluation of copper through-silicon vias (TSVs) for three-dimensional stacked-integrated circuit (3D-SIC) architectures. Microelectronics Reliability, 2010, 50, 1636-1640.	0.9	18
197	Impact of design factors and environment on the ESD sensitivity of MEMS micromirrors. Microelectronics Reliability, 2010, 50, 1383-1387.	0.9	4
198	Performance of a new type of module based on back-contact solar cells. Proceedings of SPIE, 2010, , .	0.8	2

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199	Investigating ESD sensitivity in electrostatic SiGe MEMS. Journal of Micromechanics and Microengineering, 2010, 20, 055005.	1.5	8
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