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List of Publications by Year in descending order

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Version: 2024-02-01

11
papers

176
citations

1937685

4
h-index

2272923

4
g-index

11
all docs

11
docs citations

11
times ranked

86
citing authors

#	ARTICLE	IF	CITATIONS
1	Deceptive Logic Locking for Hardware Integrity Protection Against Machine Learning Attacks. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 1716-1729.	2.7	17
2	Vertical IP Protection of the Next-Generation Devices: Quo Vadis?. , 2021, , .		4
3	ANDROMEDA: An FPGA Based RISC-V MPSoC Exploration Framework. , 2021, , .		4
4	Challenging the Security of Logic Locking Schemes in the Era of Deep Learning: A Neuroevolutionary Approach. ACM Journal on Emerging Technologies in Computing Systems, 2021, 17, 1-26.	2.3	37
5	Logic Locking at the Frontiers of Machine Learning: A Survey on Developments and Opportunities. , 2021, , .		18
6	QFlow: Quantitative Information Flow for Security-Aware Hardware Design in Verilog. , 2021, , .		11
7	Trustworthy Hardware Design with Logic Locking. , 2021, , .		0
8	A secure hardware-software solution based on RISC-V, logic locking and microkernel. , 2020, , .		12
9	Control-Lock. , 2019, , .		9
10	Evolving priority rules for resource constrained project scheduling problem with genetic programming. Future Generation Computer Systems, 2018, 86, 211-221.	7.5	51
11	Immunological algorithms paradigm for construction of Boolean functions with good cryptographic properties. Engineering Applications of Artificial Intelligence, 2017, 62, 320-330.	8.1	13