

Kokila Jagadeesh

List of Publications by Year in descending order

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28
citing authors

#	ARTICLE	IF	CITATIONS
1	Side-channel Analysis using Deep Learning on Hardware Trojans. IOP Conference Series: Materials Science and Engineering, 2021, 1049, 012018.	0.6	1
2	Resource Efficient Metering Scheme for Protecting SoC FPGA Device and IPs in IoT Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 2284-2295.	3.1	9
3	Enhanced Authentication Using Hybrid PUF with FSM for Protecting IPs of SoC FPGAs. Journal of Electronic Testing: Theory and Applications (JETTA), 2019, 35, 543-558.	1.2	6
4	Lightweight signature scheme to protect intellectual properties of Internet of things applications in system on chip field-programmable gate arrays. Turkish Journal of Electrical Engineering and Computer Sciences, 2019, 27, 3500-3515.	1.4	1
5	Adaptive Dynamic Partial Reconfigurable Security System. Communications in Computer and Information Science, 2018, , 430-439.	0.5	1
6	A Survey of Hardware and Software Co-design Issues for System on Chip Design. Advances in Intelligent Systems and Computing, 2016, , 41-49.	0.6	5
7	Enhanced and secured random number generation for eUASBP. International Journal of Systems Assurance Engineering and Management, 0, , 1.	2.4	0