Ckristian Duran

List of Publications by Year
in descending order

11 papers	81 citations	1684188 5 h-index	1720034 7 g-index
11 all docs	11 docs citations	11 times ranked	54 citing authors

#	ARTICLE	IF	CITATIONS
1	A 10pJ/bit 256b AES-SoC Exploiting Memory Access Acceleration. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 1612-1616.	3.0	3
2	Systems on a Chip With 8 and 32 Bits Processors in 0.18-μm Technology for IoT Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 2438-2442.	3.0	1
3	ChaCha20–Poly1305 Authenticated Encryption with Additional Data for Transport Layer Security 1.3. Cryptography, 2022, 6, 30.	2.3	12
4	A Fully Digital True Random Number Generator With Entropy Source Based in Frequency Collapse. IEEE Access, 2021, 9, 105748-105755.	4.2	14
5	A Sub-μ W Reversed-Body-Bias 8-bit Processor on 65-nm Silicon-on-Thin-Box (SOTB) for IoT Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3182-3186.	3.0	5
6	An Energy-Efficient RISC-V RV32IMAC Microcontroller for Periodical-Driven Sensing Applications. , 2020, , .		8
7	Quick Boot of Trusted Execution Environment With Hardware Accelerators. IEEE Access, 2020, 8, 74015-74023.	4.2	10
8	Defeating Silicon Reverse Engineering Using a Layout-Level Standard Cell Camouflage. IEEE Transactions on Consumer Electronics, 2019, 65, 109-118.	3.6	8
9	A Low-Area Direct Memory Access Controller Architecture for a RISC-V Based Low-Power Microcontroller. , 2019, , .		7
10	Standard cell camouflage method to counter silicon reverse engineering. , 2018, , .		4
11	A system-on-chip platform for the internet of things featuring a 32-bit RISC-V based microcontroller. , 2017, , .		9