Jiwon Lee

List of Publications by Year
in descending order

12 papers	**162** citations	1307594 **7** h-index	1372567 **10** g-index
12 all docs	**12** docs citations	**12** times ranked	**69** citing authors

#	Article	IF	Citations
1	Infrared Colloidal Quantum Dot Image Sensors. IEEE Transactions on Electron Devices, 2022, 69, 2840-2850.	3.0	43
2	Thin-Film Photodetector Optimization for High-Performance Short-Wavelength Infrared Imaging. IEEE Electron Device Letters, 2021, 42, 1196-1199.	3.9	34
3	A Near-Infrared Enhanced Silicon Single-Photon Avalanche Diode With a Spherically Uniform Electric Field Peak. IEEE Electron Device Letters, 2021, 42, 879-882.	3.9	21
4	On-Chip FPN Calibration for a Linear-Logarithmic APS Using Two-Step Charge Transfer. IEEE Transactions on Electron Devices, 2013, 60, 1989-1994.	3.0	18
5	Detailed Characterization of Short-Wave Infrared Colloidal Quantum Dot Image Sensors. IEEE Transactions on Electron Devices, 2022, 69, 2900-2906.	3.0	17
6	A Backside-Illuminated Charge-Focusing Silicon SPAD With Enhanced Near-Infrared Sensitivity. IEEE Transactions on Electron Devices, 2022, 69, 1129-1136.	3.0	11
7	Memoryless Wide-Dynamic-Range CMOS Image Sensor Using Nonfully Depleted PPD-Storage Dual Capture. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 26-30.	3.0	7
8	Offset and Gain FPN Calibrated Linear-Logarithmic Image Sensor With Shared Pixel Architecture. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3518-3521.	3.0	4
9	All-Silicon Photodetectors for Photonic Integrated Circuit Calibration. IEEE Photonics Technology Letters, 2021, 33, 836-839.	2.5	3
10	New FPN correction method for PD-storage dual-capture CMOS image sensor using a nonfully depleted pinned photodiode. , 2012, , .		2
11	A 0.78 mW Low-Power 4.02 High-Compression Ratio Less than 10^{-6} BER Error-Tolerant Lossless Image Compression Hardware for Wireless Capsule Endoscopy System. , 2018, , .		1
12	Characterization and Analysis of Deep Learning for 3D Point Cloud Analytics. IEEE Computer Architecture Letters, 2021, 20, 106-109.	1.5	1