Yulhwa Kim

List of Publications by Year
in descending order

8 papers	108 citations	2258059 3 h-index	2550090 3 g-index
8 all docs	8 docs citations	8 times ranked	112 citing authors

#	Article	IF	Citations
1	Area-Efficient and Variation-Tolerant In-Memory BNN Computing using 6T SRAM Array. , 2019, , .		57
2	Input-Splitting of Large Neural Networks for Power-Efficient Accelerator with Resistive Crossbar Memory Array. , 2018, , .		17
3	In-memory batch-normalization for resistive memory based binary neural network hardware. , 2019, , .		13
4	BitBlade: Energy-Efficient Variable Bit-Precision Hardware Accelerator for Quantized Neural Networks. IEEE Journal of Solid-State Circuits, 2022, 57, 1924-1935.	5.4	11
5	Maximizing Parallel Activation of Word-Lines in MRAM-Based Binary Neural Network Accelerators. IEEE Access, 2021, 9, 141961-141969.	4.2	4
6	Energy-Efficient In-Memory Binary Neural Network Accelerator Design Based on 8T2C SRAM Cell. IEEE Solid-State Circuits Letters, 2022, 5, 70-73.	2.0	3
7	Energy-efficient charge sharing-based 8T2C SRAM in-memory accelerator for binary neural networks in 28nm CMOS. , 2021, , .		2
8	Compact Convolution Mapping on Neuromorphic Hardware using Axonal Delay. , 2018, , .		1