Ang Li

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/8969130/publications.pdf

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16	277	6	7
papers	citations	h-index	g-index
16	16	16	184
all docs	docs citations	times ranked	citing authors

#	Article	lF	Citations
1	Accelerated Derivative-Free Deep Reinforcement Learning for Large-Scale Grid Emergency Voltage Control. IEEE Transactions on Power Systems, 2022, 37, 14-25.	6.5	26
2	DRIPS: Dynamic Rebalancing of Pipelined Streaming Applications on CGRAs. , 2022, , .		2
3	ASAP., 2022,,.		3
4	O3BNN-R: An Out-of-Order Architecture for High-Performance and Regularized BNN Inference. IEEE Transactions on Parallel and Distributed Systems, 2021, 32, 199-213.	5.6	25
5	OpenCGRA: Democratizing Coarse-Grained Reconfigurable Arrays. , 2021, , .		12
6	ARENA: Asynchronous Reconfigurable Accelerator Ring to Enable Data-Centric Parallel Computing. IEEE Transactions on Parallel and Distributed Systems, 2021, 32, 2880-2892.	5.6	11
7	BCNN: Binary complex neural network. Microprocessors and Microsystems, 2021, 87, 104359.	2.8	5
8	A Survey: Handling Irregularities in Neural Network Acceleration with FPGAs., 2021,,.		7
9	DynPaC: Coarse-Grained, Dynamic, and Partially Reconfigurable Array for Streaming Applications. , 2021, , .		3
10	Guarding Numerics Amidst Rising Heterogeneity., 2021,,.		3
11	Evaluating Modern GPU Interconnect: PCIe, NVLink, NV-SLI, NVSwitch and GPUDirect. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 94-110.	5.6	104
12	Accelerating Binarized Neural Networks via Bit-Tensor-Cores in Turing GPUs. IEEE Transactions on Parallel and Distributed Systems, 2020, , 1-1.	5.6	12
13	A Sparse Tensor Benchmark Suite for CPUs and GPUs. , 2020, , .		3
14	PASTA: a parallel sparse tensor algorithm benchmark suite. CCF Transactions on High Performance Computing, 2019, 1, 111-130.	1.7	10
15	O3BNN., 2019, , .		14
16	Tartan: Evaluating Modern GPU Interconnect via a Multi-GPU Benchmark Suite., 2018,,.		37