Ahmed M Soliman

List of Publications by Year in Descending Order

131 papers	2,022 citations	24 h-index	40 g-index
142 ext. papers	2,353 ext. citations	1.6 avg, IF	5.47 L-index

#	Paper	IF	Citations
131	A 0.002-mm2 8-bit 1-MS/s low-power time-based DAC (T-DAC). *IET Circuits, Devices and Systems*, **2021**, 15, 738	1.1	1
130	A switched chaotic encryption scheme using multi-mode generalized modified transition map. *Multimedia Tools and Applications*, **2021**, 80, 5373-5402	2.5	2
129	On-the-Fly Parallel Processing IP-Core for Image Blur Detection, Compression, and Chaotic Encryption Based on FPGA. *IEEE Access*, **2021**, 9, 82726-82746	3.5	2
128	Increasing LPG production by adding volatile hydrocarbons to reduce import gap in Egypt. *Journal of Petroleum Exploration and Production*, **2020**, 10, 3733-3750	2.2	0
127	Stability analysis of fractional-order Colpitts oscillators. *Analog Integrated Circuits and Signal Processing*, **2019**, 101, 267-279	1.2	7
126	Design of odd nth-order elliptic high-pass filters employing OTRAs. *IET Circuits, Devices and Systems* , **2019**, 13, 174-184	1.1	0
125	Multifunction Fractional Inverse Filter Based on OTRA **2019**,		3
124	A Universal Fractional-Order Memelement Emulation Circuit **2019**,		2
123	Fractional-order Nonminimum-phase Filter Design **2019**,		1
122	A Simple BJT Inverse Memristor Emulator and Its Application in Chaotic Oscillators **2019**,		3
121	Analytical synthesis of elliptic voltage-mode even/odd-nth-order filter structures using DDCCs, FDCCIIs, and grounded capacitors and resistors. *IET Circuits, Devices and Systems*, **2019**, 13, 279-291	1.1	6
120	Two topologies of fractional-order oscillators based on CFOA and RC networks **2018**,		1
119	A new design methodology for voltage-to-frequency converters (VFCs) circuits suitable for time-based analog-to-digital converters (T-ADCs). *Analog Integrated Circuits and Signal Processing*, **2018**, 94, 277-287	1.2	1
118	Technology Scaling Roadmap for FinFET-Based FPGA Clusters Under Process Variations. *Journal of Circuits, Systems and Computers*, **2018**, 27, 1850056	0.9	4
117	Hardware Speech Encryption Using a Chaotic Generator, Dynamic Shift and Bit Permutation **2018**,		5
116	A Low-Power Time-Domain Comparator for IoT Applications **2018**,		1
115	New Op-Amp Circuits Realizations Using Genetic Algorithm. *Journal of Circuits, Systems and Computers*, **2017**, 26, 1750131	0.9	1

114	Image encryption based on double-humped and delayed logistic maps for biomedical applications **2017**,		3
113	Generalized family of fractional-order oscillators based on single CFOA and RC network **2017**,		11
112	Biomedical image encryption based on double-humped and fractional logistic maps **2017**,		13
111	Fractional order four-phase oscillator based on double integrator topology **2017**,		4
110	Three Fractional-Order-Capacitors-Based Oscillators with Controllable Phase and Frequency. *Journal of Circuits, Systems and Computers*, **2017**, 26, 1750160	0.9	52
109	A comparative study of the Voltage-to-Time converters (VTCs) and the Voltage-to-Frequency converters (VFCs) circuits **2016**,		2
108	Fractional-order mutual inductance: analysis and design. *International Journal of Circuit Theory and Applications*, **2016**, 44, 85-97	2	42
107	On The Optimization of Fractional Order Low-Pass Filters. *Circuits, Systems, and Signal Processing*, **2016**, 35, 2017-2039	2.2	73
106	Fractional Order Oscillator Design Based on Two-Port Network. *Circuits, Systems, and Signal Processing*, **2016**, 35, 3086-3112	2.2	38
105	Fractional-order inverting and non-inverting filters based on CFOA **2016**,		11
104	Fractional-order oscillator based on single CCII **2016**,		8
103	Fractional-order multi-phase oscillators design and analysis suitable for higher-order PSK applications. *Analog Integrated Circuits and Signal Processing*, **2016**, 87, 301-312	1.2	27
102	A new highly-linear highly-sensitive differential voltage-to-time converter circuit in CMOS 65nm technology **2015**,		1
101	Analytical synthesis of voltage-mode even/odd-nth-order differential difference current conveyor and fully differential current conveyor II-grounded resistor and capacitor universal filter structures. *International Journal of Circuit Theory and Applications*, **2015**, 43, 1263-1310	2	13
100	Generalized delayed logistic map suitable for pseudo-random number generation **2015**,		3
99	The impact of FinFET technology scaling on critical path performance under process variations **2015**,		2
98	Fractional order oscillators with single non-zero transmission matrix element **2015**,		4
97	Generalized fractional logistic map suitable for data encryption **2015**,		10

96	Fractional Order Sallen-Key and KHN Filters: Stability and Poles Allocation. *Circuits, Systems, and Signal Processing*, **2015**, 34, 1461-1480	2.2	72
95	Multi-phase oscillator for higher-order PSK applications **2014**,		4
94	Current feedback operational amplifier (CFOA) based fractional order oscillators **2014**,		3
93	Fractional order oscillator with independent control of phase and frequency **2014**,		8
92	CCII based fractional filters of different orders. *Journal of Advanced Research*, **2014**, 5, 157-64	13	54
91	Two integrator loop quadrature oscillators: A review. *Journal of Advanced Research*, **2013**, 4, 1-11	13	23
90	Fractional Order Butterworth Filter: Active and Passive Realizations. *IEEE Journal on Emerging and Selected Topics in Circuits and Systems*, **2013**, 3, 346-354	5.2	138
89	CCII based KHN fractional order filter **2013**,		4
88	GENERATION OF THIRD-ORDER QUADRATURE OSCILLATOR CIRCUITS USING NAM EXPANSION. *Journal of Circuits, Systems and Computers*, **2013**, 22, 1350060	0.9	18
87	A Note on the Generation of Generalized Impedance Converter Circuits Using NAM Expansion. *Circuits, Systems, and Signal Processing*, **2012**, 31, 1147-1157	2.2	4
86	CLASSIFICATION AND PATHOLOGICAL REALIZATIONS OF TRANSCONDUCTANCE AMPLIFIERS. *Journal of Circuits, Systems and Computers*, **2012**, 21, 1250010	0.9	12
85	Generation of Generalized Impedance Converter Circuits Using NAM Expansion. *Circuits, Systems, and Signal Processing*, **2011**, 30, 1091-1114	2.2	13
84	Generation of Kerwin-Huelsman-Newcomb biquad filter circuits using nodal admittance matrix expansion. *International Journal of Circuit Theory and Applications*, **2011**, 39, 697-717	2	14
83	GENERATION OF CFOA, CCII AND DVCC BASED OSCILLATORS FROM PASSIVE RLC FILTER. *Journal of Circuits, Systems and Computers*, **2011**, 20, 621-639	0.9	2
82	Synthesis of Oscillators Using Limit Variables and NAM Expansion. *Active and Passive Electronic Components*, **2011**, 2011, 1-13	0.3	3
81	Transformation of a floating capacitor oscillator to a family of grounded capacitor oscillators. *International Journal of Electronics*, **2011**, 98, 289-300	1.2	9
80	HISTORY AND PROGRESS OF THE TOW THOMAS BI-QUADRATIC FILTER PART III: GENERATION USING NAM EXPANSION. *Journal of Circuits, Systems and Computers*, **2010**, 19, 529-548	0.9	8
79	Two Integrator Loop Filters: Generation Using NAM Expansion and Review. *Journal of Electrical and Computer Engineering*, **2010**, 2010, 1-8	1.9	10

78	Two New Families of Floating FDNR Circuits. *Journal of Electrical and Computer Engineering*, **2010**, 2010, 1-7	1.9	12
77	Generation of second generation current conveyor (CCII) family from inverting second generation current conveyor (ICCII) family. *International Journal of Electronics*, **2010**, 97, 405-414	1.2	6
76	CMOS digitally programmable lossless floating inductor **2010**,		3
75	High speed fully differential second generation current conveyor **2010**,		1
74	New CCII and ICCII Based Realizations of L-C and L-R Mutators. *Circuits, Systems, and Signal Processing*, **2010**, 29, 1181-1191	2.2	13
73	Generation of current conveyor based oscillators using nodal admittance matrix expansion. *Analog Integrated Circuits and Signal Processing*, **2010**, 65, 43-59	1.2	32
72	Transformation of oscillators using Op Amps, unity gain cells and CFOA. *Analog Integrated Circuits and Signal Processing*, **2010**, 65, 105-114	1.2	26
71	On the systematic synthesis of CCII-based floating simulators. *International Journal of Circuit Theory and Applications*, **2010**, 38, 935-967	2	47
70	Generation and classification of CCII and ICCII based negative impedance converter circuits using NAM expansion. *International Journal of Circuit Theory and Applications*, **2010**, 39, n/a-n/a	2	2
69	ON THE INTRODUCTION OF NEW FLOATING CURRENT CONVEYORS. *Journal of Circuits, Systems and Computers*, **2009**, 18, 1005-1016	0.9	8
68	ON THE DVCC AND THE BOCCII AS ADJOINT ELEMENTS. *Journal of Circuits, Systems and Computers*, **2009**, 18, 1017-1032	0.9	11
67	ACTIVE CIRCULATOR CIRCUITS USING OA, CCII, CFOA AND DVCC. *Journal of Circuits, Systems and Computers*, **2009**, 18, 629-645	0.9	4
66	GENERATION OF OSCILLATORS BASED ON GROUNDED CAPACITOR CURRENT CONVEYORS WITH MINIMUM PASSIVE COMPONENTS. *Journal of Circuits, Systems and Computers*, **2009**, 18, 857-873	0.9	10
65	BODE-TYPE EQUALIZERS USING CURRENT CONVEYORS. *Journal of Circuits, Systems and Computers*, **2009**, 18, 433-442	0.9	1
64	ADJOINT NETWORK THEOREM AND FLOATING ELEMENTS IN THE NAM. *Journal of Circuits, Systems and Computers*, **2009**, 18, 597-616	0.9	22
63	The voltage mirror-current mirror pair as a universal element. *International Journal of Circuit Theory and Applications*, **2009**, 38, n/a-n/a	2	10
62	Novel CMOS Realization of Balanced-Output Third Generation Inverting Current Conveyor with Applications. *Circuits, Systems, and Signal Processing*, **2009**, 28, 1037-1051	2.2	7
61	Novel low-power accurate wide-band CMOS negative-second-generation-current-conveyor realizations based on Floating-Current-Source building blocks **2009**,		4

60	Use of Mirror Elements in the Active Device Synthesis by Admittance Matrix Expansion. *IEEE Transactions on Circuits and Systems I: Regular Papers*, **2008**, 55, 2726-2735	3.9	66
59	HISTORY AND PROGRESS OF THE TOW–THOMAS BI-QUADRATIC FILTER PART I: GENERATION AND OP AMP REALIZATIONS. *Journal of Circuits, Systems and Computers*, **2008**, 17, 33-54	0.9	17
58	HISTORY AND PROGRESS OF THE KERWIN–HUELSMANN–NEWCOMB FILTER GENERATION AND OP AMP REALIZATIONS. *Journal of Circuits, Systems and Computers*, **2008**, 17, 637-658	0.9	7
57	HISTORY AND PROGRESS OF THE TOW–THOMAS BIQUADRATIC FILTER PART II: OTRA, CCII, AND DVCC REALIZATIONS. *Journal of Circuits, Systems and Computers*, **2008**, 17, 797-826	0.9	13
56	New CMOS operational transresistance amplifier **2008**,		3
55	Current-Mode Universal Filters Using Current Conveyors: Classification and Review. *Circuits, Systems, and Signal Processing*, **2008**, 27, 405-427	2.2	18
54	Generation, modeling, and analysis of CCII-based gyrators using the generalized symbolic framework for linear active circuits. *International Journal of Circuit Theory and Applications*, **2008**, 36, 289-309	2	49
53	The CCII+ and the ICCII as basic building blocks in low-pass filter realizations. *International Journal of Circuit Theory and Applications*, **2008**, 36, 493-509	2	12
52	Current mode filters using two output inverting CCII. *International Journal of Circuit Theory and Applications*, **2008**, 36, 875-881	2	25
51	A new approach for using the pathological mirror elements in the ideal representation of active devices. *International Journal of Circuit Theory and Applications*, **2008**, 38, n/a-n/a	2	12
50	Realizations of fully differential voltage second generation current conveyor with an application. *International Journal of Circuit Theory and Applications*, **2008**, 38, n/a-n/a	2	3
49	High-Order Gm-C Filters with Current Transfer Function Based on Multiple Loop Feedback **2007**,		5
48	Low Voltage CMOS Fully Differential Current Feedback Amplifier with Controllable 3-dB Bandwidth **2007**,		2
47	Analytical Synthesis of Low-Sensitivity High-Order Voltage-Mode DDCC and FDCCII-Grounded R and C All-Pass Filter Structures. *IEEE Transactions on Circuits and Systems Part 1: Regular Papers*, **2007**, 54, 1430-1443		40
46	Low Voltage CMOS Fully Differential Current Feedback Amplifier with Controllable 3-dB Bandwidth **2007**,		1
45	New Current-Mode Bandpass Filters Using Three Single-Output ICCIIs. *Active and Passive Electronic Components*, **2007**, 2007, 1-5	0.3	2
44	Voltage mode and current mode Tow Thomas bi-quadratic filters using inverting CCII. *International Journal of Circuit Theory and Applications*, **2007**, 35, 463-467	2	37
43	Comment on ▯he effects of non-idealities and current limitations on the simulated inductances employing current conveyors▯ *Analog Integrated Circuits and Signal Processing*, **2007**, 50, 283-283	1.2	1

42	Novel CMOS realizations of the inverting second-generation current conveyor and applications. *Analog Integrated Circuits and Signal Processing*, **2007**, 52, 57-64	1.2	16
41	Low voltage CMOS fully differential current feedback amplifier with controllable 3-dB bandwidth. *Analog Integrated Circuits and Signal Processing*, **2007**, 52, 139-146	1.2	8
40	A Transformation Method from Voltage-Mode OP-Amp-RC Circuits to Current-Mode Gm-C Circuits. *Circuits, Systems, and Signal Processing*, **2006**, 25, 609-626	2.2	7
39	New Square-Root Domain Oscillators. *Analog Integrated Circuits and Signal Processing*, **2006**, 47, 165-168	1.2	8
38	New op-amp-RC to G m -C transformation method. *Analog Integrated Circuits and Signal Processing*, **2006**, 49, 79-86	1.2	3
37	New 1.5-V CMOS second generation current conveyor based on wide range transconductor. *Analog Integrated Circuits and Signal Processing*, **2006**, 49, 267-279	1.2	32
36	A Novel CMOS Realization of the Differential Input Balanced Output Current Operational Amplifier and its Applications. *Analog Integrated Circuits and Signal Processing*, **2005**, 44, 37-53	1.2	14
35	New Four-Quadrant CMOS Current-Mode and Voltage-Mode Multipliers. *Analog Integrated Circuits and Signal Processing*, **2005**, 45, 295-307	1.2	19
34	A Tunable Square Root Domain Oscillator. *Analog Integrated Circuits and Signal Processing*, **2005**, 43, 91-95	1.2	6
33	NOVEL CMOS REALIZATIONS OF THE OPERATIONAL FLOATING CONVEYOR AND APPLICATIONS. *Journal of Circuits, Systems and Computers*, **2005**, 14, 1113-1143	0.9	8
32	A New CMOS Rail-to-Rail Low Distortion Balanced Output Transconductor. *Analog Integrated Circuits and Signal Processing*, **2004**, 40, 75-82	1.2	5
31	New Wide Band Low Power CMOS Current Conveyors. *Analog Integrated Circuits and Signal Processing*, **2004**, 40, 91-97	1.2	14
30	A Modified CMOS Balanced Output Transconductor with Extended Linearity. *Analog Integrated Circuits and Signal Processing*, **2003**, 36, 239-244	1.2	4
29	A Novel Exponential Voltage-to-Current Converter. *Circuits, Systems, and Signal Processing*, **2002**, 21, 473-483	2.2	1
28	A New Approach to Realize Variable Gain Amplifiers. *Analog Integrated Circuits and Signal Processing*, **2002**, 30, 257-263	1.2	4
27	Parasitic-capacitance-insensitive voltage-mode mosfet-C filters using differential current voltage conveyor. *Circuits, Systems, and Signal Processing*, **2001**, 20, 11-26	2.2	10
26	A Low-Voltage Low-Power Rail-to-Rail Constant g m Transconductance Amplifier. *Analog Integrated Circuits and Signal Processing*, **2000**, 24, 129-139	1.2	
25	Current Operational Amplifier (COA): CMOS Realization and Active Compensation. *Analog Integrated Circuits and Signal Processing*, **2000**, 24, 141-152	1.2	6

24	Current Feedback Operational Amplifier Based Oscillators. *Analog Integrated Circuits and Signal Processing*, **2000**, 23, 45-55	1.2	52
23	Low Voltage Rail to Rail CMOS Current Feedback Operational Amplifier and its Applications for Analog VLSI. *Analog Integrated Circuits and Signal Processing*, **2000**, 25, 47-57	1.2	23
22	A New CMOS Programmable Balanced Output Transconductor and Application to a Mixed Mode Universal Filter Suitable for VLSI. *Analog Integrated Circuits and Signal Processing*, **1999**, 19, 241-254	1.2	11
21	CMOS Programmable Imager Implementing Pre-Processing Operations. *Analog Integrated Circuits and Signal Processing*, **1999**, 19, 279-293	1.2	
20	Inverting second generation current conveyors: the missing building blocks, CMOS realizations and applications. *International Journal of Electronics*, **1999**, 86, 413-432	1.2	168
19	Current mode CCII oscillators using grounded capacitors and resistors. *International Journal of Circuit Theory and Applications*, **1998**, 26, 431-438	2	35
18	Generation of CCII and CFOA filters from passive RLC filters. *International Journal of Electronics*, **1998**, 85, 293-312	1.2	24
17	NEW GROUNDED-CAPACITOR CURRENT-MODE OSCILLATORS USING SINGLE-OUTPUT CCIIS. *Journal of Circuits, Systems and Computers*, **1998**, 08, 363-378	0.9	6
16	Port Interchange in Voltage Mode Current Conveyor Based Filters. *Journal of Circuits, Systems and Computers*, **1997**, 07, 543-561	0.9	2
15	CMOS differential current conveyors and applications for analog VLSI. *Analog Integrated Circuits and Signal Processing*, **1996**, 11, 35	1.2	28
14	Applications of the current feedback operational amplifiers. *Analog Integrated Circuits and Signal Processing*, **1996**, 11, 265	1.2	113
13	Theorem relating a class of op.-amp. and current conveyor circuits. *International Journal of Electronics*, **1995**, 79, 53-61	1.2	16
12	Letters to the editor a new phase and magnitude compensated weighted summer using three operational amplifiers. *International Journal of Circuit Theory and Applications*, **1983**, 11, 339-342	2	
11	Design of high-frequency amplifiers **1983**, 5, 9-11		4
10	A universal notch filter. *International Journal of Circuit Theory and Applications*, **1979**, 7, 139-142	2	
9	A new active c differential input integrator using the dvccs/dvcvs. *International Journal of Circuit Theory and Applications*, **1979**, 7, 272-275	2	5
8	A universal active R biquad. *International Journal of Circuit Theory and Applications*, **1978**, 6, 153-157	2	7
7	A new single operational amplifier active RC bandpass network with reduced sensitivity to amplifier gain-bandwidth product. *International Journal of Circuit Theory and Applications*, **1978**, 6, 321-326	2	2

List of Publications

6	Realizations of ideal FDNC and FDNR elements using new types of mutators. *International Journal of Electronics*, **1978**, 44, 317-323	1.2	17
5	Two novel active RC canonic bandpass networks using the current conveyor. *International Journal of Electronics*, **1977**, 42, 49-54	1.2	14
4	Generalized immittance inverters and their realizations. *International Journal of Electronics*, **1976**, 41, 59-64	1.2	11
3	Conversion of a band-pass resonator to an all-pass or a notch filter. *International Journal of Electronics*, **1975**, 38, 559-562	1.2	
2	Simple sinusoidal active RC oscillators. *International Journal of Electronics*, **1975**, 39, 455-458	1.2	28
1	A Novel Refreshment Circuit for 2T1M Neuromorphic Synapse. *Journal of Circuits, Systems and Computers*,2250047	0.9	1