Ahmed M Soliman

List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/8957865/publications.pdf

Version: 2024-02-01

141 papers 2,554 citations

218381 26 h-index 243296 44 g-index

142 all docs

 $\begin{array}{c} 142 \\ \\ \text{docs citations} \end{array}$

times ranked

142

610 citing authors

#	Article	IF	CITATIONS
1	Inverting second generation current conveyors: the missing building blocks, CMOS realizations and applications. International Journal of Electronics, 1999, 86, 413-432.	0.9	191
2	Fractional Order Butterworth Filter: Active and Passive Realizations. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2013, 3, 346-354.	2.7	179
3	Applications of the current feedback operational amplifiers. Analog Integrated Circuits and Signal Processing, 1996, 11, 265.	0.9	155
4	Fractional Order Sallen–Key and KHN Filters: Stability and Poles Allocation. Circuits, Systems, and Signal Processing, 2015, 34, 1461-1480.	1.2	86
5	On The Optimization of Fractional Order Low-Pass Filters. Circuits, Systems, and Signal Processing, 2016, 35, 2017-2039.	1.2	86
6	Use of Mirror Elements in the Active Device Synthesis by Admittance Matrix Expansion. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 2726-2735.	3.5	71
7	CCII based fractional filters of different orders. Journal of Advanced Research, 2014, 5, 157-164.	4.4	65
8	Three Fractional-Order-Capacitors-Based Oscillators with Controllable Phase and Frequency. Journal of Circuits, Systems and Computers, 2017, 26, 1750160.	1.0	65
9	Current Feedback Operational Amplifier Based Oscillators. Analog Integrated Circuits and Signal Processing, 2000, 23, 45-55.	0.9	63
10	Analytical Synthesis of Low-Sensitivity High-Order Voltage-Mode DDCC and FDCCII-Grounded & lt;emphasis>R and <emphasis> C</emphasis> All-Pass Filter Structures. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 1430-1443.	0.1	63
11	On the systematic synthesis of CCIIâ€based floating simulators. International Journal of Circuit Theory and Applications, 2010, 38, 935-967.	1.3	53
12	Fractionalâ€order mutual inductance: analysis and design. International Journal of Circuit Theory and Applications, 2016, 44, 85-97.	1.3	53
13	Generation, modeling, and analysis of CCIIâ€based gyrators using the generalized symbolic framework for linear active circuits. International Journal of Circuit Theory and Applications, 2008, 36, 289-309.	1.3	52
14	Voltage mode and current mode Tow Thomas bi-quadratic filters using inverting CCII. International Journal of Circuit Theory and Applications, 2007, 35, 463-467.	1.3	48
15	CMOS differential current conveyors and applications for analog VLSI. Analog Integrated Circuits and Signal Processing, 1996, 11, 35.	0.9	45
16	Fractional Order Oscillator Design Based on Two-Port Network. Circuits, Systems, and Signal Processing, 2016, 35, 3086-3112.	1.2	44
17	Current mode CCII oscillators using grounded capacitors and resistors. International Journal of Circuit Theory and Applications, 1998, 26, 431-438.	1.3	41
18	New 1.5-V CMOS second generation current conveyor based on wide range transconductor. Analog Integrated Circuits and Signal Processing, 2006, 49, 267-279.	0.9	39

#	Article	IF	CITATIONS
19	Fractional-order multi-phase oscillators design and analysis suitable for higher-order PSK applications. Analog Integrated Circuits and Signal Processing, 2016, 87, 301-312.	0.9	36
20	Two integrator loop quadrature oscillators: A review. Journal of Advanced Research, 2013, 4, 1-11.	4.4	34
21	Simple sinusoidal active RC oscillators. International Journal of Electronics, 1975, 39, 455-458.	0.9	33
22	Generation of current conveyor based oscillators using nodal admittance matrix expansion. Analog Integrated Circuits and Signal Processing, 2010, 65, 43-59.	0.9	33
23	Transformation of oscillators using Op Amps, unity gain cells and CFOA. Analog Integrated Circuits and Signal Processing, 2010, 65, 105-114.	0.9	30
24	Title is missing!. Analog Integrated Circuits and Signal Processing, 2000, 25, 47-57.	0.9	29
25	Generation of CCII and CFOA filters from passive RLC filters. International Journal of Electronics, 1998, 85, 293-312.	0.9	26
26	New Four-Quadrant CMOS Current-Mode and Voltage-Mode Multipliers. Analog Integrated Circuits and Signal Processing, 2005, 45, 295-307.	0.9	26
27	Current mode filters using two output inverting CCII. International Journal of Circuit Theory and Applications, 2008, 36, 875-881.	1.3	26
28	NOVEL CMOS REALIZATIONS OF THE OPERATIONAL FLOATING CONVEYOR AND APPLICATIONS. Journal of Circuits, Systems and Computers, 2005, 14, 1113-1143.	1.0	23
29	ADJOINT NETWORK THEOREM AND FLOATING ELEMENTS IN THE NAM. Journal of Circuits, Systems and Computers, 2009, 18, 597-616.	1.0	23
30	GENERATION OF THIRD-ORDER QUADRATURE OSCILLATOR CIRCUITS USING NAM EXPANSION. Journal of Circuits, Systems and Computers, 2013, 22, 1350060.	1.0	23
31	Current-Mode Universal Filters Using Current Conveyors: Classification and Review. Circuits, Systems, and Signal Processing, 2008, 27, 405-427.	1.2	21
32	HISTORY AND PROGRESS OF THE TOWâ€"THOMAS BI-QUADRATIC FILTER PART I: GENERATION AND OP AMP REALIZATIONS. Journal of Circuits, Systems and Computers, 2008, 17, 33-54.	1.0	21
33	Biomedical image encryption based on double-humped and fractional logistic maps. , 2017, , .		20
34	Realizations of ideal FDNC and FDNR elements using new types of mutators. International Journal of Electronics, 1978, 44, 317-323.	0.9	19
35	Theorem relating a class of opamp. and current conveyor circuits. International Journal of Electronics, 1995, 79, 53-61.	0.9	19
36	Two novel active RC canonic bandpass networks using the current conveyor. International Journal of Electronics, 1977, 42, 49-54.	0.9	18

#	Article	IF	CITATIONS
37	Novel CMOS realizations of the inverting second-generation current conveyor and applications. Analog Integrated Circuits and Signal Processing, 2007, 52, 57-64.	0.9	18
38	Two New Families of Floating FDNR Circuits. Journal of Electrical and Computer Engineering, 2010, 2010, 1-7.	0.6	18
39	A Novel CMOS Realization of the Differential Input Balanced Output Current Operational Amplifier and its Applications. Analog Integrated Circuits and Signal Processing, 2005, 44, 37-53.	0.9	17
40	A new approach for using the pathological mirror elements in the ideal representation of active devices. International Journal of Circuit Theory and Applications, 2010, 38, 148-178.	1.3	16
41	The voltage mirror–current mirror pair as a universal element. International Journal of Circuit Theory and Applications, 2010, 38, 787-795.	1.3	16
42	Generation of Generalized Impedance Converter Circuits Using NAM Expansion. Circuits, Systems, and Signal Processing, 2011, 30, 1091-1114.	1.2	16
43	Generation of Kerwinâ€Huelsmanâ€Newcomb biquad filter circuits using nodal admittance matrix expansion. International Journal of Circuit Theory and Applications, 2011, 39, 697-717.	1.3	16
44	Generalized family of fractional-order oscillators based on single CFOA and RC network., 2017,,.		16
45	New Wide Band Low Power CMOS Current Conveyors. Analog Integrated Circuits and Signal Processing, 2004, 40, 91-97.	0.9	15
46	HISTORY AND PROGRESS OF THE TOW–THOMAS BIQUADRATIC FILTER PART II: OTRA, CCII, AND DVCC REALIZATIONS. Journal of Circuits, Systems and Computers, 2008, 17, 797-826.	1.0	15
47	New CCII and ICCII Based Realizations of L-C andÂL-R Mutators. Circuits, Systems, and Signal Processing, 2010, 29, 1181-1191.	1.2	15
48	Fractional-order oscillator based on single CCII. , 2016, , .		15
49	Title is missing!. Analog Integrated Circuits and Signal Processing, 1999, 19, 241-254.	0.9	14
50	Analytical synthesis of voltageâ€mode even/oddâ€nthâ€order differential difference current conveyor and fully differential current conveyor llâ€grounded resistor and capacitor universal filter structures. International Journal of Circuit Theory and Applications, 2015, 43, 1263-1310.	1.3	14
51	Fractional-order inverting and non-inverting filters based on CFOA. , 2016, , .		14
52	GENERATION OF OSCILLATORS BASED ON GROUNDED CAPACITOR CURRENT CONVEYORS WITH MINIMUM PASSIVE COMPONENTS. Journal of Circuits, Systems and Computers, 2009, 18, 857-873.	1.0	13
53	Generalized immittance inverters and their realizations. International Journal of Electronics, 1976, 41, 59-64.	0.9	12
54	New Square-Root Domain Oscillators. Analog Integrated Circuits and Signal Processing, 2006, 47, 165-168.	0.9	12

#	Article	IF	CITATIONS
55	Low voltage CMOS fully differential current feedback amplifier with controllable 3-dB bandwidth. Analog Integrated Circuits and Signal Processing, 2007, 52, 139-146.	0.9	12
56	The CCII+ and the ICCII as basic building blocks in lowâ€pass filter realizations. International Journal of Circuit Theory and Applications, 2008, 36, 493-509.	1.3	12
57	CLASSIFICATION AND PATHOLOGICAL REALIZATIONS OF TRANSCONDUCTANCE AMPLIFIERS. Journal of Circuits, Systems and Computers, 2012, 21, 1250010.	1.0	12
58	Fractional order oscillator with independent control of phase and frequency. , 2014, , .		12
59	Parasitic-capacitance-insensitive voltage-mode mosfet-C filters using differential current voltage conveyor. Circuits, Systems, and Signal Processing, 2001, 20, 11-26.	1.2	11
60	ON THE DVCC AND THE BOCCII AS ADJOINT ELEMENTS. Journal of Circuits, Systems and Computers, 2009, 18, 1017-1032.	1.0	11
61	Two Integrator Loop Filters: Generation Using NAM Expansion and Review. Journal of Electrical and Computer Engineering, 2010, 2010, 1-8.	0.6	11
62	Generalized fractional logistic map suitable for data encryption. , 2015, , .		11
63	Stability analysis of fractional-order Colpitts oscillators. Analog Integrated Circuits and Signal Processing, 2019, 101, 267-279.	0.9	11
64	Realizations of fully differential voltage second generation current conveyor with an application. International Journal of Circuit Theory and Applications, 2010, 38, 441-452.	1.3	10
65	HISTORY AND PROGRESS OF THE KERWIN–HUELSMAN–NEWCOMB FILTER GENERATION AND OP AMP REALIZATIONS. Journal of Circuits, Systems and Computers, 2008, 17, 637-658.	1.0	10
66	Transformation of a floating capacitor oscillator to a family of grounded capacitor oscillators. International Journal of Electronics, 2011, 98, 289-300.	0.9	10
67	ACTIVE CIRCULATOR CIRCUITS USING OA, CCII, CFOA AND DVCC. Journal of Circuits, Systems and Computers, 2009, 18, 629-645.	1.0	9
68	HISTORY AND PROGRESS OF THE TOW THOMAS BI-QUADRATIC FILTER PART III: GENERATION USING NAM EXPANSION. Journal of Circuits, Systems and Computers, 2010, 19, 529-548.	1.0	9
69	Analytical synthesis of <i>elliptic</i> voltageâ€mode even/odd― <i>n</i> thâ€order filter structures using DDCCs, FDCCIIs, and grounded capacitors and resistors. IET Circuits, Devices and Systems, 2019, 13, 279-291.	0.9	9
70	A universal activeR biquad. International Journal of Circuit Theory and Applications, 1978, 6, 153-157.	1.3	8
71	NEW GROUNDED-CAPACITOR CURRENT-MODE OSCILLATORS USING SINGLE-OUTPUT CCIIS. Journal of Circuits, Systems and Computers, 1998, 08, 363-378.	1.0	8
72	ON THE INTRODUCTION OF NEW FLOATING CURRENT CONVEYORS. Journal of Circuits, Systems and Computers, 2009, 18, 1005-1016.	1.0	8

#	Article	IF	CITATIONS
73	Novel CMOS Realization of Balanced-Output Third Generation Inverting Current Conveyor withÂApplications. Circuits, Systems, and Signal Processing, 2009, 28, 1037-1051.	1.2	8
74	On-the-Fly Parallel Processing IP-Core for Image Blur Detection, Compression, and Chaotic Encryption Based on FPGA. IEEE Access, 2021, 9, 82726-82746.	2.6	8
75	A new active c differential input integrator using the dvccs/dvcvs. International Journal of Circuit Theory and Applications, 1979, 7, 272-275.	1.3	7
76	Design of high-frequency amplifiers. , 1983, 5, 9-11.		7
77	A Tunable Square Root Domain Oscillator. Analog Integrated Circuits and Signal Processing, 2005, 43, 91-95.	0.9	7
78	A Transformation Method from Voltage-Mode OP-Amp-RC Circuits to Current-Mode Gm-C Circuits. Circuits, Systems, and Signal Processing, 2006, 25, 609-626.	1.2	7
79	High-Order Gm-C Filters with Current Transfer Function Based on Multiple Loop Feedback. , 2007, , .		7
80	CCII based KHN fractional order filter. , 2013, , .		7
81	Current Operational Amplifier (COA): CMOS Realization and Active Compensation. Analog Integrated Circuits and Signal Processing, 2000, 24, 141-152.	0.9	6
82	A New CMOS Rail-to-Rail Low Distortion Balanced Output Transconductor. Analog Integrated Circuits and Signal Processing, 2004, 40, 75-82.	0.9	6
83	New op-amp-RC to G m -C transformation method. Analog Integrated Circuits and Signal Processing, 2006, 49, 79-86.	0.9	6
84	Generation and classification of CCII and ICCII based negative impedance converter circuits using NAM expansion. International Journal of Circuit Theory and Applications, 2011, 39, 835-847.	1.3	6
85	Generation of second generation current conveyor (CCII) family from inverting second generation current conveyor (ICCII) family. International Journal of Electronics, 2010, 97, 405-414.	0.9	6
86	High speed fully differential second generation current conveyor. , 2010, , .		6
87	Current feedback operational amplifier (CFOA) based fractional order oscillators. , 2014, , .		6
88	Fractional order four-phase oscillator based on double integrator topology. , 2017, , .		6
89	Technology Scaling Roadmap for FinFET-Based FPGA Clusters Under Process Variations. Journal of Circuits, Systems and Computers, 2018, 27, 1850056.	1.0	6
90	Hardware Speech Encryption Using a Chaotic Generator, Dynamic Shift and Bit Permutation. , 2018, , .		6

#	Article	IF	CITATIONS
91	A Simple BJT Inverse Memristor Emulator and Its Application in Chaotic Oscillators. , 2019, , .		6
92	A Modified CMOS Balanced Output Transconductor with Extended Linearity. Analog Integrated Circuits and Signal Processing, 2003, 36, 239-244.	0.9	5
93	Multi-phase oscillator for higher-order PSK applications. , 2014, , .		5
94	Generalized delayed logistic map suitable for pseudo-random number generation., 2015,,.		5
95	Fractional order oscillators with single non-zero transmission matrix element. , 2015, , .		5
96	A new highly-linear highly-sensitive differential voltage-to-time converter circuit in CMOS 65nm technology. , 2015, , .		5
97	Multifunction Fractional Inverse Filter Based on OTRA. , 2019, , .		5
98	A new single operational amplifier active RC bandpass network with reduced sensitivity to amplifier gainâ€bandwidth product. International Journal of Circuit Theory and Applications, 1978, 6, 321-326.	1.3	4
99	A New Approach to Realize Variable Gain Amplifiers. Analog Integrated Circuits and Signal Processing, 2002, 30, 257-263.	0.9	4
100	New CMOS operational transresistance amplifier. , 2008, , .		4
101	Novel low-power accurate wide-band CMOS negative-second-generation-current-conveyor realizations based on Floating-Current-Source building blocks. , 2009, , .		4
102	CMOS digitally programmable lossless floating inductor. , 2010, , .		4
103	A Note on the Generation of Generalized Impedance Converter Circuits Using NAM Expansion. Circuits, Systems, and Signal Processing, 2012, 31, 1147-1157.	1.2	4
104	Image encryption based on double-humped and delayed logistic maps for biomedical applications. , 2017,		4
105	A Low-Power Time-Domain Comparator for IoT Applications. , 2018, , .		4
106	A Universal Fractional-Order Memelement Emulation Circuit. , 2019, , .		4
107	Low Voltage CMOS Fully Differential Current Feedback Amplifier with Controllable 3-dB Bandwidth., 2007,,.		3
108	New Current-Mode Bandpass Filters Using Three Single-Output ICCIIs. Active and Passive Electronic Components, 2007, 2007, 1-5.	0.3	3

#	Article	IF	CITATIONS
109	BODE-TYPE EQUALIZERS USING CURRENT CONVEYORS. Journal of Circuits, Systems and Computers, 2009, 18, 433-442.	1.0	3
110	Synthesis of Oscillators Using Limit Variables and NAM Expansion. Active and Passive Electronic Components, 2011, 2011, 1-13.	0.3	3
111	The impact of FinFET technology scaling on critical path performance under process variations. , 2015, , .		3
112	Conversion of a band-pass resonator to an all-pass or a notch filter. International Journal of Electronics, 1975, 38, 559-562.	0.9	2
113	Port Interchange in Voltage Mode Current Conveyor Based Filters. Journal of Circuits, Systems and Computers, 1997, 07, 543-561.	1.0	2
114	A Novel Exponential Voltage-to-Current Converter. Circuits, Systems, and Signal Processing, 2002, 21, 473-483.	1.2	2
115	Two port network analysis for three impedance based oscillators. , 2011, , .		2
116	GENERATION OF CFOA, CCII AND DVCC BASED OSCILLATORS FROM PASSIVE RLC FILTER. Journal of Circuits, Systems and Computers, 2011, 20, 621-639.	1.0	2
117	A comparative study of the Voltage-to-Time converters (VTCs) and the Voltage-to-Frequency converters (VFCs) circuits. , 2016, , .		2
118	A new design methodology for voltage-to-frequency converters (VFCs) circuits suitable for time-based analog-to-digital converters (T-ADCs). Analog Integrated Circuits and Signal Processing, 2018, 94, 277-287.	0.9	2
119	Two topologies of fractional-order oscillators based on CFOA and RC networks. , 2018, , .		2
120	A switched chaotic encryption scheme using multi-mode generalized modified transition map. Multimedia Tools and Applications, 2021, 80, 5373-5402.	2.6	2
121	A Novel Refreshment Circuit for 2T1M Neuromorphic Synapse. Journal of Circuits, Systems and Computers, 2022, 31, .	1.0	2
122	CMOS Programmable Imager Implementing Pre-Processing Operations. Analog Integrated Circuits and Signal Processing, 1999, 19, 279-293.	0.9	1
123	Low voltage CMOS fully differential current feedback amplifier with controllable 3-dB bandwidth. , 2007, , .		1
124	Comment on "The effects of non-idealities and current limitations on the simulated inductances employing current conveyors― Analog Integrated Circuits and Signal Processing, 2007, 50, 283-283.	0.9	1
125	A new 16-bit low-power PVT-calibrated time-based differential Analog-to-Digital Converter (ADC) circuit in CMOS 65nm technology. , 2015, , .		1
126	New Op-Amp Circuits Realizations Using Genetic Algorithm. Journal of Circuits, Systems and Computers, 2017, 26, 1750131.	1.0	1

#	Article	IF	Citations
127	3D-NOCET: A tool for implementing 3D-NoCs based on the Direct-Elevator algorithm., 2017,,.		1
128	A Codec, tiles to NoC router interface, for next generation FPGAs with embedded NoCs. , 2017, , .		1
129	Permutation-Only FPGA Realization of Real-Time Speech Encryption. , 2018, , .		1
130	Frational Order Inverse Filters Based on CCII Family. , 2019, , .		1
131	Design of odd n thâ€order elliptic highâ€pass filters employing OTRAs. IET Circuits, Devices and Systems, 2019, 13, 174-184.	0.9	1
132	A Universal Floating Fractional-Order Elements/Memelements Emulator. , 2019, , .		1
133	Fractional-order Nonminimum-phase Filter Design. , 2019, , .		1
134	Increasing LPG production by adding volatile hydrocarbons to reduce import gap in Egypt. Journal of Petroleum Exploration and Production, 2020, 10, 3733-3750.	1.2	1
135	A 0.002â€mm ² 8â€bit 1â€MS/s lowâ€power timeâ€based DAC (Tâ€DAC). IET Circuits, Devices and 2021, 15, 738-744.	Systems,	1
136	A universal notch filter. International Journal of Circuit Theory and Applications, 1979, 7, 139-142.	1.3	0
137	Letters to the editor a new phase and magnitude compensated weighted summer using three operational amplifiers. International Journal of Circuit Theory and Applications, 1983, 11, 339-342.	1.3	0
138	A Low-Voltage Low-Power Rail-to-Rail Constant g m Transconductance Amplifier. Analog Integrated Circuits and Signal Processing, 2000, 24, 129-139.	0.9	0
139	Two-port oscillators based on three impedance structure. , 2014, , .		0
140	An intelligent technique for generating equivalent KHN circuits using genetic algorithm. , 2015, , .		0
141	Impact of technology scaling on the minimum energy point for FinFET based flip-flops. , 2015, , .		0