

Ki Chul Chun

List of Publications by Year in descending order

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4
papers

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citations

1937685

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2272923

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4
docs citations

4
times ranked

55
citing authors

#	ARTICLE	IF	CITATIONS
1	An In-DRAM BIST for 16 Gb DDR4 DRAM in the 2nd 10-nm-Class DRAM Process. IEEE Access, 2021, 9, 33487-33497.	4.2	7
2	A 16-GB 640-GB/s HBM2E DRAM With a Data-Bus Window Extension Technique and a Synergetic On-Die ECC Scheme. IEEE Journal of Solid-State Circuits, 2021, 56, 199-211.	5.4	24
3	A Bit-by-Bit Re-Writable Eflash in a Generic 65 nm Logic Process for Moderate-Density Nonvolatile Memory Applications. IEEE Journal of Solid-State Circuits, 2014, 49, 1861-1871.	5.4	14
4	A Logic-Compatible Embedded Flash Memory for Zero-Standby Power System-on-Chips Featuring a Multi-Story High Voltage Switch and a Selective Refresh Scheme. IEEE Journal of Solid-State Circuits, 2013, 48, 1302-1314.	5.4	32