

Arnout Beckers

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/8934828/publications.pdf>

Version: 2024-02-01

14

papers

697

citations

1307594

7

h-index

1588992

8

g-index

14

all docs

14

docs citations

14

times ranked

406

citing authors

#	ARTICLE	IF	CITATIONS
1	Generalized Boltzmann relations in semiconductors including band tails. <i>Journal of Applied Physics</i> , 2021, 129, .	2.5	9
2	Theoretical Limit of Low Temperature Subthreshold Swing in Field-Effect Transistors. <i>IEEE Electron Device Letters</i> , 2020, 41, 276-279.	3.9	108
3	Physical Model of Low-Temperature to Cryogenic Threshold Voltage in MOSFETs. <i>IEEE Journal of the Electron Devices Society</i> , 2020, 8, 780-788.	2.1	51
4	Inflection Phenomenon in Cryogenic MOSFET Behavior. <i>IEEE Transactions on Electron Devices</i> , 2020, 67, 1357-1360.	3.0	26
5	Cryo-CMOS Compact Modeling. , 2020, ,.		15
6	A Review on Quantum Computing: From Qubits to Front-end Electronics and Cryogenic MOSFET Physics. , 2019, ,.		57
7	Characterization and modeling of 28-nm FDSOI CMOS technology down to cryogenic temperatures. <i>Solid-State Electronics</i> , 2019, 159, 106-115.	1.4	76
8	Cryogenic MOSFET Threshold Voltage Model. , 2019, ,.		35
9	Characterization and Modeling of 28-nm Bulk CMOS Technology Down to 4.2 K. <i>IEEE Journal of the Electron Devices Society</i> , 2018, 6, 1007-1018.	2.1	107
10	28-nm Bulk and FDSOI Cryogenic MOSFET : (Invited Paper). , 2018, ,.		1
11	Energy filtering in silicon nanowires and nanosheets using a geometric superlattice and its use for steep-slope transistors. <i>Journal of Applied Physics</i> , 2018, 124, .	2.5	5
12	Design-oriented modeling of 28 nm FDSOI CMOS technology down to 4.2 K for quantum computing. , 2018, ,.		32
13	Cryogenic MOS Transistor Model. <i>IEEE Transactions on Electron Devices</i> , 2018, 65, 3617-3625.	3.0	114
14	Cryogenic characterization of 28 nm bulk CMOS technology for quantum computing. , 2017, ,.		61