Francisco Aznar

List of Publications by Year
in descending order

16 papers	138 citations	1307594 7 h-index	1474206 9 g-index
17 all docs	17 docs citations	17 times ranked	101 citing authors

#	Article	IF	Citations
1	Design-Window Methodology for Inductorless Noise-Cancelling CMOS LNAs. IEEE Access, 2022, 10, 29482-29492.	4.2	3
2	A Strategy to Achieve Competitive Performance in Basic RF LNAs. , 2021, , .		0
3	Analysis of Non-Idealities on CMOS Passive Mixers. Electronics (Switzerland), 2021, 10, 1105.	3.1	0
4	A New Approach to the Design of CMOS Inductorless Common-gate Low-noise Amplifiers. , 2020, , .		4
5	Radio over Fiber: An Alternative Broadband Network Technology for IoT. Electronics (Switzerland), 2020, 9, 1785.	3.1	13
6	Methodology for Performance Optimization in Noise- and Distortion-Canceling LNA. , 2019, , .		4
7	8ÂGbits/s inductorless transimpedance amplifier in 90Ânm CMOS technology. Analog Integrated Circuits and Signal Processing, 2014, 79, 27-36.	1.4	10
8	Multi-gigabit analog equalizers for plastic opticalfibers. Microelectronics Journal, 2013, 44, 870-879.	2.0	2
9	CMOS Receiver Front-ends for Gigabit Short-Range Optical Communications. , 2013, , .		11
10	POF Receiver. , 2013, , 147-172.		0
11	Transimpedance Amplifier. , 2013, , 61-98.		0
12	Gigabit Receiver Over 1 mm SI-POF For Home Area Networks. Journal of Lightwave Technology, 2012, 30, 2668-2674.	4.6	14
13	Cost-Effective 1.25-Gb/s CMOS Receiver for 50-m Large-Core SI-POF Links. IEEE Photonics Technology Letters, 2012, 24, 485-487.	2.5	15
14	A 0.18 Î¼m CMOS transimpedance amplifier with 26 dB dynamic range at 2.5 Gb/s. Microelectronics Journal, 2011, 42, 1136-1142.	2.0	18
15	A 0.18Î¼m CMOS linear-in-dB AGC post-amplifier for optical communications. Microelectronics Reliability, 2011, 51, 959-964.	1.7	8
16	Low-Voltage Linearly Tunable CMOS Transconductor With Common-Mode Feedforward. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 715-721.	5.4	36