

Xueliang Wei

List of Publications by Year in descending order

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7
papers

17
citations

2682572

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2272923

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7
all docs

7
docs citations

7
times ranked

10
citing authors

#	ARTICLE	IF	CITATIONS
1	A Low-Latency and High-Endurance MLC STT-MRAM-Based Cache System. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2023, 42, 122-135.	2.7	2
2	Improving Multilevel Writes on Vertical 3-D Cross-Point Resistive Memory. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2021, 40, 762-775.	2.7	2
3	Write-Optimized and Consistent Skiplists for Non-Volatile Memory. IEEE Access, 2021, 9, 69850-69859.	4.2	4
4	STC: Sub-packetization tunable codes for fast recovery. Journal of Systems Architecture, 2020, 105, 101699.	4.3	1
5	CCHL: Compression-Consolidation Hardware Logging for Efficient Failure-Atomic Persistent Memory Updates. , 2020, , .		3
6	A Generic Construction for All Parameters in Minimum Storage Regenerating Codes. , 2019, , .		1
7	NICO: Reducing Software-Transparent Crash Consistency Cost for Persistent Memory. IEEE Transactions on Computers, 2019, 68, 1313-1324.	3.4	4