

Jenny Yi-Chun Liu

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/8876023/publications.pdf>

Version: 2024-02-01

28
papers

161
citations

1937685

4
h-index

1872680

6
g-index

28
all docs

28
docs citations

28
times ranked

160
citing authors

#	ARTICLE	IF	CITATIONS
1	A 180-GHz Low-Noise Amplifier With Recursive Z-Embedding Technique in 40-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 4649-4653.	3.0	4
2	A Sub-THz Frequency Doubler with Quadrature Outputs in 40-nm CMOS. , 2021, , .		0
3	A 28-GHz CMOS Transmitter for Silicon Photonics mmWave-over-Fiber 5G Communications. , 2021, , .		0
4	A 10-bit 1026-Channel Column Driver IC With Partially Segmented Piecewise Linear Digital-to-Analog Converters for UHD TFT-LCDs With One Billion Color Display. IEEE Journal of Solid-State Circuits, 2019, 54, 2703-2716.	5.4	16
5	Signal Generation Techniques in CMOS for Millimeter-Wave and Terahertz Applications. , 2019, , .		2
6	Smart RF Integrated Circuits: A Millimeter-Wave Gigabit Transceiver with Digitally-Enabled Built-In Self-Healing and Auto-Switching Functions. IEEE Microwave Magazine, 2019, 20, 28-37.	0.8	1
7	A W-band Frequency Doubler with Differential Outputs in 90-nm CMOS. , 2019, , .		1
8	A Low Phase Noise 210-GHz Triple-Push Ring Oscillator in 90-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2018, 66, 1983-1997.	4.6	12
9	Two-Way Current Combining Power Amplifier with Multi-Stage Adaptive Bias Control. , 2018, , .		1
10	Design of V-Band CMOS Low-Noise Amplifier and Mixer with Integrated Transformers. , 2018, , .		2
11	A W-Band Low-Noise Amplifier with Shunt Inductors and Transformer Feedback Gm-Boosting Techniques. , 2018, , .		1
12	A 60-GHz Adaptively Biased Power Amplifier with Predistortion Linearizer in 90-nm CMOS. , 2018, , .		10
13	62-92 GHz low-noise transformer-coupled LNA in 90-nm CMOS. Electronics Letters, 2018, 54, 634-636.	1.0	12
14	An adaptively biased stacked power amplifier without output matching network in 90-nm CMOS. , 2017, , .		5
15	A K-band area-saving marchand balun integrated with low-noise amplifier in 0.18- μ m CMOS. , 2017, , .		3
16	A 67 GHz dual injection quadrature VCO with ~ 182.9 dBc/Hz FOM in 90-nm CMOS. , 2017, , .		1
17	Multilayer CPW-Fed Patch Antenna on New AMC Ground Plane for 60 GHz Millimeter-Wave Communications. , 2016, , .		4
18	A push-push voltage-controlled oscillator for W-band applications in 90-nm CMOS. , 2016, , .		3

#	ARTICLE	IF	CITATIONS
19	Electric Hum Signal Readout Circuit for Touch Screen Panel Applications. Journal of Display Technology, 2016, 12, 1444-1450.	1.2	0
20	A 10-GHz bias modulated class-E power amplifier in 90-nm CMOS. , 2016, , .		3
21	A 0.7-V low-phase-noise multi-mode coupled class-B/class-C voltage-controlled oscillator for millimeter-wave applications. , 2015, , .		5
22	A wideband transconductance enhancement CMOS LNA with multiple feedback technique. , 2015, , .		2
23	A K-band power amplifier with adaptive bias in 90-nm CMOS. , 2014, , .		5
24	A feedforward noise and distortion cancellation technique for CMOS broadband LNA-mixer. , 2014, , .		5
25	A Wideband Inductorless Single-to-Differential LNA in $0.18 \mu\text{m}$ CMOS Technology for Digital TV Receivers. IEEE Microwave and Wireless Components Letters, 2014, 24, 472-474.	3.2	58
26	A mixed-signal phase-domain FSK demodulator for BLE single-path low-IF receiver. , 2014, , .		2
27	A K-band power amplifier with adaptive bias in 90-nm CMOS. , 2014, , .		2
28	A 100Gb/s quad-rate transformer-coupled injection-locking CDR circuit in 65nm CMOS. , 2013, , .		1