

# Ling Ren

## List of Publications by Year in Descending Order

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

25  
papers

772  
citations

14  
h-index

27  
g-index

27  
ext. papers

1,010  
ext. citations

1.8  
avg, IF

4.21  
L-index

#	Paper	IF	Citations
25	Multi-Threshold Byzantine Fault Tolerance <b>2021</b> ,		2
24	OnionPIR: Response Efficient Single-Server PIR <b>2021</b> ,		3
23	Good-case Latency of Byzantine Broadcast <b>2021</b> ,		3
22	On the Optimality of Optimistic Responsiveness <b>2020</b> ,		3
21	Sync HotStuff: Simple and Practical Synchronous State Machine Replication <b>2020</b> ,		15
20	A Retrospective on Path ORAM. <i>IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems</i> , <b>2020</b> , 39, 1572-1576	2.5	0
19	Communication Complexity of Byzantine Agreement, Revisited <b>2019</b> ,		21
18	Onion Ring ORAM <b>2019</b> ,		2
17	Synchronous Byzantine Agreement with Expected $O(1)$ Rounds, Expected ( $O(n^2)$ ) Communication, and Optimal Resilience. <i>Lecture Notes in Computer Science</i> , <b>2019</b> , 320-334	0.9	24
16	Flexible Byzantine Fault Tolerance <b>2019</b> ,		19
15	Design and Implementation of the Ascend Secure Processor. <i>IEEE Transactions on Dependable and Secure Computing</i> , <b>2019</b> , 16, 204-216	3.9	8
14	Path ORAM. <i>Journal of the ACM</i> , <b>2018</b> , 65, 1-26	2	27
13	Breaking the Oblivious-RAM Bandwidth Wall <b>2018</b> ,		3
12	Bandwidth-Hard Functions <b>2018</b> ,		3
11	Trapdoor computational fuzzy extractors and stateless cryptographically-secure physical unclonable functions. <i>IEEE Transactions on Dependable and Secure Computing</i> , <b>2017</b> , 14, 65-82	3.9	47
10	FPGA Implementation of a Cryptographically-Secure PUF Based on Learning Parity with Noise. <i>Cryptography</i> , <b>2017</b> , 1, 23	1.9	14
9	HOP: Hardware makes Obfuscation Practical <b>2017</b> ,		24

8	Asymptotically Tight Bounds for Composing ORAM with PIR. <i>Lecture Notes in Computer Science</i> , <b>2017</b> , 91-120	0.9	18
7	Onion ORAM: A Constant Bandwidth Blowup Oblivious RAM. <i>Lecture Notes in Computer Science</i> , <b>2016</b> , 145-174	0.9	62
6	PrORAM <b>2015</b> ,		14
5	Freecursive ORAM <b>2015</b> ,		34
4	A Low-Latency, Low-Area Hardware Oblivious RAM Controller <b>2015</b> ,		25
3	Path ORAM <b>2013</b> ,		358
2	Integrity verification for path Oblivious-RAM <b>2013</b> ,		16
1	Design space exploration and optimization of path oblivious RAM in secure processors. <i>Computer Architecture News</i> , <b>2013</b> , 41, 571-582		24