

Mustak Erhan Yalcin

List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/8797406/publications.pdf>

Version: 2024-02-01

83
papers

1,413
citations

686830

13
h-index

476904

29
g-index

84
all docs

84
docs citations

84
times ranked

778
citing authors

#	ARTICLE	IF	CITATIONS
1	FAMILIES OF SCROLL GRID ATTRACTORS. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2002, 12, 23-41.	0.7	269
2	True Random Bit Generation From a Double-Scroll Attractor. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 1395-1404.	0.1	248
3	MASTER-SLAVE SYNCHRONIZATION OF LUR'E SYSTEMS WITH TIME-DELAY. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2001, 11, 1707-1722.	0.7	238
4	Experimental confirmation of 3- and 5-scroll attractors from a generalized Chua's circuit. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2000, 47, 425-429.	0.1	155
5	n-scroll chaos generators: a simple circuit model. Electronics Letters, 2001, 37, 147.	0.5	87
6	Multi-scroll and hypercube attractors from a general jerk circuit using Josephson junctions. Chaos, Solitons and Fractals, 2007, 34, 1659-1666.	2.5	67
7	INCREASING THE ENTROPY OF A RANDOM NUMBER GENERATOR USING n-SCROLL CHAOTIC ATTRACTORS. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2007, 17, 4471-4479.	0.7	42
8	n-scroll chaotic attractors from a first-order time-delay differential equation. Chaos, 2007, 17, 033112.	1.0	36
9	Toward CNN Chip-Specific Robustness. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 892-902.	0.1	25
10	MULTISCROLL CHAOTIC ATTRACTORS FROM A HYSTERESIS BASED TIME-DELAY DIFFERENTIAL EQUATION. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2010, 20, 3275-3281.	0.7	18
11	A Simple Programmable Autowave Generator Network for Wave Computing Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 1173-1177.	2.2	15
12	True random bit generation with time-delay sampled-data feedback system. Electronics Letters, 2013, 49, 543-545.	0.5	14
13	Multi-scroll chaotic attractors from a generalized time-delay sampled-data system. International Journal of Circuit Theory and Applications, 2016, 44, 1263-1276.	1.3	13
14	CNN Wave based Computation for Robot Navigation on ACE16K. , 0, , .		12
15	Watermarking on CNN-UM for image and video authentication. International Journal of Circuit Theory and Applications, 2004, 32, 591-607.	1.3	11
16	An emulated digital wave computer core implementation. , 2009, , .		11
17	Path planning on cellular nonlinear network using active wave computing technique. Proceedings of SPIE, 2009, , .	0.8	11
18	An implementation of 2D locally coupled relaxation oscillators on an FPGA for real-time autowave generation. , 2008, , .		10

#	ARTICLE	IF	CITATIONS
19	AN APPLICATION OF SMALL-WORLD CELLULAR NEURAL NETWORKS ON ODOR CLASSIFICATION. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2012, 22, 1250013.	0.7	10
20	Chaos Synchronization of Fractional-Order Lurâ€™e Systems. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2020, 30, 2050206.	0.7	9
21	A snail-shaped chaotic system with large bandwidth: dynamical analysis, synchronization and secure communication scheme. SN Applied Sciences, 2020, 2, 1.	1.5	7
22	Spectrum sensing testbed design for cognitive radio applications. , 2011, , .		6
23	Partially Reconfigurable IP Protection System with Ring Oscillator Based Physically Unclonable Functions. , 2017, , .		6
24	A design of cellular automataâ€™based PUF and its implementation on FPGA. International Journal of Circuit Theory and Applications, 2020, 48, 1244-1255.	1.3	6
25	SPATIOTEMPORAL PATTERN FORMATION ON THE ACE16k CNN CHIP. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2006, 16, 1537-1546.	0.7	5
26	Randomly reconfigurable Cellular Neural Network. , 2011, , .		5
27	Analysis of sperm motility with CNN architecture. , 2012, , .		5
28	A Chaotic Time-Delay Sampled-Data System and Its Implementation. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2014, 24, 1450039.	0.7	5
29	Attack on a Chaos-Based Random Number Generator Using Anticipating Synchronization. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2015, 25, 1550021.	0.7	5
30	Wave computer core using fixed-point arithmetic. , 2015, , .		5
31	Cellular Automata with Random Memory and Its Implementations. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2017, 27, 1730017.	0.7	5
32	Spatiotemporal pattern formation in the ACE16k CNN Chip. , 0, , .		4
33	Realization of preprocessing blocks of CNN based CASA system on FPGA. , 2013, , .		4
34	STEADY-STATE ANALYSIS OF NONLINEARLY COUPLED CHUA'S CIRCUITS WITH PERIODIC INPUT. International Journal of Bifurcation and Chaos in Applied Sciences and Engineering, 2003, 13, 3395-3407.	0.7	3
35	A new architecture for Cellular Neural Network on reconfigurable hardware with an advance memory allocation method. , 2010, , .		3
36	An active wave computing based path finding approach for 3-D environment. , 2011, , .		3

#	ARTICLE	IF	CITATIONS
37	Design of a pedestrian dead-reckoning system and comparison of methods on the system. , 2018, , .		3
38	Dynamic Weighing Using a Time-variant Low Pass Filter. , 2019, , .		3
39	Delayed outputs fractional-order hyperchaotic systems synchronization for images encryption. Multimedia Tools and Applications, 2021, 80, 14723-14752.	2.6	3
40	Watermarking for the authentication of video on CNN-UM. , 0, , .		2
41	A new CNN based path planning algorithm improved by the Doppler effect. , 2012, , .		2
42	Throughput enhancement for a new time-delay sampled-data system based True Random Bit Generator. , 2013, , .		2
43	A new Cellular Automata model with Memory and its FPGA implementation. , 2014, , .		2
44	Design and implementation of an ARM based embedded system for pedestrian dead reckoning. , 2015, , .		2
45	A High Speed True Random Number Generator with Cellular Automata with Random Memory. , 2018, , .		2
46	Solving maximum clique problem by cellular neural network. Electronics Letters, 1998, 34, 1504.	0.5	1
47	A new spatio-temporal wave based target tracking algorithm. , 2010, , .		1
48	Nonlinear spatio-temporal wave computing for real-time applications on GPU. , 2012, , .		1
49	A parallelized distance transformation architecture for FPGAs. , 2013, , .		1
50	The Doppler Effect with input driven autowaves. , 2013, , .		1
51	Dynamic behavior of 1-D array of the memristively-coupled Chua's circuits. , 2013, , .		1
52	A 16 × 16 Cellular Logical Network with partial reconfiguration feature. , 2014, , .		1
53	Asynchronous delay doubler and binary lowâ€pass filter for a timeâ€delay chaotic circuit. International Journal of Circuit Theory and Applications, 2016, 44, 1211-1221.	1.3	1
54	Emulating CNN with template learning on FPGA. , 2017, , .		1

#	ARTICLE	IF	CITATIONS
55	Realization of elementary cellular automata with random minimal memory. , 2017, , .		1
56	Design of an UWB-based indoor positioning system. , 2018, , .		1
57	Dynamics, Circuit Design and Synchronization of a New Five Equilibrium Hyperchaotic System. , 2019, , .		1
58	Sperm motility analysis system implemented on a hybrid architecture to produce an intelligent analyzer. Informatics in Medicine Unlocked, 2020, 19, 100324.	1.9	1
59	A new family of 5D, 6D, 7D and 8D hyperchaotic systems from the 4D hyperchaotic Vaidyanathan system, the dynamic analysis of the 8D hyperchaotic system with six positive Lyapunov exponents and an application to secure communication design. International Journal of Modelling, Identification and Control, 2020. 35. 241.	0.2	1
60	Fragile watermarking and unkeyed hash function implementation for image authentication on CNN-UM. , 0, , .		0
61	A programmable hardware for exploring spatiotemporal waves in real-time. , 2008, , .		0
62	Synchronization of first-order time-delay systems generating n-scroll chaotic attractors. , 2008, , .		0
63	A cellular neural network made of relaxation oscillators for autowave generation in CMOS. , 2008, , .		0
64	Using a cellular neural network based olfactory bulb model for choosing the best sensor temperature for an odor classification problem. , 2010, , .		0
65	A novel refinement method for automatic image annotation systems. , 2011, , .		0
66	CMOS realization of a cellular neural network generating autowaves. , 2011, , .		0
67	Suppression of noise from speech signal using spectral subtraction with FPGA. , 2011, , .		0
68	Temperature-Centric Evaluation of Sensor Transients. , 2011, , .		0
69	A feature filtering method for eeg data classification. , 2011, , .		0
70	Implementation of a cellular neural network-based segmentation algorithm on the bio-inspired vision system. Journal of Electronic Imaging, 2011, 20, 013004.	0.5	0
71	Autowaves in 3-D memristive cellular neural networks. , 2013, , .		0
72	Realization of processing blocks of CNN based CASA system on CPU and FPGA. , 2014, , .		0

#	ARTICLE	IF	CITATIONS
73	Digital design of skeletonization. , 2014, , .		0
74	Sperm Morphology Analysis with CNN based algorithms. , 2014, , .		0
75	Cellular network of networks on dynamically partial reconfigurable FPGA. , 2015, , .		0
76	Key generation and license authentication using physical unclonable functions. , 2015, , .		0
77	Computer assisted sperm analysis system designed on a hybrid CPU + FPGA architecture. , 2015, , .		0
78	A chaotic time-delay system based digital RNG and integrated autonomous test suite. , 2017, , .		0
79	Artificial Neural Network Models. SpringerBriefs in Applied Sciences and Technology, 2020, , 5-22.	0.2	0
80	Implementations of CNNs. SpringerBriefs in Applied Sciences and Technology, 2020, , 51-71.	0.2	0
81	Artificial Olfaction System. SpringerBriefs in Applied Sciences and Technology, 2020, , 23-50.	0.2	0
82	A Hardware Accelerated Packet Classifier Design for A Network Router. , 2020, , .		0
83	RFC 2544 Ethernet Performance Measurements Using FPGA Based Dual-Core ARM. , 2021, , .		0