

Chung-Ching Lin

List of Publications by Year in descending order

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9
papers

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citations

2258059

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1872680

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9
times ranked

34
citing authors

#	ARTICLE	IF	CITATIONS
1	A Switching-Less True-Time-Delay-Based Beam Probing Approach for Ultra-Low Latency Wireless Communications: System Analysis and Demonstration. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 4113-4117.	3.0	1
2	Multi-Mode Spatial Signal Processor With Rainbow-Like Fast Beam Training and Wideband Communications Using True-Time-Delay Arrays. IEEE Journal of Solid-State Circuits, 2022, 57, 3348-3360.	5.4	8
3	Fast Beam Training With True-Time-Delay Arrays in Wideband Millimeter-Wave Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1727-1739.	5.4	26
4	Improved Performance Tradeoffs in Harmonic Injection-Locked ULP TX for Sub-GHz Radios. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 2885-2898.	4.6	1
5	A 197.1- $\hat{1}$ / ₄ W Wireless Sensor SoC With an Energy-Efficient Analog Front-End and a Harmonic Injection-Locked OOK TX. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 2444-2456.	5.4	3
6	A 4-Element 800MHz-BW 29mW True-Time-Delay Spatial Signal Processor Enabling Fast Beam-Training with Data Communications. , 2021, , .		9
7	Spur Minimization Techniques for Ultra-Low-Power Injection-Locked Transmitters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3643-3655.	5.4	11
8	A 66.97pJ/bit, 0.0413mm ² Self-Aligned PLL-Calibrated Harmonic-Injection-Locked TX with >62dBc Spur Suppression for IoT Applications. , 2020, , .		3
9	Energy Efficient 5Gb/s/pin Baseband Transceiver for 3D Memory Interface. , 2020, , .		2