

Laszlo Bako

List of Publications by Year in descending order

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Version: 2024-02-01

24
papers

174
citations

1937457

4
h-index

1372474

10
g-index

24
all docs

24
docs citations

24
times ranked

290
citing authors

#	ARTICLE	IF	CITATIONS
1	RISC-V Online Tutor. Lecture Notes in Networks and Systems, 2022, , 131-143.	0.5	1
2	The chromatin-modifying protein HUB2 is involved in the regulation of lignin composition in xylem vessels. Journal of Experimental Botany, 2020, 71, 5484-5494.	2.4	4
3	Testing and analysis of battery wear characteristics. , 2019, , .		0
4	Vicilogic 2.0: Online Learning and Prototyping of Digital Systems Using PYNQ-Z1/-Z2 SoC. , 2018, , .		7
5	ICCapt: Online design capture and HDL generation, with PYNQ SoC prototyping in the cloud. , 2018, , .		0
6	Displacement detection method in video feeds using a distributed architecture on SoC platform for real-time control applications. , 2018, , .		0
7	Audio DSP remote hardware prototyping and console creation. , 2018, , .		2
8	Embedded Implementation of a Real-Time Motion Estimation Method in Video Sequences. Procedia Technology, 2016, 22, 897-904.	1.1	5
9	Design and validation of a low resource-cost video data processing method for embedded implementation of optical flow extraction. , 2015, , .		0
10	Hardware implemented adaptive neuro fuzzy system. , 2015, , .		3
11	Embedded Implementation of a Resource-Efficient Optical Flow Extraction Method. MACRo 2015, 2015, 1, 163-175.	0.1	1
12	Detrended Fluctuation Analysis of EEG Signals. Procedia Technology, 2014, 12, 125-132.	1.1	47
13	Multichannel EEG Signal Recording Analysis based on Cross Frequency Coupling Method. Procedia Technology, 2014, 12, 133-140.	1.1	2
14	A novel bio-signal acquisition system for brain computer interfaces. , 2013, , .		1
15	Evolving advanced neural networks on run-time reconfigurable digital hardware platform. , 2013, , .		0
16	Neuromorphic Neural Network Parallelization on CUDA Compatible GPU for EEG Signal Classification. , 2012, , .		5
17	Real-time classification of datasets with hardware embedded neuromorphic neural networks. Briefings in Bioinformatics, 2010, 11, 348-363.	3.2	11
18	Real-Time Clustering of Datasets with Hardware Embedded Neuromorphic Neural Networks. , 2009, , .		5

#	ARTICLE	IF	CITATIONS
19	Visual trajectory control of a mobile robot using FPGA implemented neural network. Pollack Periodica, 2009, 4, 129-142.	0.2	3
20	Embedded neural controllers based on spiking neuron models. Pollack Periodica, 2009, 4, 143-154.	0.2	4
21	Neural control based on RBF network implemented on FPGA. , 2008, , .		7
22	Hardware implementation of delay-coded spiking-RBF neural network for unsupervised clustering. , 2008, , .		0
23	FPGA Parallel Implementation of CMAC Type Neural Network with on Chip Learning. , 2007, , .		6
24	The Medicago CDKC;1-CYCLINT;1 kinase complex phosphorylates the carboxy-terminal domain of RNA polymerase II and promotes transcription. Plant Journal, 2005, 42, 810-820.	2.8	60