

Remigiusz Wisniewski

List of Publications by Year in Descending Order

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Version: 2024-04-09

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The third column is the impact factor (IF) of the journal, and the fourth column is the number of citations of the article.

49 papers	326 citations	11 h-index	15 g-index
67 ext. papers	470 ext. citations	2.3 avg, IF	4.24 L-index

#	Paper	IF	Citations
49	Trusted and Secure Blockchain-Based Durable Medium Electronic Service. <i>Cryptography</i> , 2022 , 6, 10	1.9	0
48	Hippo-CPS: Verification of Boundedness, Safeness and Liveness of Petri Net-Based Cyber-Physical Systems. <i>IFIP Advances in Information and Communication Technology</i> , 2022 , 74-82	0.5	1
47	Design of Petri Net-Based Cyber-Physical Systems Oriented on the Implementation in Field Programmable Gate Arrays. <i>Energies</i> , 2021 , 14, 7054	3.1	2
46	Verification of the Boundedness Property in a Petri Net-Based Specification of the Control Part of Cyber-Physical Systems. <i>IFIP Advances in Information and Communication Technology</i> , 2021 , 83-91	0.5	1
45	Determinism in Cyber-Physical Systems Specified by Interpreted Petri Nets. <i>Sensors</i> , 2020 , 20,	3.8	8
44	. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems</i> , 2020 , 50, 3592-3597	7.3	6
43	Design and Verification of Cyber-Physical Systems Specified by Petri Nets – A Case Study of a Direct Matrix Converter. <i>Mathematics</i> , 2019 , 7, 812	2.3	10
42	Decomposition of distributed edge systems based on the Petri nets and linear algebra technique. <i>Journal of Systems Architecture</i> , 2019 , 96, 20-31	5.5	6
41	C-Exact Hypergraphs in Concurrency and Sequentiality Analyses of Cyber-Physical Systems Specified by Safe Petri Nets. <i>IEEE Access</i> , 2019 , 7, 13510-13522	3.5	16
40	Petri Net-Based Specification of Cyber-Physical Systems Oriented to Control Direct Matrix Converters With Space Vector Modulation. <i>IEEE Access</i> , 2019 , 7, 23407-23420	3.5	18
39	Analysis and Design Automation of Cyber-Physical System with Hippo and IOPT-Tools 2019 ,		2
38	IEEE Access Special Section: Cyber-Physical Systems. <i>IEEE Access</i> , 2019 , 7, 157688-157692	3.5	2
37	Specification of Cyber-Physical Systems with the Application of Interpreted Nets 2019 ,		4
36	SVM algorithm oriented for implementation in a low-cost Xilinx FPGA. <i>The Integration VLSI Journal</i> , 2019 , 64, 163-172	1.4	8
35	Low-Cost FPGA Hardware Implementation of Matrix Converter Switch Control. <i>IEEE Transactions on Circuits and Systems II: Express Briefs</i> , 2019 , 66, 1177-1181	3.5	9
34	Prototyping of Concurrent Control Systems With Application of Petri Nets and Comparability Graphs. <i>IEEE Transactions on Control Systems Technology</i> , 2018 , 26, 575-586	4.8	22
33	Design of Multi-Context Reconfigurable Logic Controllers Implemented in FPGA Devices Oriented for Further Partial Reconfiguration. <i>Journal of Circuits, Systems and Computers</i> , 2018 , 27, 1850086	0.9	8

32	Safety analysis of Petri nets based on the SM-cover computed with the linear algebra technique 2018 ,		1
31	Representation of primes in the form $p = 6 \cdot x \cdot 1$ and its application to the RSA prime factorization 2018 ,		2
30	Dynamic Partial Reconfiguration of Concurrent Control Systems Specified by Petri Nets and Implemented in Xilinx FPGA Devices. <i>IEEE Access</i> , 2018 , 6, 32376-32391	3.5	19
29	Design and Verification of Real-Life Processes With Application of Petri Nets. <i>IEEE Transactions on Systems, Man, and Cybernetics: Systems</i> , 2017 , 47, 2856-2869	7.3	33
28	Dynamic Partial Reconfiguration of Concurrent Control Systems Implemented in FPGA Devices. <i>IEEE Transactions on Industrial Informatics</i> , 2017 , 13, 1734-1741	11.9	18
27	Prototyping of Concurrent Control Systems. <i>Advances in Industrial Control</i> , 2017 , 99-116	0.3	
26	Perfect Graphs and Comparability Graphs. <i>Advances in Industrial Control</i> , 2017 , 31-48	0.3	1
25	Prototyping of Concurrent Control Systems Implemented in FPGA Devices. <i>Advances in Industrial Control</i> , 2017 ,	0.3	11
24	IoT security with one-time pad secure algorithm based on the double memory technique 2017 ,		2
23	Hypergraphs and Exact Transversals. <i>Advances in Industrial Control</i> , 2017 , 49-57	0.3	
22	Modelling of Concurrent Systems in Hardware Languages. <i>Advances in Industrial Control</i> , 2017 , 117-137	0.3	
21	Analysis of Concurrent Control Systems. <i>Advances in Industrial Control</i> , 2017 , 59-76	0.3	
20	Decomposition of Concurrent Control Systems. <i>Advances in Industrial Control</i> , 2017 , 77-98	0.3	
19	Implementation of Concurrent Control Systems in FPGA. <i>Advances in Industrial Control</i> , 2017 , 139-165	0.3	
18	Effective Partial Reconfiguration of Logic Controllers Implemented in FPGA Devices. <i>Studies in Systems, Decision and Control</i> , 2016 , 45-55	0.8	1
17	Partial reconfiguration of concurrent logic controllers implemented in FPGA devices 2016 ,		1
16	Application of Hypergraphs in the Prime Implicants Selection Process. <i>IFAC-PapersOnLine</i> , 2015 , 48, 302-305		3
15	Exact cover of states in the discrete state-space system 2015 ,		3

14	Design and verification of distributed logic controllers with application of Petri nets 2015 ,		1
13	Relation between SM-covers and SM-decompositions of Petri nets 2015 ,		2
12	Decomposition, validation and documentation of control process specification in form of a Petri net 2014 ,		13
11	Dual synthesis of Petri net based dependable logic controllers for safety critical systems 2014 ,		1
10	Application of comparability graphs in decomposition of Petri nets 2014 ,		11
9	Theoretical Aspects of Petri Nets Decomposition Based on Invariants and Hypergraphs. <i>Lecture Notes in Electrical Engineering</i> , 2014 , 371-376	0.2	15
8	Application of Hypergraphs to SMCs Selection. <i>IFIP Advances in Information and Communication Technology</i> , 2014 , 249-256	0.5	14
7	Application of an Exact Transversal Hypergraph in Selection of SM-Components. <i>IFIP Advances in Information and Communication Technology</i> , 2013 , 250-257	0.5	10
6	Design of microprogrammed controllers with address converter implemented on programmable systems with embedded memories 2011 ,		2
5	Design of microprogrammed controllers to be implemented in FPGAs. <i>International Journal of Applied Mathematics and Computer Science</i> , 2011 , 21, 401-412	1.7	26
4	Application of hypergraphs in microcode length reduction of microprogrammed controllers 2009 ,		4
3	Partial reconfiguration of compositional microprogram control units implemented on FPGAs. <i>IFAC Postprint Volumes IPPV / International Federation of Automatic Control</i> , 2006 , 39, 116-119		10
2	OPTIMIZATION OF LUT-ELEMENTS AMOUNT IN CONTROL UNIT OF SYSTEM-ON-CHIP. <i>IFAC Postprint Volumes IPPV / International Federation of Automatic Control</i> , 2006 , 39, 143-146		
1	DESIGN OF COMPOSITIONAL MICROPROGRAM CONTROL UNITS WITH ELEMENTARY OPERATIONAL LINEAR CHAINS. <i>IFAC Postprint Volumes IPPV / International Federation of Automatic Control</i> , 2006 , 39, 191-194		1