Remigiusz Wisniewski

List of Publications by Citations

49 papers	326 citations	11 h-index	15 g-index
67 ext. papers	470 ext. citations	2.3 avg, IF	4.24 L-index

#	Paper	IF	Citations
49	Design and Verification of Real-Life Processes With Application of Petri Nets. *IEEE Transactions on Systems, Man, and Cybernetics: Systems*, **2017**, 47, 2856-2869	7.3	33
48	Design of microprogrammed controllers to be implemented in FPGAs. *International Journal of Applied Mathematics and Computer Science*, **2011**, 21, 401-412	1.7	26
47	Prototyping of Concurrent Control Systems With Application of Petri Nets and Comparability Graphs. *IEEE Transactions on Control Systems Technology*, **2018**, 26, 575-586	4.8	22
46	Dynamic Partial Reconfiguration of Concurrent Control Systems Specified by Petri Nets and Implemented in Xilinx FPGA Devices. *IEEE Access*, **2018**, 6, 32376-32391	3.5	19
45	Dynamic Partial Reconfiguration of Concurrent Control Systems Implemented in FPGA Devices. *IEEE Transactions on Industrial Informatics*, **2017**, 13, 1734-1741	11.9	18
44	Petri Net-Based Specification of Cyber-Physical Systems Oriented to Control Direct Matrix Converters With Space Vector Modulation. *IEEE Access*, **2019**, 7, 23407-23420	3.5	18
43	C-Exact Hypergraphs in Concurrency and Sequentiality Analyses of Cyber-Physical Systems Specified by Safe Petri Nets. *IEEE Access*, **2019**, 7, 13510-13522	3.5	16
42	Theoretical Aspects of Petri Nets Decomposition Based on Invariants and Hypergraphs. *Lecture Notes in Electrical Engineering*, **2014**, 371-376	0.2	15
41	Application of Hypergraphs to SMCs Selection. *IFIP Advances in Information and Communication Technology*, **2014**, 249-256	0.5	14
40	Decomposition, validation and documentation of control process specification in form of a Petri net **2014**,		13
39	Application of comparability graphs in decomposition of Petri nets **2014**,		11
38	Prototyping of Concurrent Control Systems Implemented in FPGA Devices. *Advances in Industrial Control*, **2017**,	0.3	11
37	Design and Verification of Cyber-Physical Systems Specified by Petri Nets—A Case Study of a Direct Matrix Converter. *Mathematics*, **2019**, 7, 812	2.3	10
36	Partial reconfiguration of compositional microprogram control units implemented on FPGAS. *IFAC Postprint Volumes IPPV / International Federation of Automatic Control*, **2006**, 39, 116-119		10
35	Application of an Exact Transversal Hypergraph in Selection of SM-Components. *IFIP Advances in Information and Communication Technology*, **2013**, 250-257	0.5	10
34	Low-Cost FPGA Hardware Implementation of Matrix Converter Switch Control. *IEEE Transactions on Circuits and Systems II: Express Briefs*, **2019**, 66, 1177-1181	3.5	9
33	Determinism in Cyber-Physical Systems Specified by Interpreted Petri Nets. *Sensors*, **2020**, 20,	3.8	8

32	Design of Multi-Context Reconfigurable Logic Controllers Implemented in FPGA Devices Oriented for Further Partial Reconfiguration. *Journal of Circuits, Systems and Computers*, **2018**, 27, 1850086	0.9	8
31	SVM algorithm oriented for implementation in a low-cost Xilinx FPGA. *The Integration VLSI Journal*, **2019**, 64, 163-172	1.4	8
30	Decomposition of distributed edge systems based on the Petri nets and linear algebra technique. *Journal of Systems Architecture*, **2019**, 96, 20-31	5.5	6
29	. *IEEE Transactions on Systems, Man, and Cybernetics: Systems*, **2020**, 50, 3592-3597	7.3	6
28	Application of hypergraphs in microcode length reduction of microprogrammed controllers **2009**,		4
27	Specification of Cyber-Physical Systems with the Application of Interpreted Nets **2019**,		4
26	Exact cover of states in the discrete state-space system **2015**,		3
25	IoT security with one-time pad secure algorithm based on the double memory technique **2017**,		2
24	Relation between SM-covers and SM-decompositions of Petri nets **2015**,		2
23	Design of microprogrammed controllers with address converter implemented on programmable systems with embedded memories **2011**,		2
22	Design of Petri Net-Based Cyber-Physical Systems Oriented on the Implementation in Field Programmable Gate Arrays. *Energies*, **2021**, 14, 7054	3.1	2
21	Analysis and Design Automation of Cyber-Physical System with Hippo and IOPT-Tools **2019**,		2
20	IEEE Access Special Section: Cyber-Physical Systems. *IEEE Access*, **2019**, 7, 157688-157692	3.5	2
19	Representation of primes in the form p = 6 x 1 and its application to the RSA prime factorization **2018**,		2
18	Effective Partial Reconfiguration of Logic Controllers Implemented in FPGA Devices. *Studies in Systems, Decision and Control*, **2016**, 45-55	0.8	1
17	Dual synthesis of Petri net based dependable logic controllers for safety critical systems **2014**,		1
16	Perfect Graphs and Comparability Graphs. *Advances in Industrial Control*, **2017**, 31-48	0.3	1
15	Design and verification of distributed logic controllers with application of Petri nets **2015**,		1

14	DESIGN OF COMPOSITIONAL MICROPROGRAM CONTROL UNITS WITH ELEMENTARY OPERATIONAL LINEAR CHAINS. *IFAC Postprint Volumes IPPV / International Federation of Automatic Control*, **2006**, 39, 191-194		1
13	Partial reconfiguration of concurrent logic controllers implemented in FPGA devices **2016**,		1
12	Safety analysis of Petri nets based on the SM-cover computed with the linear algebra technique **2018**,		1
11	Verification of the Boundedness Property in a Petri Net-Based Specification of the Control Part of Cyber-Physical Systems. *IFIP Advances in Information and Communication Technology*, **2021**, 83-91	0.5	1
10	Hippo-CPS: Verification of Boundedness, Safeness and Liveness of Petri Net-Based Cyber-Physical Systems. *IFIP Advances in Information and Communication Technology*, **2022**, 74-82	0.5	1
9	Trusted and Secure Blockchain-Based Durable Medium Electronic Service. *Cryptography*, **2022**, 6, 10	1.9	0
8	Application of Hypergraphs in the Prime Implicants Selection Process. *IFAC-PapersOnLine*, **2015**, 48, 302-305	37	
7	Prototyping of Concurrent Control Systems. *Advances in Industrial Control*, **2017**, 99-116	0.3	
6	OPTIMIZATION OF LUT-ELEMENTS AMOUNT IN CONTROL UNIT OF SYSTEM-ON-CHIP. *IFAC Postprint Volumes IPPV / International Federation of Automatic Control*, **2006**, 39, 143-146		
5	Hypergraphs and Exact Transversals. *Advances in Industrial Control*, **2017**, 49-57	0.3	
4	Modelling of Concurrent Systems in Hardware Languages. *Advances in Industrial Control*, **2017**, 117-137	0.3	
3	Analysis of Concurrent Control Systems. *Advances in Industrial Control*, **2017**, 59-76	0.3	
2	Decomposition of Concurrent Control Systems. *Advances in Industrial Control*, **2017**, 77-98	0.3	
1	Implementation of Concurrent Control Systems in FPGA. *Advances in Industrial Control*, **2017**, 139-165	0.3	