## Muzaffar Rao

## List of Publications by Year in descending order

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1684188 1125743 26 209 5 13 citations g-index h-index papers 26 26 26 228 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	Integration of an MES and AIV Using a LabVIEW Middleware Scheduler Suitable for Use in Industry 4.0 Applications. Applied Sciences (Switzerland), 2020, 10, 7054.	2.5	4
2	Geometric Insight into the Control Allocation Problem for Open-Frame ROVs and Visualisation of Solution. Robotics, 2020, 9, 7.	3.5	3
3	Real-Time Secure/Unsecure Video Latency Measurement/Analysis with FPGA-Based Bump-in-the-Wire Security. Sensors, 2019, 19, 2984.	3.8	4
4	Integration of autonomous intelligent vehicles into manufacturing environments: Challenges. Procedia Manufacturing, 2019, 38, 1683-1690.	1.9	4
5	LabVIEW-FPGA based implementation of an Authenticated Encryption core., 2019,,.		2
6	Bump in the wire (BITW) security solution for a marine ROV remote control application. Journal of Information Security and Applications, 2018, 38, 111-121.	2.5	3
7	An Efficient High Speed AES Implementation Using Traditional FPGA and LabVIEW FPGA Platforms. , 2018, , .		5
8	Real-Time Video Latency Measurement between a Robot and Its Remote Control Station: Causes and Mitigation. Wireless Communications and Mobile Computing, 2018, 2018, 1-19.	1.2	12
9	An efficient implementation of FPGA based high speed IPSec (AH/ESP) core. International Journal of Internet Protocol Technology, 2018, 11, 97.	0.2	4
10	An efficient implementation of FPGA based high speed IPSec (AH/ESP) core. International Journal of Internet Protocol Technology, 2018, 11, 97.	0.2	0
11	A secure end-to-end IoT solution. Sensors and Actuators A: Physical, 2017, 263, 291-299.	4.1	34
12	Efficient and High Speed FPGA Bump in the Wire Implementation for Data Integrity and Confidentiality Services in the IoT. Smart Sensors, Measurement and Instrumentation, 2017, , 259-285.	0.6	5
13	Cluster head election and rotation for medical-based wireless sensor networks. , 2017, , .		2
14	Defence against Black Hole and Selective Forwarding Attacks for Medical WSNs in the IoT. Sensors, 2016, 16, 118.	3.8	64
15	An FPGA based reconfigurable IPSec ESP core suitable for IoT applications. , 2016, , .		9
16	An FPGA-based reconfigurable IPSec AH core with efficient implementation of SHA-3 for high speed IoT applications. Security and Communication Networks, 2016, 9, 3282-3295.	1.5	6
17	High Speed Implementation of a SHA-3 Core on Virtex-5 and Virtex-6 FPGAs. Journal of Circuits, Systems and Computers, 2016, 25, 1650069.	1.5	8
18	AES implementation on Xilinx FPGAs suitable for FPGA based WBSNs. , 2015, , .		12

#	Article	IF	CITATIONS
19	FPGA Based Reconfigurable IPSec AH Core Suitable for IoT Applications. , 2015, , .		6
20	FPGA Based Real Time 'Secure' Body Temperature Monitoring Suitable for WBSN. , 2015, , .		2
21	Healthcare WSN: Cluster Elections and Selective Forwarding Defense. , 2015, , .		6
22	Logically Optimized Smallest FPGA Architecture for SHA- 3 Core. Communications in Computer and Information Science, 2014, , 195-203.	0.5	0
23	Efficient High Speed Implementation of Secure Hash Algorithm-3 on Virtex-5 FPGA. , 2014, , .		3
24	Secure Hash Algorithm-3(SHA-3) implementation on Xilinx FPGAs, Suitable for IoT Applications. International Journal on Smart Sensing and Intelligent Systems, 2014, 7, 1-6.	0.7	10
25	Efficient Utilization of FPGA Using LUT-6 Architecture. Applied Mechanics and Materials, 0, 241-244, 2548-2554.	0.2	1
26	Middleware Application, Suitable to Build an Automated and Connected Smart Manufacturing Environment. , 0, , .		0