

# Horst K Zimmermann

## List of Publications by Year in descending order

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261  
papers

2,324  
citations

279701

23  
h-index

302012

39  
g-index

274  
all docs

274  
docs citations

274  
times ranked

1844  
citing authors

#	ARTICLE	IF	CITATIONS
1	Zero-bias 40Gbit/s germanium waveguide photodetector on silicon. Optics Express, 2012, 20, 1096.	1.7	368
2	A Comparator With Reduced Delay Time in 65-nm CMOS for Supply Voltages Down to 0.65 V. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 810-814.	2.2	125
3	Integrated Silicon Optoelectronics. Springer Series in Optical Sciences, 2010, , .	0.5	95
4	Integrated Silicon Optoelectronics. Springer Series in Photonics, 2000, , .	0.8	66
5	A 65nm CMOS comparator with modified latch to achieve 7GHz/1.3mW at 1.2V and 700MHz/47&#x00B5;W at 0.6V. , 2009, , .		58
6	A 16\$, imes \$16 Pixel Distance Sensor With In-Pixel Circuitry That Tolerates 150 klx of Ambient Light. IEEE Journal of Solid-State Circuits, 2010, 45, 1345-1353.	3.5	51
7	Optical Wireless Communication With Adaptive Focus and MEMS-Based Beam Steering. IEEE Photonics Technology Letters, 2013, 25, 1428-1431.	1.3	51
8	1.25 Gbit/s Over 50 m Step-Index Plastic Optical Fiber Using a Fully Integrated Optical Receiver With an Integrated Equalizer. Journal of Lightwave Technology, 2012, 30, 118-122.	2.7	48
9	Design and Implementation of an Integrated Reconfigurable Silicon Photonics Switch Matrix in IRIS Project. IEEE Journal of Selected Topics in Quantum Electronics, 2016, 22, 155-168.	1.9	44
10	Low-power 10ÂGb/s inductorless inverter based common-drain active feedback transimpedance amplifier in 40Ânm CMOS. Analog Integrated Circuits and Signal Processing, 2013, 76, 367-376.	0.9	42
11	Integrated fiber optical receiver reducing the gap to the quantum limit. Scientific Reports, 2017, 7, 2652.	1.6	36
12	Single-Photon Avalanche Photodiode Based Fiber Optic Receiver for Up to 200 Mb/s. IEEE Journal of Selected Topics in Quantum Electronics, 2018, 24, 1-8.	1.9	36
13	Linear Mode Avalanche Photodiode With High Responsivity Integrated in High-Voltage CMOS. IEEE Electron Device Letters, 2014, 35, 897-899.	2.2	34
14	Silicon Optoelectronic Integrated Circuits. Springer Series in Advanced Microelectronics, 2004, , .	0.3	33
15	Avalanche Double Photodiode in 40-nm Standard CMOS Technology. IEEE Journal of Quantum Electronics, 2013, 49, 350-356.	1.0	33
16	PWMâ€Driven Thermally Tunable Silicon Microring Resonators: Design, Fabrication, and Characterization. Laser and Photonics Reviews, 2019, 13, 1800275.	4.4	33
17	Optical Receiver Using Noise Cancelling With an Integrated Photodiode in 40 nm CMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2013, 60, 1929-1936.	3.5	32
18	Silicon carrier depletion modulator with 10ÂGbit/s driver realized in high-performance photonic BiCMOS. Laser and Photonics Reviews, 2014, 8, 180-187.	4.4	31

#	ARTICLE	IF	CITATIONS
19	Dynamic Integrated MPP Tracker in 0.35 $\mu\text{m}$ CMOS. IEEE Transactions on Power Electronics, 2013, 28, 2886-2894.	5.4	29
20	Integrated BiCMOS p-i-n Photodetectors With High Bandwidth and High Responsivity. IEEE Journal of Selected Topics in Quantum Electronics, 2006, 12, 1469-1475.	1.9	26
21	OWC Using a Fully Integrated Optical Receiver With Large-Diameter APD. IEEE Photonics Technology Letters, 2015, 27, 482-485.	1.3	26
22	A design example of a 65-nm CMOS operational amplifier. International Journal of Circuit Theory and Applications, 2007, 35, 343-354.	1.3	24
23	Pulse Shape Measurements by On-Chip Sense Amplifiers of Single Event Transients Propagating Through a 90 nm Bulk CMOS Inverter Chain. IEEE Transactions on Nuclear Science, 2012, 59, 2778-2784.	1.2	23
24	Optical Wireless APD Receiver With High Background-Light Immunity for Increased Communication Distances. IEEE Journal of Solid-State Circuits, 2016, 51, 1663-1673.	3.5	23
25	10Gbit/s 2mW inductorless transimpedance amplifier. , 2012, , .		20
26	PIN Photodiode Optoelectronic Integrated Receiver Used for 3-Gb/s Free-Space Optical Communication. IEEE Journal of Selected Topics in Quantum Electronics, 2014, 20, 391-400.	1.9	20
27	A 0.18 $\mu\text{m}$ CMOS transimpedance amplifier with 26 dB dynamic range at 2.5 Gb/s. Microelectronics Journal, 2011, 42, 1136-1142.	1.1	18
28	035 $\mu\text{m}$ CMOS avalanche photodiode with high responsivity and responsivity-bandwidth product. Optics Letters, 2014, 39, 586.	1.7	18
29	$400\text{-}\mu\text{m}$ Diameter APD OEIC in $0.35\text{-}\mu\text{m}$ BiCMOS. IEEE Photonics Technology Letters, 2016, 28, 2004-2007.	1.3	18
30	Temperature Dependence of Dark Count Rate and After Pulsing of a Single-Photon Avalanche Diode with an Integrated Active Quenching Circuit in 0.35 $\mu\text{m}$ CMOS. Journal of Sensors, 2018, 2018, 1-7.	0.6	18
31	Extraneous-light resistant multipixel range sensor based on a low-power correlating pixel-circuit. , 2009, , .		16
32	Optical Communication over Plastic Optical Fibers. Springer Series in Optical Sciences, 2013, , .	0.5	16
33	10Gb/s inverter based cascode transimpedance amplifier in 40nm CMOS technology. , 2013, , .		16
34	Integrated Reconfigurable Silicon Photonics Switch Matrix in IRIS Project: Technological Achievements and Experimental Results. Journal of Lightwave Technology, 2019, 37, 345-355.	2.7	16
35	Real-Time 1.25-Gb/s Transmission Over 50-m SI-POF Using a Green Laser Diode. IEEE Photonics Technology Letters, 2012, 24, 1331-1333.	1.3	15
36	An infrastructure for accurate characterization of single-event transients in digital circuits. Microprocessors and Microsystems, 2013, 37, 772-791.	1.8	15

#	ARTICLE	IF	CITATIONS
37	Linear Mode Avalanche Photodiode With 1-GHz Bandwidth Fabricated in 0.35- $\mu\text{m}$ CMOS. IEEE Photonics Technology Letters, 2014, 26, 1511-1514.	1.3	15
38	A Fully Integrated SPAD-Based CMOS Data-Receiver With a Sensitivity of $\sim 64$ dBm at 20 Mb/s. IEEE Solid-State Circuits Letters, 2018, 1, 2-5.	1.3	15
39	Low-Power BiCMOS Optical Receiver With Voltage-Controlled Transimpedance. IEEE Journal of Solid-State Circuits, 2007, 42, 613-626.	3.5	14
40	Experimental Investigation of Single-Event Transient Waveforms Depending on Transistor Spacing and Charge Sharing in 65-nm CMOS. IEEE Transactions on Nuclear Science, 2017, 64, 2136-2143.	1.2	14
41	2.5Gbit/s transimpedance amplifier using noise cancelling for optical receivers. , 2012, , .		13
42	Integrated Pulsewidth Modulation Control for a Scalable Optical Switch Matrix. IEEE Photonics Journal, 2015, 7, 1-7.	1.0	13
43	A maximum power-point tracker without digital signal processing in 0.35 $\mu\text{m}$ CMOS for automotive applications. , 2012, , .		12
44	Nonlinear Current Control for Power Electronic Converters: IC Design Aspects and Implementation. IEEE Transactions on Power Electronics, 2013, 28, 4910-4916.	5.4	12
45	Continuous-Time Common-Mode Feedback Circuit for Applications with Large Output Swing and High Output Impedance. , 2008, , .		11
46	Blue-Enhanced PIN Finger Photodiodes in a 0.35- $\mu\text{m}$ SiGe BiCMOS Technology. IEEE Photonics Technology Letters, 2009, 21, 1656-1658.	1.3	11
47	Range finding sensor in 90nm CMOS with bridge correlator based background light suppression. , 2010, , .		11
48	Automated alignment system for optical wireless communication systems using image recognition. Optics Letters, 2014, 39, 4045.	1.7	11
49	Optoelectronic Circuits in Nanometer CMOS Technology. Springer Series in Advanced Microelectronics, 2016, , .	0.3	11
50	A 3D Photonic-Electronic Integrated Transponder Aggregator With $48 \times 16$ Heater Control Cells. IEEE Photonics Technology Letters, 2018, 30, 681-684.	1.3	11
51	APD and SPAD Receivers : Invited Paper. , 2019, , .		11
52	A $2 \times 32$ range-finding sensor array with pixel-inherent suppression of ambient light up to 120klx. , 2009, , .		10
53	High dynamic range background light suppression for a TOF distance measurement sensor in 180nm CMOS. , 2011, , .		10
54	8Gbits/s inductorless transimpedance amplifier in 90nm CMOS technology. Analog Integrated Circuits and Signal Processing, 2014, 79, 27-36.	0.9	10

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55	Modeling and Analysis of BER Performance in a SPAD-Based Integrated Fiber Optical Receiver. IEEE Photonics Journal, 2018, 10, 1-11.	1.0	10
56	Optical wireless communication using a fully integrated 400 Åµm diameter APD receiver. Journal of Engineering, 2017, 2017, 506-511.	0.6	10
57	A 0.12µm CMOS Comparator Requiring 0.5V at 600MHz and 1.5V at 6GHz. , 2007, , .		9
58	Supply Voltage Dependent On-Chip Single-Event Transient Pulse Shape Measurements in 90-nm Bulk CMOS Under Alpha Irradiation. IEEE Transactions on Nuclear Science, 2013, 60, 2640-2646.	1.2	9
59	FPGA based time-of-flight 3D camera characterization system. , 2013, , .		9
60	Statistical Study of Intrinsic Parasitics in an SPAD-Based Integrated Fiber Optical Receiver. IEEE Transactions on Electron Devices, 2019, 66, 497-504.	1.6	9
61	Optical and Electrical Characterization and Modeling of Photon Detection Probability in CMOS Single-Photon Avalanche Diodes. IEEE Sensors Journal, 2021, 21, 7572-7580.	2.4	9
62	Optical Receiver With Widely Tunable Sensitivity in BiCMOS Technology. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1223-1236.	3.5	8
63	Ultra-low power low-complexity 3.5GHz IR-UWB transmitter with spectrum tunability. IET Circuits, Devices and Systems, 2020, 14, 521-527.	0.9	8
64	A Low-Power 4GHz Comparator in 120nm CMOS Technology with a Technique to tune Resolution. , 2006, , .		7
65	A 3rd-Order 235MHz Low-Pass gmC-Filter in 120nm CMOS. , 2006, , .		7
66	Efficient four-stage frequency compensation for low-voltage amplifiers. , 2008, , .		7
67	A 78.4 dB Photo-Sensitivity Dynamic Range, 285 TΩHz Transimpedance Bandwidth Product BiCMOS Optical Sensor for Optical Storage Systems. IEEE Journal of Solid-State Circuits, 2011, 46, 1170-1182.	3.5	7
68	Investigation of the distance error induced by cycle-to-cycle jitter in a correlating time-of-flight distance measurement system. Optical Engineering, 2014, 53, 073104.	0.5	7
69	Analog Filters in Nanometer CMOS. Springer Series in Advanced Microelectronics, 2014, , .	0.3	7
70	Visible light communication at 50 Mbit/s using a red LED and an SPAD receiver. , 2018, , .		7
71	Performance of high-voltage CMOS single-photon avalanche diodes with and without well-modulation technique. Optical Engineering, 2020, 59, 1.	0.5	7
72	Parallel optical interconnects with mixed-signal OEIC and fibre arrays for high-speed communication. , 2004, 5453, 111.		6

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73	Distance Measurement Line Sensor with PIN Photodiodes. , 2006, , .		6
74	A low-voltage low-power fully differential rail-to-rail input/output opamp in 65-nm CMOS. , 2008, , .		6
75	An integrated low power buck converter with a comparator controlled low-side switch. , 2010, , .		6
76	An 85dB dynamic range transimpedance amplifier in 40nm CMOS technology. , 2011, , .		6
77	Thick detection zone single-photon avalanche diode fabricated in 0.35 $\mu\text{m}$ complementary metal-oxide semiconductors. Optical Engineering, 2015, 54, 050503.	0.5	6
78	10 Gb/s Switchable Binary/PAM-4 Receiver and Ring Modulator Driver for 3-D Optoelectronic Integration. IEEE Journal of Selected Topics in Quantum Electronics, 2016, 22, 344-352.	1.9	6
79	OWC using a monolithically integrated 200 $\mu\text{m}$ APD OEIC in 035 $\mu\text{m}$ BiCMOS technology. Optics Express, 2016, 24, 918.	1.7	6
80	Optoelectronic Circuits in Nanometer CMOS Technology. Springer Series in Advanced Microelectronics, 2016, , 217-240.	0.3	6
81	Integrated Fast-Sensing Triple-Voltage SPAD Quenching/Resetting Circuit for Increasing PDP. IEEE Photonics Technology Letters, 2021, 33, 139-142.	1.3	6
82	Integrated photodiodes in standard BiCMOS technology. , 2003, 4989, 103.		5
83	Complete low-cost 625Mbit/s optical fiber receiver in 0.6 $\mu\text{m}$ BiCMOS technology. , 2005, , .		5
84	A 1GHz-GBW operational amplifier for DVB-H receivers in 65nm CMOS. , 2009, , .		5
85	TOF range finding sensor in 90nm CMOS capable of suppressing 180 klx ambient light. , 2010, , .		5
86	An integrated optical receiver for 2.5Gbit/s using 4-PAM signaling. , 2010, , .		5
87	40Gbit/s germanium waveguide photodetector on silicon. , 2012, , .		5
88	On fully differential and complementary single-stage self-biased CMOS differential amplifiers. , 2013, , .		5
89	A background light resistant TOF range finder with integrated PIN photodiode in 0.35 $\mu\text{m}$ CMOS. Proceedings of SPIE, 2013, , .	0.8	5
90	Phototransistor noise model based on noise measurements on PNP PIN phototransistors. Optical and Quantum Electronics, 2014, 46, 1269-1275.	1.5	5

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91	Evidence of Pulse Quenching in AND and OR Gates by Experimental Probing of Full Single-Event Transient Waveforms. IEEE Transactions on Nuclear Science, 2018, 65, 382-390.	1.2	5
92	Photon detection probability enhancement using an anti-reflection coating in CMOS-based SPADs. Applied Optics, 2021, 60, 7815.	0.9	5
93	Comparison of CMOS and BiCMOS optical receiver SoCs. , 2003, , .		5
94	Optical wireless APD receivers in 035 Åµm HV CMOS technology with large detection area. Optics Express, 2019, 27, 11930.	1.7	5
95	<title>High-performance receivers for optical interconnects in standard MOS technology</title>. , 2001, , .		4
96	A clocked, regenerative comparator in 0.12&#x03BC;m CMOS with tunable sensitivity. Solid-State Circuits Conference, 2008 ESSCIRC 2008 34th European, 2007, , .	0.0	4
97	A 122ÂĤ Hz transimpedance bandwidth product BiCMOS optical sensor front-end with a 54.7ÂĤdB voltage-controlled photo-sensitivity range. Analog Integrated Circuits and Signal Processing, 2009, 61, 19-33.	0.9	4
98	Comparator-Controlled Rectification at Monolithic Buck Converters for Higher Input Voltages. IEEE Transactions on Power Electronics, 2012, 27, 628-631.	5.4	4
99	Ultra-high bandwidth fully-differential three-stage operational amplifiers in 40nm digital CMOS. , 2013, , .		4
100	A 40 nm LP CMOS self-biased continuous-time comparator with sub-100ps delay at 1.1V &#x0026;#x0026; 1.2mW. , 2013, , .		4
101	Highly sensitive 2 Gb/s optoreceiver with CMOS compatible avalanche photodiode. , 2014, , .		4
102	pn photodiode in 0.35 â€¼ m high-voltage CMOS with 1.2-GHz bandwidth. Optical Engineering, 2014, 53, 116114.	0.5	4
103	Monolithically integrated optical random pulse generator in high voltage CMOS technology. , 2015, , .		4
104	Improvement of CMOS-Integrated Vertical APDs by Applying Lateral Well Modulation. IEEE Photonics Technology Letters, 2015, 27, 1907-1910.	1.3	4
105	A DC-to-8.5 GHz 32â€‰%â€‰1 Analog Multiplexer for On-Chip Continuous-Time Probing of Single-Event Transients in a 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 377-381.	2.2	4
106	Optical wireless monolithically integrated receiver with large-area APD and dc current rejection. , 2017, , .		4
107	Experimental Investigation of the Joint Influence of Reduced Supply Voltage and Charge Sharing on Single-Event Transient Waveforms in 65-nm Triple-Well CMOS. IEEE Transactions on Nuclear Science, 2018, 65, 1908-1913.	1.2	4
108	Area and Power Efficient 3â€‰“8.8-GHz IR-UWB Transmitter With Spectrum Tunability. IEEE Microwave and Wireless Components Letters, 2020, 30, 39-42.	2.0	4

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109	Noise and Breakdown Characterization of SPAD Detectors with Time-Gated Photon-Counting Operation. <i>Sensors</i> , 2021, 21, 5287.	2.1	4
110	Optimized silicon CMOS reach-through avalanche photodiode with 2.3-GHz bandwidth. <i>Optical Engineering</i> , 2017, 56, 1.	0.5	4
111	Investigation of optical interconnect receivers in standard micron and submicron MOS technology. <i>Optical Engineering</i> , 2003, 42, 773.	0.5	3
112	Time-of-flight based pixel architecture with integrated double-cathode photodetector. , 2007, , .		3
113	Fast transimpedance switching burst-mode CMOS optical receiver. <i>International Journal of Circuit Theory and Applications</i> , 2007, 35, 355-370.	1.3	3
114	Low-Voltage Low-Power Highly Linear Down-Sampling Mixer in 65nm Digital CMOS Technology. , 2008, , .		3
115	Input adapter based BiCMOS operational amplifier with rail-to-rail capability. , 2008, , .		3
116	A current-mode continuous-time filter for software defined radio solutions. <i>Analog Integrated Circuits and Signal Processing</i> , 2009, 58, 27-33.	0.9	3
117	Basics of Optical Emission and Absorption. <i>Springer Series in Optical Sciences</i> , 2009, , 1-9.	0.5	3
118	HELIOS: photonics electronics functional integration on CMOS. <i>Proceedings of SPIE</i> , 2010, , .	0.8	3
119	Rail-to-rail BiCMOS operational amplifier using input signal adapters with floating outputs. <i>Analog Integrated Circuits and Signal Processing</i> , 2010, 63, 433-449.	0.9	3
120	A low-voltage complementary metal-oxide semiconductor adapter circuit suitable for input rail-to-rail operation. <i>International Journal of Electronics</i> , 2010, 97, 1283-1309.	0.9	3
121	Integrated phototransistors in a CMOS process for optoelectronic integrated circuits. , 2010, , .		3
122	A BJT translinear loop based optoelectronic integrated circuit with variable transimpedance for optical storage systems. <i>Analog Integrated Circuits and Signal Processing</i> , 2011, 66, 293-298.	0.9	3
123	Charging multiple batteries using the boost-flyback converter. , 2012, , .		3
124	Analytical analysis of a p-n junction with arbitrary shaped doping profile. , 2012, , .		3
125	Time-Of-Flight range finding sensor using an integrated PNP PIN Phototransistor in 180 nm CMOS. , 2012, , .		3
126	Photovoltaic energy harvesting for hybrid/electric vehicles: Topology comparison and optimisation of a discrete power stage for European Efficiency. , 2012, , .		3



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127	Correction of the temperature induced error of the illumination source in a time-of-flight distance measurement setup. , 2013, , .		3
128	Correction of a phase dependent error in a time-of-flight range sensor. , 2013, , .		3
129	Avalanche photodiode with high responsivity in 0.35 $\mu\text{m}$ CMOS. Optical Engineering, 2014, 53, 043105.	0.5	3
130	Vertical triple-junction RGB optical sensor with signal processing based on the determination of the space-charge region borders. Optics Letters, 2014, 39, 5042.	1.7	3
131	Monolithically integrated avalanche photodiode receiver in 0.35 $\mu\text{m}$ bipolar complementary metal oxide semiconductor. Optical Engineering, 2015, 54, 110502.	0.5	3
132	10 Gb/s 4-PAM Ring Modulator Driver. , 2015, , .		3
133	On Optimal Latin Hypercube Design for Yield Analysis of Analog Circuits. , 2015, , .		3
134	A Nonlinear Average-Current-Controlled Multiphase Boost Converter With Monolithically Integrated Control and Low-Side Power Switches in 0.35- $\mu\text{m}$ HV CMOS for the Automotive Sector. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2015, 3, 405-421.	3.7	3
135	A new sampling technique for Monte Carlo-based statistical circuit analysis. , 2017, , .		3
136	Laser Diode Current Driver With $(1-t/T)^{-1}$ Time Dependence in 0.35- $\mu\text{m}$ BiCMOS Technology for Quantum Random Number Generators. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 510-514.	2.2	3
137	A Cascaded Thermometer-Coded Current-Steering Digital-to-Analog Converter. , 2018, , .		3
138	A 54.2-dB Current Gain Dynamic Range, 1.78-GHz Gain-Bandwidth Product CMOS VCCA <sup>2</sup> . IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 46-50.	2.2	3
139	Quadruple voltage mixed quenching and active resetting circuit in 150 nm CMOS for an external SPAD. , 2020, , .		3
140	High Slew-Rate Quadruple-Voltage Mixed-Quenching Active-Resetting Circuit for SPADs in 0.35- $\mu\text{m}$ CMOS for Increasing PDP. IEEE Solid-State Circuits Letters, 2021, 4, 18-21.	1.3	3
141	Determination of the excess noise of avalanche photodiodes integrated in 0.35- $\mu\text{m}$ CMOS technologies. Optical Engineering, 2018, 57, 1.	0.5	3
142	Avalanche Transients of Thick 0.35 $\mu\text{m}$ CMOS Single-Photon Avalanche Diodes. Micromachines, 2020, 11, 869.	1.4	3
143	Transconductance Boosting Technique for Bandwidth Extension in Low-Voltage and Low-Noise Optical TIAs. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 834-838.	2.2	3
144	Comprehensive Modeling of Photon Detection Probability in CMOS-based SPADs. , 2020, , .		3

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145	Cascoded Active Quencher for SPADs With Bipolar Differential Amplifier in 0.35 $\mu$ m BiCMOS. IEEE Photonics Journal, 2022, 14, 1-8.	1.0	3
146	Optical receivers with large-diameter photodiode. , 2006, 6183, 315.		2
147	Integrated optical receiver for lens-less free-space communication. , 2008, , .		2
148	BiCMOS phototransistors. Proceedings of SPIE, 2008, , .	0.8	2
149	High-speed PNP PIN phototransistors in a 0.18 $\mu$ m CMOS process. , 2011, , .		2
150	Sunlight-proof optical distance measurements with a dual-line lock-in time-of-flight sensor. Analog Integrated Circuits and Signal Processing, 2011, 68, 59-68.	0.9	2
151	A 33 $\mu$ m <sup>2</sup> low-power range finder. , 2012, , .		2
152	Phototransistor based Time-of-Flight range finding sensor in an 180 nm CMOS process. , 2012, , .		2
153	Passive mixer with OPA filter for DVB-H front-end in 65nm digital CMOS technology. Microelectronics Journal, 2012, 43, 975-979.	1.1	2
154	Analyses of single-stage complementary self-biased CMOS differential amplifiers. , 2012, , .		2
155	Optoelectronic integrated circuit for indoor optical wireless communication with adjustable beam. , 2013, , .		2
156	Ultra-thin multi-axial shear stress sensor based on a segmented photodiode. , 2013, , .		2
157	BiCMOS-integrated photodiode exploiting drift enhancement. Optical Engineering, 2014, 53, 087103.	0.5	2
158	CMOS integrated MPP tracker with analog power measurement at the PV converter input. Analog Integrated Circuits and Signal Processing, 2014, 79, 385-393.	0.9	2
159	Optical wireless receiver circuit with integrated APD and high background-light immunity. , 2015, , .		2
160	Synchronous OEIC Integrating Receiver for Optically Reconfigurable Gate Arrays. Sensors, 2016, 16, 761.	2.1	2
161	Influence of On-Off Keying Duty Cycle on BER in Wireless Optical Communication Up to 75 Mbit/s Using an SPAD and a RC LED. , 2018, , .		2
162	PIN-photodiode based active pixel in 0.35 $\mu$ m high-voltage CMOS for optical coherence tomography. , 2019, , .		2

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163	Highly Sensitive Indirect Time-of-Flight Distance Sensor With Integrated Single-Photon Avalanche Diode in 0.35 $\mu\text{m}$ CMOS. IEEE Photonics Journal, 2022, 14, 1-6.	1.0	2
164	Single-chip optoelectronic rangefinder using PIN-photodiode and correlated active integration. , 2005, , .		1
165	PIN-photodiode based pixel architecture for high-speed optical distance measurement systems. , 2005, , .		1
166	Monolithische Korrelationsempfänger für optische Abstandsmessung (Integrated Correlation) Tj ETQq0 0 0 rgBT/Overlock 10 Tf 50 6	0.3	1
167	A 2.5-Gb/s Receiver PDIC With Low-Noise Integrated Charge Pump. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2006, 53, 1308-1312.	2.3	1
168	A SiGe optical receiver with large-area photodiode. , 2007, , .		1
169	A 3 <sup>rd</sup> -order current-mode continuous-time filter in 65 nm CMOS. , 2007, , .		1
170	High-Gain Double-Bulk Mixer in 65 nm CMOS with 830 $\mu\text{W}$ Power Consumption. ETRI Journal, 2010, 32, 457-459.	1.2	1
171	Segmented Photo-Detection for a MIMO Multi-Mode Fiber Transmission System. Journal of Optical Communications, 2010, 31, .	4.0	1
172	A mixer-filter combination of a direct conversion receiver for DVB-H applications in 65nm CMOS. , 2010, , .		1
173	Integrated 180 nm CMOS phototransistors with an optimized responsivity-bandwidth-product. , 2012, , .		1
174	A fully complementary and fully differential self-biased asynchronous CMOS comparator. , 2012, , .		1
175	A 10Gb/s inductorless push pull current mirror transimpedance amplifier. , 2012, , .		1
176	Complementary edge alignment and digital output signal speed-up CMOS positive feedback latches. , 2012, , .		1
177	Double-Gilbert mixer with enhanced linearity in 65 $\text{nm}$ low-power CMOS technology. Analog Integrated Circuits and Signal Processing, 2012, 71, 313-317.	0.9	1
178	On frequency response and stability of an optical front-end with variable-gain current amplifier using a bipolar junction transistor translinear loop. International Journal of Circuit Theory and Applications, 2013, 41, 792-817.	1.3	1
179	CMOS chip with multi junction photo detector for sensing biomedical signals. , 2013, , .		1
180	A fully differential CMOS self-biased two-stage preamplifier-latch threshold detection comparator. , 2013, , .		1

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181	A monolithically integrated silicon modulator with a 10 Gb/s 5 Vpp or 5.6 Vpp driver in 0.25 $\mu\text{m}$ SiGe:C BiCMOS. <i>Frontiers in Physics</i> , 2014, 2, .	1.0	1
182	Current-Mode Filters. <i>Springer Series in Advanced Microelectronics</i> , 2014, , 67-117.	0.3	1
183	A 10 Gb/s 0.25 $\mu\text{m}$ SiGe modulator driver for photonic-integration. <i>Analog Integrated Circuits and Signal Processing</i> , 2014, 79, 15-25.	0.9	1
184	A processing approach for a correlating time-of-flight range sensor based on a least squares method. , 2014, , .		1
185	Corrections to "Optical Wireless Communication With Adaptive Focus and MEMS-Based Beam Steering" [Aug 1 2013 1428-1431]. <i>IEEE Photonics Technology Letters</i> , 2014, 26, 2411-2411.	1.3	1
186	45-channel monolithically integrated, high-temperature capable optical receiver with a total data rate of 140 Gbit / s. <i>Optical Engineering</i> , 2015, 54, 067111.	0.5	1
187	Dependence of inverter chain single-event cross sections on inverter spacing in 65 nm bulk CMOS technology. , 2016, , .		1
188	Transimpedance Amplifiers. <i>Springer Series in Advanced Microelectronics</i> , 2016, , 105-161.	0.3	1
189	An integrated current sensing circuit with comparator function for a buck DC-DC converter in HV-CMOS. , 2016, , .		1
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