

Thomas Chen

List of Publications by Year in descending order

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8
papers

144
citations

2258059

3
h-index

2550090

3
g-index

8
all docs

8
docs citations

8
times ranked

209
citing authors

#	ARTICLE	IF	CITATIONS
1	A Sparse Coding Neural Network ASIC With On-Chip Learning for Feature Extraction and Encoding. IEEE Journal of Solid-State Circuits, 2015, 50, 1070-1079.	5.4	62
2	A 640M pixel/s 3.65mW sparse event-driven neuromorphic object recognition processor with on-chip learning. , 2015, , .		41
3	LEIA: A 2.05mm ² 140mW lattice encryption instruction accelerator in 40nm CMOS. , 2018, , .		19
4	A 1.87-mm ² 56.9-GOPS Accelerator for Solving Partial Differential Equations. IEEE Journal of Solid-State Circuits, 2020, 55, 1709-1718.	5.4	11
5	A Maximum-Likelihood Sequence Detection Powered ADC-Based Serial Link. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 2269-2278.	5.4	9
6	A 127mW 1.63TOPS sparse spatio-temporal cognitive SoC for action classification and motion tracking in videos. , 2017, , .		2
7	High-performance spiking neural net accelerators for embedded computer vision applications. , 2017, , .		0
8	A 135-mW 1.70TOPS Sparse Video Sequence Inference SoC for Action Classification. IEEE Journal of Solid-State Circuits, 2019, 54, 2081-2090.	5.4	0