

Hyunseok Choi

List of Publications by Year in descending order

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7
papers

23
citations

2682572

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2917675

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7
docs citations

7
times ranked

31
citing authors

#	ARTICLE	IF	CITATIONS
1	A Digital Polar CMOS Power Amplifier With a 102-dB Power Dynamic Range Using a Digitally Controlled Bias Generator. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 579-589.	4.6	17
2	A Triple-Power-Mode Digital Polar CMOS RF Power Amplifier With LO Duty Cycle Control. IEEE Microwave and Wireless Components Letters, 2016, 26, 702-704.	3.2	6
3	A direct down - Converted band pass delta sigma receiver with frequency translating and RF DAC. , 2011, , .		0
4	Digitally controlled envelope modulator for a polar transmitter with low code-AM distortion. , 2011, , .		0
5	A direct down converted low-jitter band pass delta sigma receiver with frequency translating technique and sinusoidal RF DAC. , 2012, , .		0
6	A comparator with reduced regeneration time for continuous-time delta-sigma modulator. , 2013, , .		0
7	Digital polar CMOS power amplifier researches in KAIST. , 2015, , .		0