Won Namgoong

List of Publications by Year
in descending order

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Version: 2024-02-01

26	246	1162889	940416
papers	citations	8	16
		h-index	g-index

26	26	26	231
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	A Non-Iterative Technique for Phase Noise ICI Mitigation in Packet-Based OFDM Systems. IEEE Transactions on Signal Processing, 2010, 58, 5945-5950.	3.2	62
2	On the Performance of OFDM-Based Amplify-and-Forward Relay Networks in the Presence of Phase Noise. IEEE Transactions on Communications, 2011, 59, 1458-1466.	4.9	32
3	Comparator Power Minimization Analysis for SAR ADC Using Multiple Comparators. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2369-2379.	3.5	25
4	Observer-Controller Digital PLL. IEEE Transactions on Circuits and Systems I: Regular Papers, 2010, 57, 631-641.	3.5	18
5	Comparator Power Reduction in Low-Frequency SAR ADC Using Optimized Vote Allocation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 2384-2394.	2.1	15
6	Digital Equalization of a Polyphase Harmonic Mixer. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 467-471.	2.2	11
7	A Wideband Digital Receiver With Hard-Switching Mixers for Cognitive Radio. IEEE Journal on Emerging and Selected Topics in Circuits and Systems, 2013, 3, 576-585.	2.7	11
8	Adaptive and Robust Digital Harmonic-Reject Mixer With Optimized Local Oscillator Spacing. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 580-589.	3.5	9
9	Sub-Nyquist Sampling Receiver for Overlay Cognitive Radio Users. IEEE Transactions on Signal Processing, 2018, 66, 4160-4169.	3.2	8
10	Frequency domain joint channel and phase noise estimation in OFDM WLAN systems. , 2008, , .		7
11	Flicker Noise in Observer-Controller Digital PLL. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 556-560.	2.2	7
12	A Dual-Path 4-Phase Nonuniform Wideband Receiver With Digital MMSE Harmonic Rejection Equalizer. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 386-395.	2.9	6
13	A Modified Proportionalâ€“Integral Loop Filter to Suppress DCO Noise in Digital PLL. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 974-978.	2.2	6
14	Spectral Detection of Frequency-Sparse Signals: Compressed Sensing vs. Sweeping Spectrum Scanning. IEEE Access, 2021, 9, 30060-30070.	2.6	6
15	An All-Digital Approach to Supply Noise Cancellation in Digital Phase-Locked Loop. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1025-1035.	2.1	5
16	Interferer-Robust Compressed Sensing Receiver Based on Mixer Harmonics. , 2019, , .		5
17	Digital Cancellation of Harmonic and Intermodulation Distortion in Wideband SAW-Less Receivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1554-1558.	2.2	4
18	Matching for Concurrent Harmonic Sensing in an ${M}$ -Phase Mixer-First Receiver. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 1017-1021.	2.2	3

#	Article	IF	Citations
19	High-Speed and Low-Power UWB Radio System Design. Conference Record of the Asilomar Conference on Signals, Systems and Computers, 2007, , .	0.0	2
20	Pilot design for OFDM systems in the presence of phase noise. , 2010, , .		2
21	Timing Jitter Distribution and Power Spectral Density of a Second-Order Bangâ€“Bang Digital PLL With Transport Delay Using Fokkerâ€“Planck Equations. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 398-406.	2.1	1
22	Joint Sparse Support Recovery for Asynchronous Multicarrier Modulation Signals in Cognitive Radio Networks. , 2022, , .		1
23	A charge sharing based harmonic-reject mixer for RF applications. , 2013, , .		0
24	Modeling and compensation of antenna RF switching non-idealities in OFDM receivers. , 2014, , .		0
25	An observer-controller digital PLL â€” A time-domain approach. , 2016, , .		0
26	Nonlinear analysis of bang-bang digital PLL with accumulative noise using Markov Chains. , 2016, , .		0