

# Shixuan Zheng

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/8613428/publications.pdf>

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#	ARTICLE	IF	CITATIONS
1	Atomic Dataflow based Graph-Level Workload Orchestration for Scalable DNN Accelerators. , 2022, , .		9
2	Efficient Scheduling of Irregular Network Structures on CNN Accelerators. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 3408-3419.	2.7	16
3	An Ultra-Low Power Binarized Convolutional Neural Network-Based Speech Recognition Processor With On-Chip Self-Learning. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 4648-4661.	5.4	34
4	A High Energy Efficient Reconfigurable Hybrid Neural Network Processor for Deep Learning Applications. IEEE Journal of Solid-State Circuits, 2018, 53, 968-982.	5.4	163