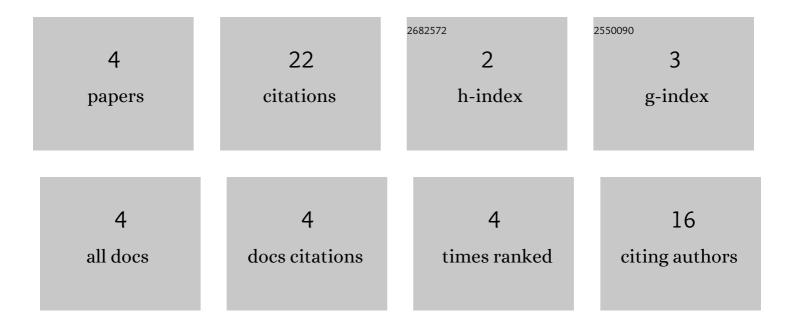
## VEJayanthi

## List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/8594362/publications.pdf

Version: 2024-02-01



#	Article	IF	CITATIONS
1	Performance analysis for geometrical attack on digital image watermarking. International Journal of Electronics, 2011, 98, 1565-1580.	1.4	12
2	High performance VLSI architecture for block based visible image watermarking. International Journal of Electronics, 2012, 99, 1191-1206.	1.4	6
3	Image encryption using separable reversible data hiding scheme. , 2013, , .		3
4	Architecture Design and Field Programmable Gate Array Implementation of Removable Visible Image Watermarking for Digital Camera Images Using a Private Key. Journal of Imaging Science and Technology, 2014, 58, 605021-6050210.	0.5	1