Rui Martins

List of Publications by Year
in descending order

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444	7,586	71061	114418
papers	citations	41 h-index	63 g-index

455	455	455	4448
all docs	docs citations	times ranked	citing authors

#	Article	IF	Citations
1	A 10-bit 100-MS/s Reference-Free SAR ADC in 90 nm CMOS. IEEE Journal of Solid-State Circuits, 2010, 45, 1111-1121.	3.5	571
2	A 0.016-mm^{2} 144-muW Three-Stage Amplifier Capable of Driving 1-to-15 nF Capacitive Load With $> $0.95-MHz GBW. IEEE Journal of Solid-State Circuits, 2013, 48, 527-540.	3.5	126
3	Transceiver architecture selection: Review, state-of-the-art survey and case study. IEEE Circuits and Systems Magazine, 2007, 7, 6-25.	2.6	121
4	A Fully Integrated Digital LDO With Coarseâ€“Fine-Tuning and Burst-Mode Operation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 683-687.	2.2	116
5	A 0.83-<formula formulatype="inline"><tex Notation="TeX">$mu {m W}$</tex></formula> QRS Detection Processor Using Quadratic Spline Wavelet Transform for Wireless ECG Acquisition in 0.35-<formula formulatype="inline"> <tex Notation="TeX">$mu{m m}$</tex></formula> CMOS. IEEE Transactions on Biomedical Circuits and Systems, 2012, 6, 586-595.	2.7	112
6	A Wide Input Range Dual-Path CMOS Rectifier for RF Energy Harvesting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2017, 64, 166-170.	2.2	111
7	An Analog-Assisted Tri-Loop Digital Low-Dropout Regulator. IEEE Journal of Solid-State Circuits, 2018, 53, 20-34.	3.5	88
8	A Single-Chip Solar Energy Harvesting IC Using Integrated Photodiodes for Biomedical Implant Applications. IEEE Transactions on Biomedical Circuits and Systems, 2017, 11, 44-53.	2.7	83
9	An 8-b 400-MS/s 2-b-Per-Cycle SAR ADC With Resistive DAC. IEEE Journal of Solid-State Circuits, 2012, 47, 2763-2772.	3.5	78
10	Fully Integrated Inductor-Less Flipping-Capacitor Rectifier for Piezoelectric Energy Harvesting. IEEE Journal of Solid-State Circuits, 2017, 52, 3168-3180.	3.5	77
11	A Single-Stage Inductive-Power-Transfer Converter for Constant-Power and Maximum-Efficiency Battery Charging. IEEE Transactions on Power Electronics, 2020, 35, 8973-8984.	5.4	77
12	A 73.9%-Efficiency CMOS Rectifier Using a Lower DC Feeding (LDCF) Self-Body-Biasing Technique for Far-Field RF Energy-Harvesting Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 992-1002.	3.5	76
13	Drug screening of cancer cell lines and human primary tumors using droplet microfluidics. Scientific Reports, 2017, 7, 9109.	1.6	69
14	A 0.096-mm$^{2}~1$ â€“20-GHz Triple-Path Noise- Canceling Common-Gate Common-Source LNA With Dual Complementary pMOSâ€“nMOS Configuration. IEEE Transactions on Microwave Theory and Techniques, 2020, 68, 144-159.	2.9	64
15	15-nW Biopotential LPFs in 0.35-<formula formulatype="inline"> <tex Notation="TeX">$mu{m m}$</tex></formula> CMOS Using Subthreshold-Source-Follower Biquads With and Without Gain Compensation. IEEE Transactions on Biomedical Circuits and Systems, 2013, 7, 690-702.	2.7	63
16	Nested-Current-Mirror Rail-to-Rail-Output Single-Stage Amplifier With Enhancements of DC Gain, GBW and Slew Rate. IEEE Journal of Solid-State Circuits, 2015, 50, 2353-2366.	3.5	63
17	A Reconfigurable Bidirectional Wireless Power Transceiver for Battery-to-Battery Wireless Charging. IEEE Transactions on Power Electronics, 2019, 34, 7745-7753.	5.4	63
18	Double recycling technique for folded-cascode OTA. Analog Integrated Circuits and Signal Processing, 2012, 71, 137-141.	0.9	59

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19	A digital microfluidic system for loop-mediated isothermal amplification and sequence specific pathogen detection. Scientific Reports, 2017, 7, 14586.	1.6	56
20	A 0.083-mm² 25.2-to-29.5 GHz Multi-LC-Tank Class-F₂₃₄ VCO With a 189.6-dBc/Hz FOM. IEEE Solid-State Circuits Letters, 2018, 1, 86-89.	1.3	56
21	20.4 An output-capacitor-free analog-assisted digital low-dropout regulator with tri-loop control. , 2017, , .		55
22	A Two-Way Interleaved 7-b 2.4-GS/s 1-Then-2 b/Cycle SAR ADC With Background Offset Calibration. IEEE Journal of Solid-State Circuits, 2018, 53, 850-860.	3.5	55
23	An intelligent digital microfluidic system with fuzzy-enhanced feedback for multi-droplet manipulation. Lab on A Chip, 2013, 13, 443-451.	3.1	54
24	Split-SAR ADCs: Improved Linearity With Power and Speed Optimization. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2014, 22, 372-383.	2.1	53
25	A 0.044-mm² 0.5-to-7-GHz Resistor-Plus-Source-Follower-Feedback Noise-Cancelling LNA Achieving a Flat NF of 3.3Â±0.45 dB. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 71-75.	2.2	52
26	A 2.4 GHz ZigBee Receiver Exploiting an RF-to-BB-Current-Reuse Blixer + Hybrid Filter Topology in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2014, 49, 1333-1344.	3.5	51
27	A SAW-Less Tunable RF Front End for FDD and IBFD Combining an Electrical-Balance Duplexer and a Switched-<italic>LC</italic> N-Path LNA. IEEE Journal of Solid-State Circuits, 2018, 53, 1431-1442.	3.5	51
28	Low-Phase-Noise Wideband Mode-Switching Quad-Core-Coupled mm-wave VCO Using a Single-Center-Tapped Switched Inductor. IEEE Journal of Solid-State Circuits, 2018, 53, 3232-3242.	3.5	51
29	A 0.46-mm$ ^{2}$ 4-dB NF Unified Receiver Front-End for Full-Band Mobile TV in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2011, 46, 1970-1984.	3.5	50
30	Analysis and Modeling of a Gain-Boosted N-Path Switched-Capacitor Bandpass Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 2560-2568.	3.5	50
31	A Handheld High-Sensitivity Micro-NMR CMOS Platform With B-Field Stabilization for Multi-Type Biological/Chemical Assays. IEEE Journal of Solid-State Circuits, 2017, 52, 284-297.	3.5	50
32	A 0.18-V 382-<inline-formula> <tex-math notation="LaTeX">mu </tex-math> </inline-formula>W Bluetooth Low-Energy Receiver Front-End With 1.33-nW Sleep Power for Energy-Harvesting Applications in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 1618-1627.	3.5	50
33	Limit Cycle Oscillation Reduction for Digital Low Dropout Regulators. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 903-907.	2.2	49
34	On the Design of a Programmable-Gain Amplifier With Built-In Compact DC-Offset Cancellers for Very Low-Voltage WLAN Systems. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 496-509.	3.5	48
35	A Sub-GHz Multi-ISM-Band ZigBee Receiver Using Function-Reuse and Gain-Boosted N-Path Techniques for IoT Applications. IEEE Journal of Solid-State Circuits, 2014, 49, 2990-3004.	3.5	48
36	Cell-based drug screening on microfluidics. TrAC - Trends in Analytical Chemistry, 2019, 117, 231-241.	5.8	48

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37	A Fully Integrated FVF LDO With Enhanced Full-Spectrum Power Supply Rejection. IEEE Transactions on Power Electronics, 2021, 36, 4326-4337.	5.4	48
38	A digital microfluidic system with 3D microstructures for single-cell culture. Microsystems and Nanoengineering, 2020, 6, 6.	3.4	47
39	A 0.032-mm² 0.15-V Three-Stage Charge-Pump Scheme Using a Differential Bootstrapped Ring-VCO for Energy-Harvesting Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 146-150.	2.2	46
40	Nano-Ampere Low-Dropout Regulator Designs for IoT Devices. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 4017-4026.	3.5	46
41	A 550-mu W 20-kHz BW 100.8-dB SNDR Linear- Exponential Multi-Bit Incremental $SigmaDelta$ ADC With 256 Clock Cycles in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 1161-1172.	3.5	45
42	IIR switched-capacitor decimator building blocks with optimum implementation. IEEE Transactions on Circuits and Systems, 1990, 37, 81-90.	0.9	43
43	A Temperature-Stabilized Single-Channel 1-GS/s 60-dB SNDR SAR-Assisted Pipelined ADC With Dynamic Gm-R-Based Amplifier. IEEE Journal of Solid-State Circuits, 2020, 55, 322-332.	3.5	43
44	An Inverse-Class-F CMOS Oscillator With Intrinsic-High-Q First Harmonic and Second Harmonic Resonances. IEEE Journal of Solid-State Circuits, 2018, 53, 3528-3539.	3.5	42
45	LampPort: a handheld digital microfluidic device for loop-mediated isothermal amplification (LAMP). Biomedical Microdevices, 2019, 21, 9.	1.4	42
46	A 0.2-V Energy-Harvesting BLE Transmitter With a Micropower Manager Achieving 25% System Efficiency at 0-dBm Output and 5.2-nW Sleep Power in 28-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 1351-1362.	3.5	42
47	An RF-to-BB-Current-Reuse Wideband Receiver With Parallel N-Path Active/Passive Mixers and a Single-MOS Pole-Zero LPF. IEEE Journal of Solid-State Circuits, 2014, 49, 2547-2559.	3.5	41
48	A 1.6-GS/s 12.2-mW Seven-/Eight-Way Split Time-Interleaved SAR ADC Achieving 54.2-dB SNDR With Digital Background Timing Mismatch Calibration. IEEE Journal of Solid-State Circuits, 2020, 55, 693-705.	3.5	41
49	A reconfigurable low-noise dynamic comparator with offset calibration in 90nm CMOS. , 2011, , .		40
50	60-dB SNDR 100-MS/s SAR ADCs With Threshold Reconfigurable Reference Error Calibration. IEEE Journal of Solid-State Circuits, 2017, 52, 2576-2588.	3.5	40
51	A 4.2-mW 77.1-dB SNDR 5-MHz BW DT 2-1 MASH $Delta Sigma $ Modulator With Multirate Opamp Sharing. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 2641-2654.	3.5	39
52	A 76.6-dB-SNDR 50-MHz-BW 29.2-mW Multi-Bit CT Sturdy MASH With DAC Non-Linearity Tolerance. IEEE Journal of Solid-State Circuits, 2020, 55, 344-355.	3.5	38
53	A palm-size Î¼NMR relaxometer using a digital microfluidic device and a semiconductor transceiver for chemical/biological diagnosis. Analyst, The, 2015, 140, 5129-5137.	1.7	37
54	Wideband Receivers: Design Challenges, Tradeoffs and State-of-the-Art. IEEE Circuits and Systems Magazine, 2015, 15, 12-24.	2.6	36

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55	A 2- 45-nV/â^šHz Readout Front End With Multiple-Chopping Active-High-Pass Ripple Reduction Loop and Pseudofeedback DC Servo Loop. IEEE Transactions on Circuits and Systems II: Express Briefs, 2016, 63, 351-355.	2.2	36
56	Micro- and nanofabrication NMR technologies for point-of-care medical applications â€“ A review. Microelectronic Engineering, 2019, 209, 66-74.	1.1	36
57	20.4 A 123-phase DC-DC converter-ring with fast-DVS for microprocessors. , 2015, , .		35
58	A Dual-Output Wireless Power Transfer System With Active Rectifier and Three-Level Operation. IEEE Transactions on Power Electronics, 2017, 32, 927-930.	5.4	35
59	A 2.3 mW 10-bit 170 MS/s Two-Step Binary-Search Assisted Time-Interleaved SAR ADC. IEEE Journal of Solid-State Circuits, 2013, 48, 1783-1794.	3.5	34
60	A 0.4V 430nA quiescent current NMOS digital LDO with NAND-based analog-assisted loop in 28nm CMOS. , 2018, , .		34
61	On the droplet velocity and electrode lifetime of digital microfluidics: voltage actuation techniques and comparison. Microfluidics and Nanofluidics, 2015, 18, 673-683.	1.0	33
62	A 3D microblade structure for precise and parallel droplet splitting on digital microfluidic chips. Lab on A Chip, 2017, 17, 896-904.	3.1	33
63	A 3.3-mW 25.2-to-29.4-GHz Current-Reuse VCO Using a Single-Turn Multi-Tap Inductor and Differential-Only Switched-Capacitor Arrays With a 187.6-dBc/Hz FOM. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3704-3717.	3.5	33
64	A 6 b 5 GS/s 4 Interleaved 3 b/Cycle SAR ADC. IEEE Journal of Solid-State Circuits, 2016, 51, 365-377.	3.5	32
65	A Reconfigurable Cross-Connected Wireless-Power Transceiver for Bidirectional Device-to-Device Wireless Charging. IEEE Journal of Solid-State Circuits, 2019, 54, 2579-2589.	3.5	32
66	Algebraic Series-Parallel-Based Switched-Capacitor DCâ€“DC Boost Converter With Wide Input Voltage Range and Enhanced Power Density. IEEE Journal of Solid-State Circuits, 2019, 54, 3118-3134.	3.5	32
67	A 0.6-V 13-bit 20-MS/s Two-Step TDC-Assisted SAR ADC With PVT Tracking and Speed-Enhanced Techniques. IEEE Journal of Solid-State Circuits, 2019, 54, 3396-3409.	3.5	32
68	A 470-nA Quiescent Current and 92.7%/94.7% Efficiency DCT/PWM Control Buck Converter With Seamless Mode Selection for IoT Application. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4085-4098.	3.5	31
69	A 312 ps responseâ€time LDO with enhanced super source follower in 28Ânm CMOS. Electronics Letters, 2016, 52, 1368-1370.	0.5	30
70	An 11b 450 MS/s Three-Way Time-Interleaved Subranging Pipelined-SAR ADC in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2016, 51, 1223-1234.	3.5	30
71	An 8-Bit 10-GS/s 16Ã— Interpolation-Based Time-Domain ADC With <1.5-ps Uncalibrated Quantization Steps. IEEE Journal of Solid-State Circuits, 2020, 55, 3225-3235.	3.5	30
72	Design of an ESD-Protected Ultra-Wideband LNA in Nanoscale CMOS for Full-Band Mobile TV Tuners. IEEE Transactions on Circuits and Systems I: Regular Papers, 2009, 56, 933-942.	3.5	29

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73	High-/Mixed-Voltage RF and Analog CMOS Circuits Come of Age. IEEE Circuits and Systems Magazine, 2010, 10, 27-39.	2.6	29
74	Two Stage Operational Amplifiers: Power and Area Efficient Frequency Compensation for Driving a Wide Range of Capacitive Load. IEEE Circuits and Systems Magazine, 2011, 11, 26-42.	2.6	29
75	A 0.14-${hbox {mm}}^{2}$ 1.4-mW 59.4-dB-SFDR 2.4-GHz ZigBee/WPAN Receiver Exploiting a â€œSplit-LNTA + 50% LOâ€•Topology in 65-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2014, 62, 1525-1534.	2.9	29
76	Design and Experimental Verification of a Power Effective Flash-SAR Subranging ADC. IEEE Transactions on Circuits and Systems II: Express Briefs, 2010, 57, 607-611.	2.2	28
77	A review and design of the on-chip rectifiers for RF energy harvesting. , 2015, , .		28
78	An inverse-class-F CMOS VCO with intrinsic-high-Q 1st- and 2nd-harmonic resonances for 1/f²-to-1/f³ phase-noise suppression achieving 196.2dBc/Hz FOM. , 2018, , .		28
79	Portable NMR with Parallelism. Analytical Chemistry, 2020, 92, 2112-2120.	3.2	28
80	A 0.5-V Supply, 36 nW Bandgap Reference With 42 ppm/Â°C Average Temperature Coefficient Within â^'40 Â°C to 120 Â°C. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3656-3669.	3.5	28
81	Piezoelectric Energy-Harvesting Interface Using Split-Phase Flipping-Capacitor Rectifier With Capacitor Reuse for Input Power Adaptation. IEEE Journal of Solid-State Circuits, 2020, 55, 2106-2117.	3.5	28
82	A 50-fJ 10-b 160-MS/s Pipelined-SAR ADC Decoupled Flip-Around MDAC and Self-Embedded Offset Cancellation. IEEE Journal of Solid-State Circuits, 2012, 47, 2614-2626.	3.5	27
83	A 2-<formula formulatype="inline"><tex Notation="TeX">$mu{hbox{m}}$</tex></formula> InGaP/GaAs Class-J Power Amplifier for Multi-Band LTE Achieving 35.8-dB Gain, 40.5% to 55.8% PAE and 28-dBm Linear Output Power. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 200-209.	2.9	27
84	A 0.45 V 147â€“375 nW ECG Compression Processor With Wavelet Shrinkage and Adaptive Temporal Decimation Architectures. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 1307-1319.	2.1	27
85	A Wideband Inductorless dB-Linear Automatic Gain Control Amplifier Using a Single-Branch Negative Exponential Generator for Wireline Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3196-3206.	3.5	27
86	A 7-bit 2 GS/s Time-Interleaved SAR ADC With Timing Skew Calibration Based on Current Integrating Sampler. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 557-568.	3.5	27
87	An optimum CMOS switched-capacitor antialiasing decimating filter. IEEE Journal of Solid-State Circuits, 1993, 28, 962-970.	3.5	26
88	A 36-Gb/s 1.3-mW/Gb/s Duobinary-Signal Transmitter Exploiting Power-Efficient Cross-Quadrature Clocking Multiplexers With Maximized Timing Margin. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3014-3026.	3.5	25
89	A Sub-GHz Multi-ISM-Band ZigBee Receiver Using Function-Reuse and Gain-Boosted N-Path Techniques for IoT Applications. Analog Circuits and Signal Processing Series, 2016, , 81-103.	0.3	25
90	Statistical Spectra and Distortion Analysis of Time-Interleaved Sampling Bandwidth Mismatch. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 648-652.	2.2	24

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91	Histogram-Based Ratio Mismatch Calibration for Bridge-DAC in 12-bit 120 MS/s SAR ADC. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1203-1207.	2.1	24
92	A 12b 180MS/s 0.068mm² With Full-Calibration-Integrated Pipelined-SAR ADC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1684-1695.	3.5	24
93	An Area-Efficient and Tunable Bandwidth- Extension Technique for a Wideband CMOS Amplifier Handling 50+ Gb/s Signaling. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 4960-4975.	2.9	24
94	Algorithmic Voltage-Feed-In Topology for Fully Integrated Fine-Grained Rational Buckâ€“Boost Switched-Capacitor DCâ€“DC Converters. IEEE Journal of Solid-State Circuits, 2018, 53, 3455-3469.	3.5	24
95	Many-Objective Sizing Optimization of a Class-C/D VCO for Ultralow-Power IoT and Ultralow-Phase-Noise Cellular Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2019, 27, 69-82.	2.1	24
96	A Two-Phase Three-Level Buck Converter With Cross-Connected Flying Capacitors for Inductor Current Balancing. IEEE Transactions on Power Electronics, 2021, 36, 13855-13866.	5.4	24
97	A Time-Interleaved Ring-VCO with Reduced 1/ $ext {f}^{3}$ Phase Noise Corner, Extended Tuning Range and Inherent Divided Output. IEEE Journal of Solid-State Circuits, 2016, 51, 2979-2991.	3.5	23
98	Sub-7-second genotyping of single-nucleotide polymorphism by high-resolution melting curve analysis on a thermal digital microfluidic device. Lab on A Chip, 2016, 16, 743-752.	3.1	23
99	26.2 A 0.08mm2 25.5-to-29.9GHz Multi-Resonant-RLCM-Tank VCO Using a Single-Turn Multi-Tap Inductor and CM-Only Capacitors Achieving 191.6dBc/Hz FoM and 130kHz 1/f3 PN Corner. , 2019, , .		23
100	A Wide-PCE-Dynamic-Range CMOS Cross-Coupled Differential-Drive Rectifier for Ambient RF Energy Harvesting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 1743-1747.	2.2	23
101	Systematic analysis and cancellation of kickback noise in a dynamic latched comparator. Analog Integrated Circuits and Signal Processing, 2013, 77, 277-284.	0.9	22
102	Energy Optimized Subthreshold VLSI Logic Family With Unbalanced Pull-Up/Down Network and Inverse Narrow-Width Techniques. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 3119-3123.	2.1	22
103	A <inline-formula> <tex-math notation="LaTeX">$mu $ </tex-math> </inline-formula>NMR CMOS Transceiver Using a Butterfly-Coil Input for Integration With a Digital Microfluidic Device Inside a Portable Magnet. IEEE Journal of Solid-State Circuits, 2016, 51, 2274-2286.	3.5	22
104	A 2.4-GHz ZigBee Transmitter Using a Function-Reuse Class-F DCO-PA and an ADPLL Achieving 22.6% (14.5%) System Efficiency at 6-dBm (0-dBm) $P_{mathrm {out}}$. IEEE Journal of Solid-State Circuits, 2017, 52, 1495-1508.	3.5	22
105	CMOS Cross-Coupled Differential-Drive Rectifier in Subthreshold Operation for Ambient RF Energy Harvestingâ€”Model and Analysis. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1942-1946.	2.2	22
106	Design of a 4.2-to-5.1 GHz Ultralow-Power Complementary Class-B/C Hybrid-Mode VCO in 65-nm CMOS Fully Supported by EDA Tools. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3965-3977.	3.5	22
107	An FPGA-Based Energy-Efficient Reconfigurable Convolutional Neural Network Accelerator for Object Recognition Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 3143-3147.	2.2	22
108	A 0.35-V 5,200-Î¼m² 2.1-MHz Temperature-Resilient Relaxation Oscillator With 667 fJ/Cycle Energy Efficiency Using an Asymmetric Swing-Boosted RC Network and a Dual-Path Comparator. IEEE Journal of Solid-State Circuits, 2021, 56, 2701-2710.	3.5	22

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109	Cancer drug screening with an on-chip multi-drug dispenser in digital microfluidics. Lab on A Chip, 2021, 21, 4749-4759.	3.1	22
110	A 0.0045- <inline-formula> <tex-math notation="TeX">$hbox{mm}^{2}$</tex-math></inline-formula> 32.4- <inline-formula> <tex-math notation="TeX">$muhbox{W} $</tex-math></inline-formula> Two-Stage Amplifier for pF-to-nF Load Using CM Frequency Compensation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 246-250.	2.2	21
111	A 0.07 mm<formula formulatype="inline"><tex Notation="TeX">^{2}</tex></formula> 2.2 mW 10 GHz Current-Reuse Class-B/C Hybrid VCO Achieving 196-dBc/Hz FoM<formula formulatype="inline"><tex Notation="TeX">$_{{m A}}$</tex> </formula>. IEEE Microwave and Wireless Components Letters, 2015, 25, 457-459.	2.0	21
112	Thermal and Reference Noise Analysis of Time-Interleaving SAR and Partial-Interleaving Pipelined-SAR ADCs. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2196-2206.	3.5	21
113	22.4 A reconfigurable bidirectional wireless power transceiver with maximum-current charging mode and 58.6% battery-to-battery efficiency. , 2017, , .		21
114	16.4 A 5mW 7b 2.4GS/s 1-then-2b/cycle SAR ADC with background offset calibration. , 2017, , .		21
115	A 7.8-mW 5-b 5-GS/s Dual-Edges-Triggered Time-Based Flash ADC. IEEE Transactions on Circuits and Systems I: Regular Papers, 2017, 64, 1966-1976.	3.5	21
116	Passive Noise Shaping in SAR ADC With Improved Efficiency. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 416-420.	2.1	21
117	A Regulation-Free Sub-0.5-V 16-/24-MHz Crystal Oscillator With 14.2-nJ Startup Energy and 31.8-mu W Steady-State Power. IEEE Journal of Solid-State Circuits, 2018, 53, 2624-2635.	3.5	21
118	A 0.0056-mm² â^'249-dB-FoM All-Digital MDLL Using a Block-Sharing Offset-Free Frequency-Tracking Loop and Dual Multiplexed-Ring VCOs. IEEE Journal of Solid-State Circuits, 2019, 54, 88-98.	3.5	21
119	High-Performance Harmonic-Rich Single-Core VCO With Multi-LC Tank: A Tutorial. IEEE Transactions on Circuits and Systems II: Express Briefs, 2022, 69, 3115-3121.	2.2	21
120	MMIC active filter with tuned transversal element. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1998, 45, 632-634.	2.3	20
121	Activeâ€"Passive $Delta Sigma $ Modulator for High-Resolution and Low-Power Applications. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 364-374.	2.1	20
122	A 0.35-V 520- $mu ext{W}$ 2.4-GHz Current-Bleeding Mixer With Inductive-Gate and Forward-Body Bias, Achieving >13-dB Conversion Gain and >55-dB Port-to-Port Isolation. IEEE Transactions on Microwave Theory and Techniques, 2017, 65, 1284-1293.	2.9	20
123	A $6.5imes7,mu$ m² 0.98-to-1.5 mW Nonself-Oscillation-Mode Frequency Divider-by-2 Achieving a Single-Band Untuned Locking Range of 166.6% (4â€"44 GHz). IEEE Solid-State Circuits Letters, 2019, 2, 37-40.	1.3	20
124	Constant-Frequency and Noncommunication-Based Inductive Power Transfer Converter for Battery Charging. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2022, 10, 2147-2162.	3.7	20
125	An Analog-Proportional Digital-Integral Multiloop Digital LDO With PSR Improvement and LCO Reduction. IEEE Journal of Solid-State Circuits, 2020, , 1-14.	3.5	20
126	20.1 A 5.0-to-6.36GHz Wideband-Harmonic-Shaping VCO Achieving 196.9dBc/Hz Peak FoM and 90-to-180kHz 1/f³ PN Corner Without Harmonic Tuning. , 2021, , .		20

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127	A 3.15-mW +16.0-dBm IIP3 22-dB CG Inductively Source Degenerated Balun-LNA Mixer With Integrated Transformer-Based Gate Inductor and IM2 Injection Technique. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 700-713.	2.1	19
128	A 0.14-to-0.29-pJ/bit 14-GBaud/s Trimodal (NRZ/PAM-4/PAM-8) Half-Rate Bang-Bang Clock and Data Recovery (BBCDR) Circuit in 28-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 89-102.	3.5	19
129	A 90nm CMOS bio-potential signal readout front-end with improved powerline interference rejection. , 2009, , .		18
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