

# Chia-Hsiang Chen

## List of Publications by Year in descending order

Source: <https://exaly.com/author-pdf/8524183/publications.pdf>

Version: 2024-02-01

5  
papers

44  
citations

2258059

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2272923

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docs citations

5  
times ranked

77  
citing authors

#	ARTICLE	IF	CITATIONS
1	Characterization of Heavy-Ion-Induced Single-Event Effects in 65Ånm Bulk CMOS ASIC Test Chips. IEEE Transactions on Nuclear Science, 2014, 61, 2694-2701.	2.0	31
2	A 2.4-mm <sup>2</sup> 130-mW MMSE-Nonbinary LDPC Iterative Detector Decoder for 4 $\times$ 4 256-QAM MIMO in 65-nm CMOS. IEEE Journal of Solid-State Circuits, 2019, 54, 2070-2080.	5.4	5
3	An FPGA-Based Transient Error Simulator for Resilient Circuit and System Design and Evaluation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2015, 62, 471-475.	3.0	3
4	A 0.58-mm <sup>2</sup> 2.76-Gb/s 79.8-pJ/b 256-QAM Message-Passing Detector for a 128 $\times$ 32 Massive MIMO Uplink System. IEEE Journal of Solid-State Circuits, 2021, 56, 1722-1731.	5.4	3
5	A confidence-driven model for error-resilient computing. , 2011, , .		2