

Parit Kanjanavirojkul

List of Publications by Year in descending order

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7
papers

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3311381
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7
times ranked

5
citing authors

#	ARTICLE	IF	CITATIONS
1	A Consideration on LUT Linearization of Stochastic ADC in Sub-Ranging Architecture. , 2018, , .		0
2	Analysis and design of impulse signal generator based on current-mode excitation and transmission line resonator. Analog Integrated Circuits and Signal Processing, 2018, 97, 457-470.	1.4	0
3	CMOS-on-quartz pulse generator for low power applications. , 2017, , .		0
4	Impulse signal generator based on current-mode excitation and transmission line resonator. , 2017, , .		3
5	Design, Analysis and Implementation of Pulse Generator by CMOS Flipped on Glass for Low Power UWB-IR. IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, 2017, E100.A, 200-209.	0.3	1
6	Analysis and implementation of quick-start pulse generator by CMOS flipped on quartz substrate. , 2016, , .		5
7	Burst-pulse Generator based on transmission line toward sub-MMW. , 2014, , .		3