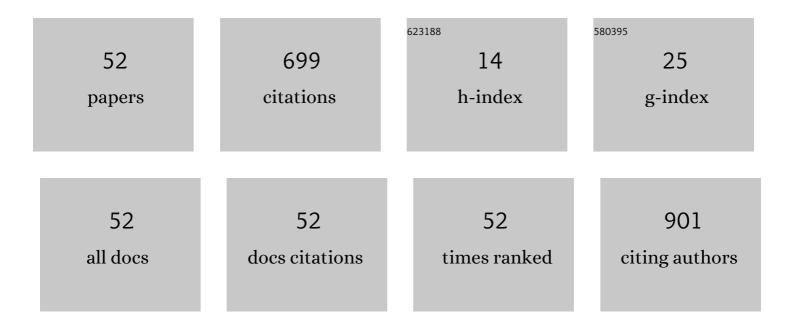
Segolene Olivier

List of Publications by Year in descending order

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SECOLENE OLIVIED

#	Article	lF	CITATIONS
1	Coupled-mode theory and propagation losses in photonic crystal waveguides. Optics Express, 2003, 11, 1490.	1.7	106
2	III-V-on-Silicon Integration: From Hybrid Devices to Heterogeneous Photonic Integrated Circuits. IEEE Journal of Selected Topics in Quantum Electronics, 2020, 26, 1-13.	1.9	87
3	A Versatile Silicon-Silicon Nitride Photonics Platform for Enhanced Functionalities and Applications. Applied Sciences (Switzerland), 2019, 9, 255.	1.3	78
4	Hybrid III–V/Silicon Technology for Laser Integration on a 200-mm Fully CMOS-Compatible Silicon Photonics Platform. IEEE Journal of Selected Topics in Quantum Electronics, 2019, 25, 1-10.	1.9	44
5	Enhanced transmission through photonic-crystal-based bent waveguides by bend engineering. Applied Physics Letters, 2001, 79, 3579-3581.	1.5	41
6	Silicon nitride-on-silicon bi-layer grating couplers designed by a global optimization method. Optics Express, 2018, 26, 13656.	1.7	39
7	New Advances on Heterogeneous Integration of Ill–V on Silicon. Journal of Lightwave Technology, 2015, 33, 976-983.	2.7	36
8	Hybrid III-V on silicon lasers for photonic integrated circuits on silicon. , 2014, , .		27
9	Hartmann wave-front scanner. Optics Letters, 1999, 24, 1796.	1.7	23
10	Hybrid III-V Silicon Photonic Integrated Circuits for Optical Communication Applications. IEEE Journal of Selected Topics in Quantum Electronics, 2016, 22, 379-389.	1.9	23
11	Direct measurement of the near-field super resolved focused spot in InSb. Optics Express, 2012, 20, 10426.	1.7	20
12	Hybrid III-V/Silicon SOA in Optical Network Based on Advanced Modulation Formats. IEEE Photonics Technology Letters, 2015, 27, 2383-2386.	1.3	20
13	Liquid-crystal Hartmann wave-front scanner. Applied Optics, 2000, 39, 3838.	2.1	19
14	Two-mode fringes in planar photonic crystal waveguides with constrictions: a probe that is sensitive to propagation losses. Journal of the Optical Society of America B: Optical Physics, 2002, 19, 2403.	0.9	16
15	Electro-Optical Ring Modulator: An Ultracompact Model for the Comparison and Optimization of p-n, p-i-n, and Capacitive Junction. IEEE Journal of Selected Topics in Quantum Electronics, 2016, 22, 89-98.	1.9	16
16	Photonic crystals and the real world of optical telecommunications. Annales Des Telecommunications/Annals of Telecommunications, 2003, 58, 1197.	1.6	9
17	Low-loss, compact, spot-size-converter based vertical couplers for photonic integrated circuits. Journal Physics D: Applied Physics, 2019, 52, 214001.	1.3	9
18	Hybrid III-V on Silicon Fast and Widely Tunable Laser Based on Rings Resonators with PIN Junctions. , 2018, , .		7

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#	Article	IF	CITATIONS
19	A hybrid SOI/SiN photonic platform for high-speed and temperature-insensitive CWDM optical transceivers. , 2018, , .		7
20	Application of micro solid immersion lens as probe for near-field scanning microscopy. Applied Physics Letters, 2014, 104, .	1.5	6
21	25-Gb/s Transmission Over 2.5-km SSMF by Silicon MRR Enhanced 1.55- \$mu ext{m}\$ III-V/SOI DML. IEEE Photonics Technology Letters, 2017, 29, 960-963.	1.3	6
22	Hybrid III-V on Silicon Integrated Distributed Feedback Laser and Ring Resonator for 25ÂGb/s Future Access Networks. Journal of Lightwave Technology, 2018, 36, 1498-1502.	2.7	6
23	Thin film characterization by total reflection x-ray fluorescence. Spectrochimica Acta, Part B: Atomic Spectroscopy, 2008, 63, 1365-1369.	1.5	5
24	Silicon modulator based on interleaved capacitors in subwavelength grating waveguides. , 2016, , .		5
25	Two-dimensional photonic crystals: new feasible confined optical systems. Comptes Rendus Physique, 2002, 3, 89-102.	0.3	4
26	Low-loss photonic crystal and monolithic InP integration: bands, bends, lasers, and filters. , 2004, 5360, 119.		4
27	Inhomogeneous nucleation and growth of palladium and alloyed cobalt during self-aligned capping of advanced copper interconnects. Thin Solid Films, 2010, 518, 4773-4778.	0.8	4
28	Optimization of Silicon MZM Fabrication Parameters for High Speed Short Reach Interconnects at 1310 nm. Applied Sciences (Switzerland), 2016, 6, 395.	1.3	4
29	Improvement of critical temperature of niobium nitride deposited on 8-inch silicon wafers thanks to an AIN buffer layer. Superconductor Science and Technology, 2021, 34, 045002.	1.8	4
30	Advanced optical characterization of micro solid immersion lens. , 2012, , .		3
31	50km Error Free Transmission of Fully Integrated Chirp-Managed 10Gb/s Directly Modulated C-band Tunable III-V/SOI Hybrid Lasers. , 2016, , .		3
32	Hybrid III-V/silicon photonic integrated circuits for high bitrates telecommunication applications. , 2017, , .		3
33	Advanced Si photonics platform for high-speed and energy-efficient optical transceivers for datacom. , 2020, , .		3
34	Optimization Design of Efficient Broadband Bi-Layer Grating Couplers for a Silicon Nitride-on-Silicon Foundry Platform. , 2018, , .		3
35	Validation by 25km error free transmission of 10Gb/s directly modulated III-V/SOI hybrid DFB laser. , 2015, , .		2
36	Waveguide-based two-dimensional photonic crystals. , 2001, , .		1

#	Article	IF	CITATIONS
37	Toward real-world devices in InP-based PCs. , 2004, 5360, 77.		1
38	New advances on heterogeneous integration of III-V on silicon. , 2014, , .		1
39	Towards a low noise class-A hybrid III-V/Silicon laser. , 2014, , .		1
40	Comparison among Silicon modulators based on Free-Carrier Plasma Dispersion Effect. , 2015, , .		1
41	Fiber grating couplers for optical access via the chip backside. Journal of Lightwave Technology, 2020, , 1-1.	2.7	1
42	O-band echelle grating demultiplexers on SiNOI featuring low-loss and reduced thermal sensitiveness. , 2018, , .		1
43	Introducing photonic devices for 40Gbits/s wavelength division multiplexing transceivers on 300-mm SOI wafers using CMOS processes. , 2014, , .		0
44	Towards a low noise class-A hybrid III-V/silicon laser. Proceedings of SPIE, 2014, , .	0.8	0
45	40 Gbit/s silicon modulators fabricated on 200-mm and 300-mm SOI wafers. , 2014, , .		Ο
46	Intensity noise reduction in an hybrid III-V/Silicon laser using a gain saturated SOA. , 2015, , .		0
47	Hybrid III-V/silicon photonic integrated circuits for optical communication applications. , 2016, , .		Ο
48	25Gb/s Error-free transmission with a packaged chipset integrating a III-V/SOI DFB laser an electro-absorption modulator and a semiconductor optical amplifier. , 2017, , .		0
49	25-Gb/s transmission over 2.5-km SSMF by silicon MRR enhanced 1.55-μm III-V/SOI DML. , 2017, , .		0
50	A Packaged Silicon Photonic Circuit Integrating a Hybrid Tunable Laser, a Modulator and an Amplifier. , 2018, , .		0
51	Enhanced Silicon photonics platform: towards low energy consumption of optical transceivers for datacenter communications , 2018, , .		0
52	Ultra-Fast Tunable Laser Enabling 4 ns Coherent Slot Switching Beyond 100 Gbit/s. , 2020, , .		0