

Abhijit Dasgupta

List of Publications by Year in descending order

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19
papers

288
citations

1684188

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docs citations

19
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299
citing authors

#	ARTICLE	IF	CITATIONS
1	A Novel Step-Up Multilevel Inverter Based On Switched-Capacitor Technique For Renewable Energy Conversion System. , 2019, , .		4
2	Cross-Switched Multilevel Inverter Using Novel Switched Capacitor Converters. IEEE Transactions on Industrial Electronics, 2019, 66, 8521-8532.	7.9	114
3	A Novel Reduced Device Count Multilevel Inverter Structure using Non-isolated Power Supplies. , 2018, , .		0
4	Harmonic Analysis in L-G Fault for LCC Based HVDC. , 2018, , .		0
5	Development, Analysis and Simulation Study of a Novel Switched Capacitor Multilevel Inverter Structure for Different DC Source Configurations. , 2018, , .		0
6	Phasor Estimation and Modelling Techniques of PMU- A Review. Energy Procedia, 2017, 109, 64-77.	1.8	26
7	PMU implementation for a Wide Area Measurement of a power system. , 2017, , .		7
8	Fault detection in direct current transmission lines using discrete fourier transform from single terminal current signals. , 2017, , .		11
9	Intelligent computing based converter fault identification in line commutated high voltage direct current transmission line. , 2017, , .		2
10	Study of a new single phase multilevel inverter based on switched capacitor units. , 2016, , .		1
11	A novel isolated boost converter fed BLDCM drive for electric vehicle. , 2016, , .		2
12	Application of an Advanced Repetitive Controller to Mitigate Harmonics in MMC With APOD Scheme. IEEE Transactions on Power Electronics, 2016, 31, 6112-6121.	7.9	35
13	A new technique in hydro thermal interconnected automatic generation control system by using minority charge carrier inspired algorithm. International Journal of Electrical Power and Energy Systems, 2015, 68, 259-268.	5.5	46
14	Voltage Balancing Scheme in MMC â€œ A New Approach. International Journal of Emerging Electric Power Systems, 2014, 15, 389-399.	0.8	3
15	New harmonic mitigation scheme for modular multilevel converter â€œ an experimental approach. IET Power Electronics, 2014, 7, 3090-3100.	2.1	24
16	High frequency link soft switched pulse shifted modulated front end 3 level diode clamped inverter using APSO. , 2014, , .		1
17	ZVCS based high frequency link grid connected SVPWM applied three phase three level diode clamped inverter for photovoltaic applications. , 2014, , .		6
18	A Review and Advance Technology in Multi-Area Automatic Generation Control by Using Minority Charge Carrier Inspired Algorithm. International Journal of Emerging Electric Power Systems, 2013, 14, 609-627.	0.8	4

#	ARTICLE	IF	CITATIONS
19	Hardware implementation of three-phase VSI using PIC16F877A microcontroller and optimisation using PSO. International Journal of Power and Energy Conversion, 2013, 4, 182.	0.3	2