

# Govinda Rao Locharla

## List of Publications by Year in descending order

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2258059

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#	ARTICLE	IF	CITATIONS
1	Design of a Gabor Filter-Based Image Denoising Hardware Model. Electronics (Switzerland), 2022, 11, 1063.	3.1	7
2	Implementation of MIMO data reordering and scheduling methodologies for eightâ€parallel variable length multiâ€path delay commutator FFT/IFFT. IET Computers and Digital Techniques, 2016, 10, 215-225.	1.2	6
3	Variable length mixed radix MDC FFT/IFFT processor for MIMOâ€OFDM application. IET Computers and Digital Techniques, 2018, 12, 9-19.	1.2	6
4	Implementation of input data buffering and scheduling methodology for 8 parallel MDC FFT. , 2015, , .		5
5	Radix-8 Modified Booth Fixed-Width Signed Multipliers with Error Compensation. Arabian Journal for Science and Engineering, 2021, 46, 1115-1125.	3.0	1