

Stephen A Edwards

List of Publications by Year in descending order

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Version: 2024-02-01

50
papers

913
citations

1040056

9
h-index

713466

21
g-index

53
all docs

53
docs citations

53
times ranked

363
citing authors

#	ARTICLE	IF	CITATIONS
1	VIS: A system for verification and synthesis. Lecture Notes in Computer Science, 1996, , 428-432.	1.3	351
2	Predictable programming on a precision timed architecture. , 2008, , .		102
3	SHIM. , 2005, , .		43
4	Scheduling-independent threads and exceptions in SHIM. , 2006, , .		30
5	A disruptive computer design idea: Architectures with repeatable timing. , 2009, , .		27
6	Code Generation in the Columbia Esterel Compiler. Eurasip Journal on Embedded Systems, 2007, 2007, 1-31.	1.2	23
7	Simple and fast biased locks. , 2010, , .		23
8	From functional programs to pipelined dataflow circuits. , 2017, , .		22
9	Experiences teaching an FPGA-based embedded systems class. ACM SIGBED Review, 2005, 2, 56-62.	1.8	20
10	VIS. Lecture Notes in Computer Science, 1996, , 248-256.	1.3	17
11	Programming shared memory multiprocessors with deterministic message-passing concurrency. , 2008, , .		17
12	A Processor Extension for Cycle-Accurate Real-Time Software. Lecture Notes in Computer Science, 2006, , 449-458.	1.3	17
13	Compositional dataflow circuits. , 2017, , .		14
14	Separate Compilation for Synchronous Modules. Lecture Notes in Computer Science, 2005, , 129-140.	1.3	13
15	Compiling Esterel into Static Discrete-Event Code. Electronic Notes in Theoretical Computer Science, 2006, 153, 117-131.	0.9	11
16	Optimizing Sequential Cycles Through Shannon Decomposition and Retiming. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2007, 26, 456-467.	2.7	11
17	Static Deadlock Detection for the SHIM Concurrent Language. , 2008, , .		11
18	Compile-Time Analysis and Specialization of Clocks in Concurrent Programs. Lecture Notes in Computer Science, 2009, , 48-62.	1.3	11

#	ARTICLE	IF	CITATIONS
19	Celling SHIM. , 2009, , .		9
20	Implementing latency-insensitive dataflow blocks. , 2015, , .		8
21	Master of none acceleration. , 2019, , .		8
22	Efficient code generation from SHIM models. , 2006, , .		8
23	MEMOCODE 2012 hardware/software codesign contest: DNA sequence aligner. , 2012, , .		7
24	Compositional Dataflow Circuits. Transactions on Embedded Computing Systems, 2019, 18, 1-27.	2.9	7
25	A Domain-Specific Language for Generating Dataflow Analyzers. Electronic Notes in Theoretical Computer Science, 2006, 164, 103-119.	0.9	5
26	Static elaboration of recursion for concurrent software. , 2008, , .		5
27	A deterministic multi-way rendezvous library for haskell. Parallel and Distributed Processing Symposium (IPDPS), Proceedings of the International Conference on, 2008, , .	1.0	5
28	A novel analysis space for pointer analysis and its application for bug finding. Science of Computer Programming, 2010, 75, 921-942.	1.9	5
29	Buffer sharing in CSP-like programs. , 2009, , .		4
30	Cache Impacts of Datatype Acceleration. IEEE Computer Architecture Letters, 2012, 11, 21-24.	1.5	4
31	Hardware synthesis from a recursive functional language. , 2015, , .		4
32	Programming Shared Memory Multiprocessors with Deterministic Message-Passing Concurrency: Compiling SHIM to Pthreads. , 2008, , .		3
33	Compositional deadlock detection for rendezvous communication. , 2009, , .		3
34	The Sparse Synchronous Model. , 2020, , .		3
35	Efficient code generation from SHIM models. ACM SIGPLAN Notices, 2006, 41, 125-134.	0.2	2
36	Flexible pointer analysis using assign-fetch graphs. , 2008, , .		2

#	ARTICLE	IF	CITATIONS
37	Computation vs. Memory Systems: Pinning Down Accelerator Bottlenecks. Lecture Notes in Computer Science, 2011, , 86-98.	1.3	2
38	Deadlock-free joins in DB-mesh, an asynchronous systolic array accelerator. , 2017, , .		1
39	On Determinism. Lecture Notes in Computer Science, 2018, , 240-253.	1.3	1
40	Approximate Reachability for Dead Code Elimination in Esterel. Lecture Notes in Computer Science, 2005, , 323-337.	1.3	1
41	Concurrency and Communication: Lessons from the SHIM Project. Lecture Notes in Computer Science, 2009, , 276-287.	1.3	1
42	Further Experiences Teaching an FPGA-Based Embedded Systems Class. Lecture Notes in Computer Science, 2019, , 222-230.	1.3	1
43	Instantaneous Transitions in Esterel. Electronic Notes in Theoretical Computer Science, 2008, 203, 49-64.	0.9	0
44	Ensuring deterministic concurrency through compilation. , 2010, , .		0
45	Buffer Sharing in Rendezvous Programs. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2010, 29, 1471-1480.	2.7	0
46	MEMOCODE 2014 software design contest: Space Invaders emulator. , 2014, , .		0
47	Welcome message from the chairs. , 2014, , .		0
48	Network Synthesis for Database Processing Units. , 2017, , .		0
49	Generating fast code from concurrent program dependence graphs. ACM SIGPLAN Notices, 2004, 39, 175-181.	0.2	0
50	Compiling SHIM. , 2010, , 121-146.		0