Deruo Cheng

List of Publications by Year
in descending order

8 papers	**87** citations	2258059 **3** h-index	2272923 **4** g-index
8 all docs	**8** docs citations	**8** times ranked	**14** citing authors

#	Article	IF	Citations
1	Delayered IC image analysis with template‐based Tanimoto Convolution and Morphological Decision. IET Circuits, Devices and Systems, 2022, 16, 169-177.	1.4	3
2	Deep learning-based image analysis framework for hardware assurance of digital integrated circuits. Microelectronics Reliability, 2021, 123, 114196.	1.7	13
3	Joint Anomaly Detection and Inpainting for Microscopy Images Via Deep Self-Supervised Learning. , 2021, , .		4
4	Deep Learning-Based Image Analysis Framework for Hardware Assurance of Digital Integrated Circuits. , 2020, , .		3
5	Global Template Projection and Matching Method for Training-Free Analysis of Delayered IC Images. , 2019, , .		9
6	A Hierarchical Multiclassifier System for Automated Analysis of Delayered IC Images. IEEE Intelligent Systems, 2019, 34, 36-43.	4.0	14
7	Hybrid <inline-formula> <tex-math notation="LaTeX">${K}$ </tex-math> </inline-formula>-Means Clustering and Support Vector Machine Method for via and Metal Line Detections in Delayered IC Images. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1849-1853.	3.0	18
8	Deep Learning for Automatic IC Image Analysis. , 2018, , .		23