

Faik Baskaya

List of Publications by Year in descending order

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Version: 2024-02-01

27
papers

275
citations

1478505

6
h-index

1474206

9
g-index

28
all docs

28
docs citations

28
times ranked

198
citing authors

#	ARTICLE	IF	CITATIONS
1	A Floating-Gate-Based Field-Programmable Analog Array. IEEE Journal of Solid-State Circuits, 2010, 45, 1781-1794.	5.4	109
2	Placement for large-scale floating-gate field-programable analog arrays. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2006, 14, 906-910.	3.1	26
3	Rapid Prototyping of Large-scale Analog Circuits With Field Programmable Analog Array. , 2007, , .		16
4	A generic reconfigurable array specification and programming environment (GRASPER). , 2009, , .		12
5	Adaptive sized Quasi-Monte Carlo based yield aware analog circuit optimization tool. , 2014, , .		12
6	A Hybrid Quasi Monte Carlo Method for Yield Aware Analog Circuit Sizing Tool. , 2015, , .		12
7	A deterministic aging simulator and an analog circuit sizing tool robust to aging phenomena. , 2015, , .		11
8	Design, implementation, and evaluation of a backstepping control algorithm for an active ankleâ€‘foot orthosis. Control Engineering Practice, 2021, 106, 104667.	5.5	11
9	An analog circuit synthesis tool based on efficient and reliable yield estimation. Microelectronics Journal, 2016, 54, 14-22.	2.0	10
10	Net-Sensitivity-Based Optimization of Large-Scale Field-Programmable Analog Array (FPAA) Placement and Routing. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 565-569.	3.0	9
11	A novel yield aware multi-objective analog circuit optimization tool. , 2015, , .		9
12	Reliability assessment of CMOS differential cross-coupled LC oscillators and a novel on chip self-healing approach against aging phenomena. Microelectronics Reliability, 2014, 54, 397-403.	1.7	8
13	A lifetime-aware analog circuit sizing tool. The Integration VLSI Journal, 2016, 55, 349-356.	2.1	6
14	Design of a digitally tunable 5 th order GM-C filter using linearized OTA in 90nm CMOS technology. , 2015, , .		5
15	Sensitivity based methodologies for process variation aware analog IC optimization. , 2014, , .		4
16	A Rare Event Based Yield Estimation Methodology for Analog Circuits. , 2018, , .		3
17	A Self-Contained Large-Scale FPAA Development Platform. , 2007, , .		2
18	Design of blocks for a Field Programmable Mixed Array (FPMA). , 2015, , .		2

#	ARTICLE	IF	CITATIONS
19	Semi-empirical aging model development via accelerated aging test. , 2016, , .		2
20	Efficient signature selection tool for sense & react systems. , 2016, , .		2
21	On Chip Reconfigurable CMOS Analog Circuit Design and Automation Against Aging Phenomena. ACM Transactions on Design Automation of Electronic Systems, 2019, 24, 1-22.	2.6	2
22	Analog layer extensions for analog/mixed-signal assertion languages. , 2013, , .		1
23	Convolutional Neural Network-Based Signal Classification in Real Time. IEEE Embedded Systems Letters, 2021, 13, 186-189.	1.9	1
24	Layout Tools for Analog Integrated Circuits and Mixed-Signal Systems-on-Chip: A Survey. , 2016, , 479-499.		0
25	Aging signature properties and an efficient signature determination tool for online monitoring. The Integration VLSI Journal, 2017, 58, 496-503.	2.1	0
26	A modified relay-race algorithm for floorplanning in PCB and IC design. Turkish Journal of Electrical Engineering and Computer Sciences, 2020, 28, 679-692.	1.4	0
27	A Configurable Interface for Analog Sensor Outputs. Sakarya University Journal of Science, 0, , .	0.7	0