Youhui Zhang

List of Publications by Year
in descending order

35	894	1170033	1051228
35 papers	894 citations	9 h-index	16 g-index
35 all docs	35 docs citations	35 times ranked	954 citing authors

#	Article	IF	Citations
1	Polyhedral-Based Compilation Framework for In-Memory Neural Network Accelerators. ACM Journal on Emerging Technologies in Computing Systems, 2022, 18, 1-23.	1.8	0
2	Brain-inspired global-local learning incorporated with neuromorphic computing. Nature Communications, 2022, 13, 65.	5.8	33
3	A review of basic software for brain-inspired computing. CCF Transactions on High Performance Computing, 2022, 4, 34-42.	1.1	2
4	A framework for the general design and computation of hybrid neural networks. Nature Communications, 2022, 13, .	5.8	14
5	A Reduced Architecture for ReRAM-Based Neural Network Accelerator and Its Software Stack. IEEE Transactions on Computers, 2021, 70, 316-331.	2.4	2
6	ERA-LSTM: An Efficient ReRAM-Based Architecture for Long Short-Term Memory. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 1328-1342.	4.0	26
7	A system hierarchy for brain-inspired computing. Nature, 2020, 586, 378-384.	13.7	88
8	A hybrid and scalable brain-inspired robotic platform. Scientific Reports, 2020, 10, 18160.	1.6	11
9	High Performance Simulation of Spiking Neural Network on GPGPUs. IEEE Transactions on Parallel and Distributed Systems, 2020, 31, 2510-2523.	4.0	12
10	Towards artificial general intelligence with hybrid Tianjic chip architecture. Nature, 2019, 572, 106-111.	13.7	517
11	Design Guidelines of RRAM based Neural-Processing-Unit. , 2019, , .		39
12	FPSA. , 2019, , .		44
13	A Unified Framework for Training, Mapping and Simulation of ReRAM-Based Convolutional Neural Network Acceleration. IEEE Computer Architecture Letters, 2019, 18, 63-66.	1.0	7
14	Service-Oriented Architecture on FPGA-Based MPSoC. IEEE Transactions on Parallel and Distributed Systems, 2017, 28, 2993-3006.	4.0	39
15	An approach of processor core customization for stencil computation. , 2014, , .		1
16	Employing intelligence in object-based storage devices to provide attribute-based file access. Science China Information Sciences, 2013, 56, 1-10.	2.7	1
17	HW/SW approaches to accelerate GRAPES in an FU array. , 2013, , .		0
18	Model of Network-on-Chip routers and performance analysis. IEICE Electronics Express, 2011, 8, 986-993.	0.3	4

#	ARTICLE	IF	CITATIONS
19	A user-space file system for on-demand legacy desktop software. Science China Information Sciences, 2011, 54, 1142-1150.	2.7	0
20	Efficient Monte Carlo-based options pricing on graphics processors and its optimizations. Science China Information Sciences, 2010, 53, 1703-1712.	2.7	3
21	Converting legacy desktop applications into on-demand personalized software. IEEE Transactions on Services Computing, 2010, 3, 306-321.	3.2	5
22	A Performance Analytical Approach Based on Queuing Model for Network-on-Chip. , 2010, , .		5
23	Applying file information to block-level content addressable storage. Tsinghua Science and Technology, 2009, 14, 41-49.	4.1	1
24	A Software-Controlled Cache Coherence Optimization for Snoopy-Based SMP System. , 2009, , .		0
25	Optimized mapping of pixels into memory for H.264/AVC decoding. IEICE Electronics Express, 2009, 6, 283-289.	0.3	0
26	Codec-on-Demand Based on User-Level Virtualization. IEICE Transactions on Information and Systems, 2009, E92-D, 2422-2429.	0.4	0
27	Portable desktop applications based on user-level virtualization. , 2008, , .		0
28	Semantic Data De-duplication for archival storage systems. , 2008, , .		11
29	IDRS: Combining File-level Intrusion Detection with Block-level Data Recovery based on iSCSI. , 2008, , .		3
30	On Virtual-Machine-Based Windows File Reads: A Performance Study. , 2008, , .		2
31	Teleport: Load Balancing with Dynamic Adjustment Strategy Based on OSD Storage System. , 2007, , .		0
32	Seamless Peripherals Integration for Network Computers based on the Reversed Server Message Block Protocol. , 2006, , .		2
33	Research on object-storage-based intrusion detection. , 2006, , .		10
34	Virtual-Machine-based Intrusion Detection on File-aware Block Level Storage. Proceedings Symposium on Computer Architecture and High Performance Computing, 2006, , .	0.0	11
35	A Checkpointing/Recovery System for MPI Applications on Cluster of IA-64 Computers. , 0, , .		1