

# Junhua Shen

## List of Publications by Year in descending order

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citations

1937685

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#	ARTICLE	IF	CITATIONS
1	A 14b 80 MS/s SAR ADC With 73.6 dB SNDR in 65 nm CMOS. IEEE Journal of Solid-State Circuits, 2013, 48, 3059-3066.	5.4	161
2	A 16-bit 16-MS/s SAR ADC With On-Chip Calibration in 55-nm CMOS. IEEE Journal of Solid-State Circuits, 2018, 53, 1149-1160.	5.4	75
3	Edge-Pursuit Comparator: An Energy-Scalable Oscillator Collapse-Based Comparator With Application in a 74.1 dB SNDR and 20 kS/s 15 b SAR ADC. IEEE Journal of Solid-State Circuits, 2017, 52, 1077-1090.	5.4	58
4	A 12-Bit 31.1-MS/s SAR ADC With On-Chip Input-Signal-Independent Calibration Achieving 100.4-dB SFDR Using 256-fF Sampling Capacitance. IEEE Journal of Solid-State Circuits, 2019, 54, 937-947.	5.4	18
5	Corrections to "Precision Passive-Charge-Sharing SAR ADC: Analysis, Design, and Measurement Results" [May 18 1481-1492]. IEEE Journal of Solid-State Circuits, 2018, 53, 1869-1869.	5.4	1