

S Suresh Kumar

List of Publications by Year in descending order

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Version: 2024-02-01

8
papers

38
citations

2258059

3
h-index

2053705

5
g-index

8
all docs

8
docs citations

8
times ranked

28
citing authors

#	ARTICLE	IF	CITATIONS
1	Recurrent deep neural learning classification algorithm based high level synthesis in VLSI circuit with runtime adaptability. Journal of Intelligent and Fuzzy Systems, 2022, 43, 2503-2514.	1.4	1
2	Spider monkey optimization based high level synthesis in VLSI circuits for runtime adaptability. Concurrency Computation Practice and Experience, 2021, 33, e5280.	2.2	2
3	Strip lines and blotches noise removal using dynamic artifacts removal algorithm for HD images. Materials Today: Proceedings, 2021, , .	1.8	1
4	Quantization based wavelet transformation technique for digital image compression with removal of multiple artifacts and noises. Cluster Computing, 2019, 22, 11271-11284.	5.0	3
5	Design and development of high performance MOS current mode logic (MCML) processor for fast and power efficient computing. Cluster Computing, 2019, 22, 13387-13395.	5.0	12
6	On Analyzing LDPC Codes over Multiantenna MC-CDMA System. Mathematical Problems in Engineering, 2014, 2014, 1-6.	1.1	1
7	Quadrant Dynamic with Automatic Plateau Limit Histogram Equalization for Image Enhancement. Mathematical Problems in Engineering, 2014, 2014, 1-8.	1.1	6
8	Contrast Stretching Recursively Separated Histogram Equalization for Brightness Preservation and Contrast Enhancement. , 2009, , .		12