Dixian Zhao

List of Publications by Year in descending order

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59 papers	1,142 citations	516215 16 h-index	433756 31 g-index
59 all docs	59 docs citations	59 times ranked	736 citing authors

#	Article	IF	Citations
1	Analysis and Design of a CMOS LNA With Transformer-Based Integrated Notch Filter for Ku-Band Satellite Communications. IEEE Transactions on Microwave Theory and Techniques, 2022, 70, 790-800.	2.9	9
2	A 196.5 dBc/Hz FOM_T 16.8â€“21.6-GHz Class-F₂₃ CMOS VCO With Transformer-Based Optimal Q-Factor Tank. IEEE Solid-State Circuits Letters, 2022, 5, 62-65.	1.3	10
3	Analysis and Design of a 0.3-THz Signal Generator Using an Oscillator-Doubler Architecture in 40-nm CMOS. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 2284-2296.	3.5	1
4	A 1.9-dB NF K-Band Temperature-Healing Phased-Array Receiver Employing Hybrid Packaged 65-nm CMOS Beamformer and 0.1-Î¼m GaAs LNAs. IEEE Microwave and Wireless Components Letters, 2022, 32, 768-771.	2.0	9
5	A 24â€“29.5-GHz Highly Linear Phased-Array Transceiver Front-End in 65-nm CMOS Supporting 800-MHz 64-QAM and 400-MHz 256-QAM for 5G New Radio. IEEE Journal of Solid-State Circuits, 2022, 57, 2702-2718.	3.5	23
6	An X-Band CMOS VCO Using Ultra-Wideband Dual Common-Mode Resonance Technique. IEEE Transactions on Circuits and Systems I: Regular Papers, 2022, 69, 3579-3590.	3.5	6
7	A W-Band 2 Ã— 2 Rectenna Array With On-Chip CMOS Switching Rectifier and On-PCB Tapered Slot Antenna for Wireless Power Transfer. IEEE Transactions on Microwave Theory and Techniques, 2021, 69, 969-979.	2.9	10
8	A Ka-Band Balanced Four-Beam Phased-Array Receiver With Symmetrical Beam-Distribution Network in 65-nm CMOS. IEEE Access, 2021, 9, 110026-110038.	2.6	7
9	A Ku-Band CMOS Power Amplifier With Series-Shunt LC Notch Filter for Satellite Communications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1869-1880.	3.5	18
10	Eâ€band broadband digital controlled phaseâ€inverting variable gain amplifier in 65â€nm CMOS. Electronics Letters, 2021, 57, 179-182.	0.5	3
11	A Non-Contact Vital Signs Detection in a Multi-Channel 77GHz LFMCW Radar System. IEEE Access, 2021, 9, 49614-49628.	2.6	24
12	Analysis and Design of a CMOS Bidirectional Passive Vector-Modulated Phase Shifter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1398-1408.	3.5	29
13	K-band 360Â° Passive Vector Modulator Phase Shifter with Coupled Line Qadrature Coupler and Passive Transistor Array. , 2021, , .		3
14	K-band 360Â° Passive Vector Modulator Phase Shifter with Coupled Line Qadrature Coupler and Passive Transistor Array. , 2021, , .		1
15	A 28-GHz Doherty Power Amplifier With a Compact Transformer-Based Quadrature Hybrid in 65-nm CMOS. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 2790-2794.	2.2	13
16	Millimeter-Wave Integrated Phased Arrays. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3977-3990.	3.5	42
17	Corrections to â€œMillimeter-Wave Integrated Phased Arraysâ€•[early access, Jul 12, 21 doi: 10.1109/TCSI.2021.3093093]. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 4413-4413.	3.5	0
18	A 196.2 dBc/Hz FOM_T 16.8-to-21.6 GHz Class-F₂₃ VCO with Transformer-Based Optimal Q-factor Tank in 65-nm CMOS. , 2021, , .		3

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19	High-Throughput Low-Power Area-Efficient Outphasing Modulator Based on Unrolled and Pipelined Radix-2 CORDIC. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 480-491.	2.1	7
20	A Ka-Band CMOS 4-Beam Phased-Array Receiver With Symmetrical Beam-Distribution Network. IEEE Solid-State Circuits Letters, 2020, 3, 410-413.	1.3	11
21	A W-Band Rectenna Using On-Chip CMOS Switching Rectifier and On-PCB Tapered Slot Antenna Achieving 25% Effective-Power-Conversion Efficiency for Wireless Power Transfer. , 2020, , .		6
22	A DC-50 GHz CMOS Switched-Type Attenuator With Capacitive Compensation Technique. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 3389-3399.	3.5	29
23	A 20-GHz 1.9-mW LNA Using g _m-Boost and Current-Reuse Techniques in 65-nm CMOS for Satellite Communications. IEEE Journal of Solid-State Circuits, 2020, 55, 2714-2723.	3.5	56
24	Geometric Analysis and Systematic Design of Millimeter-Wave Low-Power Frequency Dividers in 65-nm CMOS. IEEE Access, 2020, 8, 20658-20665.	2.6	4
25	W-band Amplifiers in 0.1 Î¼m GaAs. , 2020, , .		0
26	High-Efficiency Millimeter-Wave CMOS Switching Rectifiers: Theory and Implementation. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 5171-5180.	2.9	18
27	Geometric Analysis and Systematic Design of a Reflective-Type Phase Shifter With Full 360Â° Phase Shift Range and Minimal Loss Variation. IEEE Transactions on Microwave Theory and Techniques, 2019, 67, 4156-4166.	2.9	26
28	Analysis, Design and Modeling of Millimeter-Wave Wilkinson Power Combiner for 5G Phased Array. , 2019, , .		5
29	A W-Band Switching Rectifier with 27% Efficiency for Wireless Power Transfer in 65-nm CMOS. , 2019, , .		4
30	A Ka-Band Compact Active Switch With Broadband Amplifiers for Phased-Array Transceiver in 65-nm CMOS. IEEE Solid-State Circuits Letters, 2019, 2, 33-36.	1.3	1
31	5G Millimeter-Wave Phased-Array Transceiver: System Considerations and Circuit Implementations. , 2019, , .		13
32	Design Techniques of Passive Devices for Broadband CMOS Millimeter-Wave Circuits. , 2019, , .		0
33	A Ku-Band Low-Noise Amplifier in 40-nm CMOS. , 2019, , .		9
34	Kaâ€band wideband VCO with LC filtering technique in 65â€nm CMOS. Electronics Letters, 2019, 55, 581-583.	0.5	5
35	Ku-band compact Wilkinson power divider based on multi-tap inductor technique in 65-nm CMOS. IEICE Electronics Express, 2018, 15, 20180973-20180973.	0.3	6
36	A 1.2-dB Noise Figure Broadband GaAs Low-Noise Amplifier with 17-dB Gain for Millimeter-Wave 5G Base-Station. , 2018, , .		4

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37	Key Circuit Building Blocks for 5G Millimeter-Wave Phased-Array Transceiver Front-End (Invited). , 2018, , .		1
38	A Broadband 1-dB Noise Figure GaAs Low-Noise Amplifier for Millimeter-Wave 5G Base-Stations. , 2018, , .		10
39	RF Front-End Circuits and Architectures for IoT/LTE-A/5G Connectivity. Wireless Communications and Mobile Computing, 2018, 2018, 1-2.	0.8	4
40	A 28-GHz CMOS Broadband Single-Path Power Amplifier with 17.4-dBm P1dB for 5G Phased-Array. , 2018, , .		18
41	Ka-Band CMOS 360Â° Reflective-Type Phase Shifter with Â±0.2 dB Insertion Loss Variation Using Triple-Resonating Load and Dual-Voltage Control Techniques. , 2018, , .		25
42	A Ka-band CMOS Digital-Controlled Phase-Invariant Variable Gain Amplifier with 4-bit Tuning Range and 0.5-dB Resolution. , 2018, , .		53
43	CORDIC-Based Multi-Gb/s Digital Outphasing Modulator for Highly Efficient Millimeter-Wave Transmitters. Wireless Communications and Mobile Computing, 2018, 2018, 1-6.	0.8	2
44	A 0.45â€‰W 18% PAE E-Band Power Amplifier in 100â€‰nm InGaAs pHEMT Technology. Wireless Communications and Mobile Computing, 2018, 2018, 1-6.	0.8	1
45	Next Generation Wireless Technologies for Internet of Things. Sensors, 2018, 18, 221.	2.1	18
46	94â€GHz 360Â° reflectiveâ€type phase shifter with minimal loss variation using tripleâ€resonating load technique. Electronics Letters, 2018, 54, 215-217.	0.5	4
47	Highâ€gain lowâ€cost broadband 60 GHz differential integrated patch array antennas with wireâ€bonding packaging and onâ€board compensation network. IET Microwaves, Antennas and Propagation, 2017, 11, 971-975.	0.7	16
48	21.3ÂdBm 18.5ÂGHzâ€BW 8â€way Eâ€band power amplifier in 28Ânm high performance mobile CMOS. Electronics Letters, 2017, 53, 1310-1312.	0.5	6
49	A low-power wide gain range digitally controlled variable gain amplifier with a wide tuning bandwidth for 60 GHz applications. , 2016, , .		5
50	A Flip-Chip Packaging Design With Waveguide Output on Single-Layer Alumina Board for E-Band Applications. IEEE Transactions on Microwave Theory and Techniques, 2016, 64, 1255-1264.	2.9	16
51	Low-cost aperture-coupled integrated 60 GHz phased array antenna in PCB process. , 2016, , .		2
52	A 20-to-75 dB gain 5-dB noise figure broadband 60-GHz receiver with digital calibration. , 2016, , .		3
53	A 40-nm CMOS E-Band 4-Way Power Amplifier With Neutralized Bootstrapped Cascode Amplifier and Optimum Passive Circuits. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 4083-4089.	2.9	70
54	An E-Band Power Amplifier With Broadband Parallel-Series Power Combiner in 40-nm CMOS. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 683-690.	2.9	92

#	Article	IF	Citations
55	A 40 nm CMOS E-Band Transmitter With Compact and Symmetrical Layout Floor-Plans. IEEE Journal of Solid-State Circuits, 2015, 50, 2560-2571.	3.5	48
56	Design of an Optimal Layout Polyphase Filter for Millimeter-Wave Quadrature LO Generation. IEEE Transactions on Circuits and Systems II: Express Briefs, 2013, 60, 202-206.	2.2	45
57	A 60-GHz Dual-Mode Class AB Power Amplifier in 40-nm CMOS. IEEE Journal of Solid-State Circuits, 2013, 48, 2323-2337.	3.5	194
58	A 60-GHz Outphasing Transmitter in 40-nm CMOS. IEEE Journal of Solid-State Circuits, 2012, 47, 3172-3183.	3.5	88
59	A 60ÂGHz 14ÂdBm power amplifier with a transformer-based power combiner in 65Ânm CMOS. International Journal of Microwave and Wireless Technologies, 2011, 3, 99-105.	1.5	6