

# Rahul Dewani

## List of Publications by Year in descending order

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#	ARTICLE	IF	CITATIONS
1	A Very High Resolution 30-Sided Space Vector Generation From a Single DC-Link for Induction Motor Drives. IEEE Transactions on Industrial Electronics, 2022, 69, 160-168.	7.9	7
2	A Single DC-Link Multilevel 42-Sided Polygonal Voltage Space Vector Generation With Lower Order Harmonic Suppression Using Switched-Capacitor Filter. IEEE Transactions on Industrial Electronics, 2022, 69, 12369-12378.	7.9	0
3	A General Multilevel Polygonal Space Vector Generation Scheme With Reduced Switching for the Inverter and Harmonic Suppression Using a Switched-Capacitive Filter for the Full Modulation Range. IEEE Transactions on Power Electronics, 2022, 37, 8167-8176.	7.9	7
4	Suppression of Lower Order Harmonics for the Full Modulation Range for a Two-Level Inverter-Fed IM Drive With a Switched-Capacitive Filter Technique Forming a 42-Sided Voltage Space Vector Structure. IEEE Transactions on Industrial Electronics, 2021, 68, 6701-6709.	7.9	6
5	Suppression of lower order harmonics by Switched-Capacitive filtering using Polygonal Space Vector Structures and Capacitor Sizing for Induction Motor Drive Applications. , 2021, , .		1
6	Suppression of Lower Order Harmonics using a 21-Concentric 42-sided polygonal Space Vector Structure for Induction Motor Drive Applications. , 2021, , .		0
7	Generation of 42-sided polygonal Voltage Space Vector Structure for suppression of lower order harmonics in IM Drive Applications. , 2020, , .		4
8	A Novel approach for the analysis of Harmonic Suppression in higher-sided polygonal SV structures. , 2020, , .		3