Seyyed Reza Sharafinejad

List of Publications by Year
in descending order

4 papers	**19** citations	2682572 **2** h-index	2917675 **2** g-index
4 all docs	**4** docs citations	**4** times ranked	**10** citing authors

#	Article	IF	Citations
1	Incremental SAT-Based Accurate Auto-Correction of Sequential Circuits Through Automatic Test Pattern Generation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 245-252.	2.7	10
2	UPF-based formal verification of low power techniques in modern processors. , 2015, , .		7
3	Automatic Correction of Dynamic Power Management Architecture in Modern Processors. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 308-318.	3.1	2
4	Formal Verification of Non-Functional Strategies of System-Level Power Management Architecture in Modern Processors. , 2020, , .		0