## Kunal Sinha

## List of Publications by Year in descending order

Source: https://exaly.com/author-pdf/8231505/publications.pdf

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3311381 3475538 4 15 1 1 citations h-index g-index papers 5 5 5 18 docs citations times ranked citing authors all docs

#	Article	IF	CITATIONS
1	Incorporation of Tensile and Compressive Channel Stress by Modulating SiGe Stressor Length in Embedded Source/Drain Si-FinFET Architecture. , $2018, \ldots$		2
2	Investigation of process induced stress in the channel of a SiGe embedded source/drain Ge-FinFET architecture. , $2018,  ,  .$		2
3	A technique to incorporate both tensile and compressive channel stress in Ge FinFET architecture. Journal of Computational Electronics, 2017, 16, 620-630.	2.5	10
4	A study on the performance of stress induced p-channel MOSFETs with embeded Si <inf>1−x</inf> Ge <inf>x</inf> source/drain. , 2012, , .		1