

Kunal Sinha

List of Publications by Year in descending order

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4
papers

15
citations

3311381

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3475538

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docs citations

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18
citing authors

| # | ARTICLE | IF | CITATIONS |
|---|--|-----|-----------|
| 1 | A technique to incorporate both tensile and compressive channel stress in Ge FinFET architecture. Journal of Computational Electronics, 2017, 16, 620-630. | 2.5 | 10 |
| 2 | Incorporation of Tensile and Compressive Channel Stress by Modulating SiGe Stressor Length in Embedded Source/Drain Si-FinFET Architecture. , 2018, , . | | 2 |
| 3 | Investigation of process induced stress in the channel of a SiGe embedded source/drain Ge-FinFET architecture. , 2018, , . | | 2 |
| 4 | A study on the performance of stress induced p-channel MOSFETs with embedded Si _{1-x} Ge _x source/drain. , 2012, , . | | 1 |