

Edgar SÃ¡nchez-Sinencio

List of Publications by Year in descending order

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215
papers

6,735
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57758

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times ranked

3815
citing authors

#	ARTICLE	IF	CITATIONS
1	Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1841-1854.	5.4	11
2	Current Reference Circuits: A Tutorial. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 830-836.	3.0	14
3	Design Trade-Offs in Common-Mode Feedback Implementations for Highly Linear Three-Stage Operational Transconductance Amplifiers. Electronics (Switzerland), 2021, 10, 991.	3.1	2
4	A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3587-3597.	5.4	2
5	Analog/RF IP Protection: Attack Models, Defense Techniques, and Challenges. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 36-41.	3.0	5
6	A Reconfigurable Rectifier With Optimal Loading Point Determination for RF Energy Harvesting From $\hat{\sim}22$ dBm to $\hat{\sim}2$ dBm. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 87-91.	3.0	33
7	Multiple-Input Harvesting Power Management Unit With Enhanced Boosting Scheme for IoT Applications. IEEE Transactions on Industrial Electronics, 2020, 67, 3662-3672.	7.9	12
8	A Fully Integrated Maximum Power Tracking Combiner for Energy Harvesting IoT Applications. IEEE Transactions on Industrial Electronics, 2020, 67, 2744-2754.	7.9	17
9	A PVT-Resilient, Highly-Linear Fifth-Order Ring-Oscillator-Based Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4295-4308.	5.4	5
10	Breaking Analog Locking Techniques. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2157-2170.	3.1	11
11	Wien Oscillator Using Organic Enzyme $\hat{\sim}$ Chemiresistors for Fused Measurement of Glucose and Lactate. Advanced Intelligent Systems, 2020, 2, 2000004.	6.1	9
12	A 1-nA 4.5-nW 289-ppm/ $\hat{\sim}$ C Current Reference Using Automatic Calibration. IEEE Journal of Solid-State Circuits, 2020, 55, 2498-2512.	5.4	17
13	A 175.2-mW 4-Stage OTA With Wide Load Range (400 pF $\hat{\sim}$ 12 nF) Using Active Parallel Compensation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1621-1629.	3.1	20
14	Power-Scaling Output-Compensated Three-Stage OTAs for Wide Load Range Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 2180-2192.	5.4	20
15	A 0.6-V Power-Efficient Active-RC Analog Low-Pass Filter With Cutoff Frequency Selection. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1757-1769.	3.1	23
16	Gaussian-Process-Based Surrogate for Optimization-Aided and Process-Variations-Aware Analog Circuit Design. Electronics (Switzerland), 2020, 9, 685.	3.1	10
17	Schmitt Trigger-Based Key Provisioning for Locking Analog/RF Integrated Circuits. , 2020, , .		4
18	Taming the Stability-Constrained Performance Optimization Challenge of Distributed On-Chip Voltage Regulation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1571-1584.	2.7	3

#	ARTICLE	IF	CITATIONS
19	A Unified Amplifier-Based CC-CV Linear Charger for Energy-Constrained Low-Power Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 377-381.	3.0	11
20	Design of Sub-Gigahertz Reconfigurable RF Energy Harvester From $\hat{\sim}22$ to 4 dBm With 99.8% Peak MPPT Power Efficiency. IEEE Journal of Solid-State Circuits, 2019, 54, 2601-2613.	5.4	55
21	Classification and Design Space Exploration of Low-Power Three-Stage Operational Transconductance Amplifier Architectures for Wide Load Ranges. Electronics (Switzerland), 2019, 8, 1268.	3.1	21
22	Design and Fabrication of a 3-D Printed Concentrating Solar Thermoelectric Generator for Energy Harvesting Based Wireless Sensor Nodes. , 2019, 3, 1-4.		10
23	An On-Chip Built-in Linearity Estimation Methodology and Hardware Implementation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 897-908.	5.4	1
24	Reconfigurable System for Electromagnetic Energy Harvesting With Inherent Activity Sensing Capabilities for Wearable Technology. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1302-1306.	3.0	7
25	A High Power Supply Rejection and Fast Settling Time Capacitor-Less LDO. IEEE Transactions on Power Electronics, 2019, 34, 474-484.	7.9	65
26	An Integrated Concurrent Multiple-Input Self-Startup Energy Harvesting Capacitive-Based DC Adder Combiner. IEEE Transactions on Industrial Electronics, 2018, 65, 6281-6290.	7.9	16
27	Search for Optimal Pulse Charging Parameters for Li-Ion Polymer Batteries Using Taguchi Orthogonal Arrays. IEEE Transactions on Industrial Electronics, 2018, 65, 8982-8992.	7.9	83
28	An Area Efficient Thermal Energy Harvester With Reconfigurable Capacitor Charge Pump for IoT Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1974-1978.	3.0	39
29	An Efficient and Fast Li-Ion Battery Charging System Using Energy Harvesting or Conventional Sources. IEEE Transactions on Industrial Electronics, 2018, 65, 7383-7394.	7.9	42
30	A Temperature Compensation Technique for a Dynamic Amplifier in Pipelined-SAR ADCs. IEEE Solid-State Circuits Letters, 2018, 1, 10-13.	2.0	9
31	A Built-In Self-Test and <i>In Situ</i> Analog Circuit Optimization Platform. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3445-3458.	5.4	24
32	A Time-Interleave-Based Power Management System with Maximum Power Extraction and Health Protection Algorithm for Multiple Microbial Fuel Cells for Internet of Things Smart Nodes. Applied Sciences (Switzerland), 2018, 8, 2404.	2.5	8
33	Towards provably-secure analog and mixed-signal locking against overproduction. , 2018, , .		31
34	The Impact of Pulse Charging Parameters on the Life Cycle of Lithium-Ion Polymer Batteries. Energies, 2018, 11, 2162.	3.1	51
35	Smart Soil Parameters Estimation System Using an Autonomous Wireless Sensor Network With Dynamic Power Management Strategy. IEEE Sensors Journal, 2018, 18, 8913-8923.	4.7	60
36	A 13.56-MHz CMOS Active Rectifier With a Voltage Mode Switched-Offset Comparator for Implantable Medical Devices. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2050-2060.	3.1	28

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37	Multiple Input Energy Harvesting Systems for Autonomous IoT End-Nodes. Journal of Low Power Electronics and Applications, 2018, 8, 6.	2.0	27
38	Noise-sensitive feedback loop identification in linear time-varying analog circuits. , 2017, , .		1
39	A Fully Integrated Reconfigurable Self-Startup RF Energy-Harvesting System With Storage Capability. IEEE Journal of Solid-State Circuits, 2017, 52, 704-719.	5.4	99
40	A 0.8â€“1.2 V 10â€“50 MS/s 13-bit Subranging Pipelined-SAR ADC Using a Temperature-Insensitive Time-Based Amplifier. IEEE Journal of Solid-State Circuits, 2017, 52, 2991-3005.	5.4	38
41	A universal fast battery charging and management solution for stand-alone solar photovoltaic home systems in Sub-Saharan Africa. , 2017, , .		4
42	Efficient use of gain-bandwidth product in active filters: Gm-C and Active-R alternatives. , 2017, , .		4
43	Thwarting analog IC piracy via combinational locking. , 2017, , .		38
44	An Autonomous Energy Harvesting Power Management Unit With Digital Regulation for IoT Applications. IEEE Journal of Solid-State Circuits, 2016, 51, 1457-1474.	5.4	84
45	21.1 A single-cycle MPPT charge-pump energy harvester using a thyristor-based VCO without storage capacitor. , 2016, , .		26
46	A Highly Efficient Reconfigurable Charge Pump Energy Harvester With Wide Harvesting Range and Two-Dimensional MPPT for Internet of Things. IEEE Journal of Solid-State Circuits, 2016, 51, 1302-1312.	5.4	121
47	Built-In Self Optimization for Variation Resilience of Analog Filters. , 2015, , .		9
48	A Highly Efficient Ultralow Photovoltaic Power Harvesting System With MPPT for Internet of Things Smart Nodes. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 3065-3075.	3.1	81
49	An Automatic Resonance Tracking Scheme With Maximum Power Transfer for Piezoelectric Transducers. IEEE Transactions on Industrial Electronics, 2015, 62, 7136-7145.	7.9	53
50	20.7 A 0.45-to-3V reconfigurable charge-pump energy harvester with two-dimensional MPPT for Internet of Things. , 2015, , .		24
51	An Inductorless DCâ€“DC Converter for an Energy Aware Power Management Unit Aimed at Microbial Fuel Cell Arrays. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2015, 3, 1109-1121.	5.4	26
52	150â€“850 MHz High-Linearity Sine-wave Synthesizer Architecture Based on FIR Filter Approach and SFDR Optimization. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2227-2237.	5.4	30
53	Low power complementary metalâ€“oxide semiconductor classâ€“G audio amplifier with gradual power supply switching. IET Circuits, Devices and Systems, 2015, 9, 256-264.	1.4	2
54	Low-Input Power-Level CMOS RF Energy-Harvesting Front End. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 3794-3805.	4.6	41

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73	A Low-Power High-PSRR Clock-Free Current-Controlled Class-D Audio Amplifier. IEEE Journal of Solid-State Circuits, 2011, 46, 1553-1561.	5.4	85
74	A 20-µm ² 32-GHz Wideband Mixer With 12-GHz IF bandwidth in 0.18-µm ² SiGe Process. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 2731-2740.	4.6	20
75	Attenuation-Predistortion Linearization of CMOS OTAs With Digital Correction of Process Variations in OTA-C Filter Applications. IEEE Journal of Solid-State Circuits, 2010, 45, 351-367.	5.4	84
76	A Millimeter-Wave (23-µm ² 32 GHz) Wideband BiCMOS Low-Noise Amplifier. IEEE Journal of Solid-State Circuits, 2010, 45, 289-299.	5.4	56
77	High PSR Low Drop-Out Regulator With Feed-Forward Ripple Cancellation Technique. IEEE Journal of Solid-State Circuits, 2010, 45, 565-577.	5.4	211
78	A Low THD, Low Power, High Output-Swing Time-Mode-Based Tunable Oscillator Via Digital Harmonic-Cancellation Technique. IEEE Journal of Solid-State Circuits, 2010, 45, 1061-1071.	5.4	52
79	A 2-µm ² 1100 MHz wideband low noise amplifier with 1.43 dB minimum noise figure. , 2010, , .		5
80	A 140mA 90nm CMOS low drop-out regulator with −56dB power supply rejection at 10MHz. , 2010, , .		7
81	A 470μW clock-free current-controlled class D amplifier with 0.02% THD&N and 82dB PSRR. , 2010, , .		1
82	A Millimeter-Wave (24/31-GHz) Dual-Band Switchable Harmonic Receiver in 0.18-µm ² SiGe Process. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 2717-2730.	4.6	22
83	New applications and technology scaling driving next generation A/D converters. , 2009, , .		2
84	Power-Aware Multiband&Multistandard CMOS Receiver System-Level Budgeting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 570-574.	3.0	14
85	An On-Chip Loopback Block for RF Transceiver Built-In Test. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 444-448.	3.0	14
86	A 1-V +31 dBm IIP3, Reconfigurable, Continuously Tunable, Power-Adjustable Active-RC LPF. IEEE Journal of Solid-State Circuits, 2009, 44, 495-508.	5.4	76
87	A CMOS Low-Noise Amplifier With Reconfigurable Input Matching Network. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1054-1062.	4.6	65
88	A Broadband CMOS Amplitude Detector for On-Chip RF Measurements. IEEE Transactions on Instrumentation and Measurement, 2008, 57, 1470-1477.	4.7	95
89	A Noise Reduction and Linearity Improvement Technique for a Differential Cascode LNA. IEEE Journal of Solid-State Circuits, 2008, 43, 588-599.	5.4	140
90	Applications of Multipath Transform-Domain Charge-Sampling Wide-Band Receivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 309-313.	3.0	17

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91	A low power 1.3GHz dual-path current mode Gm-C filter. , 2008, , .		1
92	A Current Injection Built-In Test Technique for RF Low-Noise Amplifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1794-1804.	5.4	38
93	An Accurate Automatic Quality-Factor Tuning Scheme for Second-Order <i>LC</i> Filters. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 745-756.	0.1	7
94	A Low-Power Frequency Synthesizer with Quadrature Signal Generation for 2.4 GHz Zigbee Transceiver Applications. , 2007, , .		14
95	Full On-Chip CMOS Low-Dropout Voltage Regulator. IEEE Transactions on Circuits and Systems I: Regular Papers, 2007, 54, 1879-1890.	5.4	420
96	Nonlinear Shaping SC Oscillator With Enhanced Linearity. IEEE Journal of Solid-State Circuits, 2007, 42, 2421-2431.	5.4	15
97	A systematic system level design methodology for dual band CMOS RF receivers. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	1
98	THD+Noise Estimation in Class-D Amplifiers. , 2007, , .		6
99	State space approach to design of continuous time sigma delta ADC with delay in feedback path. Midwest Symposium on Circuits and Systems, 2006, , .	1.0	2
100	An Integrated Frequency Response Characterization System With a Digital Interface for Analog Testing. IEEE Journal of Solid-State Circuits, 2006, 41, 2301-2313.	5.4	33
101	Robust highly linear high-frequency CMOS OTA with IM3 below - 70 dB at 26 MHz. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1433-1447.	0.1	52
102	CMOS RF receiver system design: a systematic approach. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1023-1034.	0.1	49
103	A Highly Linear Low-Noise Amplifier. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 4079-4085.	4.6	91
104	A 10-bit 44-MS/s 20-mW Configurable Time-Interleaved Pipeline ADC for a Dual-Mode 802.11b/Bluetooth Receiver. IEEE Journal of Solid-State Circuits, 2006, 41, 530-539.	5.4	28
105	Series/Parallel Time-Multiplexed Switched-Capacitor Filters with Programmability Based on Non-Uniform Sampling. Analog Integrated Circuits and Signal Processing, 2006, 46, 241-252.	1.4	3
106	Frequency planning and synthesizer architectures for multiband OFDM UWB radios. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 3744-3756.	4.6	48
107	RF bandpass filter design using capacitive degeneration. , 2005, , .		1
108	A GSM LNA using mutual-coupled degeneration. IEEE Microwave and Wireless Components Letters, 2005, 15, 68-70.	3.2	20

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109	A rail-to-rail amplifier input stage with $\pm 0.35\%$ fluctuation. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 271-282.	0.1	26
110	An RC Time Constant Auto-Tuning Structure for High Linearity Continuous-Time $\Sigma\Delta$ Modulators and Active Filters. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 2179-2188.	0.1	67
111	A Continuous-Time $\Sigma\Delta$ Modulator With 88-dB Dynamic Range and 1.1-MHz Signal Bandwidth. IEEE Journal of Solid-State Circuits, 2004, 39, 75-86.	5.4	197
112	A Fully Parallel CMOS Analog Median Filter. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2004, 51, 116-123.	2.2	20
113	A 2.4-GHz monolithic fractional-N frequency synthesizer with robust phase-switching prescaler and loop capacitance multiplier. IEEE Journal of Solid-State Circuits, 2003, 38, 866-874.	5.4	142
114	Switched-capacitor circuits with periodical nonuniform individual sampling. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2003, 50, 404-414.	2.2	10
115	On-chip ramp generators for mixed-signal BIST and ADC self-test. IEEE Journal of Solid-State Circuits, 2003, 38, 263-273.	5.4	119
116	An enhanced adaptive Q-tuning scheme for a 100-MHz fully symmetric OTA-based bandpass filter. IEEE Journal of Solid-State Circuits, 2003, 38, 585-593.	5.4	42
117	A 1.3-V 5-mW fully integrated tunable bandpass filter at 2.1 GHz in $0.35\text{-}\mu\text{m}$ CMOS. IEEE Journal of Solid-State Circuits, 2003, 38, 918-928.	5.4	58
118	A 2.7-v 1.8-GHz fourth-order tunable LC bandpass filter based on emulation of magnetically coupled resonators. IEEE Journal of Solid-State Circuits, 2003, 38, 1172-1181.	5.4	26
119	A wide input bandwidth 7-bit 300-msample/s folding and current-mode interpolating adc. IEEE Journal of Solid-State Circuits, 2003, 38, 1405-1410.	5.4	37
120	A fully balanced pseudo-differential OTA with common-mode feedforward and inherent common-mode feedback detector. IEEE Journal of Solid-State Circuits, 2003, 38, 663-668.	5.4	108
121	A 3-V, $0.35\text{-}\mu\text{m}$ CMOS Bluetooth receiver IC. IEEE Journal of Solid-State Circuits, 2003, 38, 30-42.	5.4	64
122	Floating-gate analog implementation of the additive soft-input soft-output decoding algorithm. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2003, 50, 1256-1269.	0.1	13
123	Transconductance amplifier structures with very small transconductances: a comparative design approach. IEEE Journal of Solid-State Circuits, 2002, 37, 770-775.	5.4	153
124	A CMOS transconductance amplifier architecture with wide tuning range for very low frequency applications. IEEE Journal of Solid-State Circuits, 2002, 37, 776-781.	5.4	67
125	A 100-MHz 8-mW ROM-less quadrature direct digital frequency synthesizer. IEEE Journal of Solid-State Circuits, 2002, 37, 1235-1243.	5.4	32
126	Title is missing!. Analog Integrated Circuits and Signal Processing, 2002, 32, 249-256.	1.4	11

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127	High-Selectivity Switched-Capacitor Bandpass Filter with Quasi-Continuous Quality Factor Tunability. Analog Integrated Circuits and Signal Processing, 2002, 33, 117-126.	1.4	7
128	Analog implementation of an active noise controller system for portable audio applications. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2001, 48, 400-404.	2.2	3
129	Lorenz-based chaotic cryptosystem: a monolithic implementation. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2000, 47, 1243-1247.	0.1	51
130	A 60-dB dynamic-range CMOS sixth-order 2.4-Hz low-pass filter for medical applications. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2000, 47, 1391-1398.	2.2	193
131	Corrections to "CMOS transconductance multipliers: a tutorial". IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1999, 46, 660-660.	2.2	10
132	A CMOS Four Quadrant Current/Transconductance Multiplier. Analog Integrated Circuits and Signal Processing, 1999, 19, 163-168.	1.4	6
133	A Modular Analog NLMS Structure for Adaptive Filtering. Analog Integrated Circuits and Signal Processing, 1999, 21, 127-142.	1.4	8
134	A flexible and expendable neuroimage processor architecture. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1999, 46, 1055-1063.	0.1	4
135	Constant-g/sub m/ rail-to-rail CMOS op-amp input stage with overlapped transition regions. IEEE Journal of Solid-State Circuits, 1999, 34, 148-156.	5.4	55
136	Time multiplexed color image processing based on a CNN with cell-state outputs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 1998, 6, 314-322.	3.1	25
137	CMOS transconductance multipliers: a tutorial. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1998, 45, 1550-1563.	2.2	231
138	Low-voltage class AB buffers with quiescent current control. IEEE Journal of Solid-State Circuits, 1998, 33, 915-920.	5.4	97
139	An accurate quality factor tuning scheme for IF and high-Q continuous-time filters. IEEE Journal of Solid-State Circuits, 1998, 33, 1970-1978.	5.4	64
140	A 4-D chaotic oscillator based on a differential hysteresis comparator. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1998, 45, 3-10.	0.1	34
141	On the common mode rejection ratio in low voltage operational amplifiers with complementary N-P input pairs. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1997, 44, 678-683.	2.2	15
142	Programmable time-multiplexed switched-capacitor variable equalizer for arbitrary frequency response realizations. IEEE Journal of Solid-State Circuits, 1997, 32, 274-278.	5.4	14
143	An improved tail current source for low voltage applications. IEEE Journal of Solid-State Circuits, 1997, 32, 1173-1180.	5.4	51
144	Multistage amplifier topologies with nested G/sub m/-C compensation. IEEE Journal of Solid-State Circuits, 1997, 32, 2000-2011.	5.4	206

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145	VERDI: an acoustically programmable and adjustable CMOS mixed-mode signal processor for hearing aid applications. IEEE Journal of Solid-State Circuits, 1996, 31, 634-645.	5.4	37
146	Analog fault diagnosis based on ramping power supply current signature clusters. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1996, 43, 703-712.	2.2	44
147	Biquadratic programmable sc filters with additional flexibility and reduced total capacitance. International Journal of Circuit Theory and Applications, 1989, 17, 241-248.	2.0	5
148	Characterization, evaluation, and comparison of laser-trimmed film resistors. IEEE Journal of Solid-State Circuits, 1987, 22, 1177-1189.	5.4	16
149	Excess phase jitter cancellation method for SC relaxation oscillators. IEEE Transactions on Circuits and Systems, 1987, 34, 695-700.	0.9	8
150	Tradeoffs between passive sensitivity, output voltage swing, and total capacitance in biquadratic SC filters. IEEE Transactions on Circuits and Systems, 1984, 31, 984-987.	0.9	10
151	Finite gain - bandwidth product effects on a pair of pseudo-N-path SC filters. IEEE Transactions on Circuits and Systems, 1984, 31, 583-584.	0.9	0
152	Fully integrated MOSFET-C variable equalizer circuit with on-chip automatic tuning. , 0, , .		1
153	Low voltage current-mode filters: high performance and limitations. , 0, , .		0
154	A nonlinear macromodel for CMOS OTAs. , 0, , .		21
155	Optimal manufacturable CNN array size for time multiplexing schemes. , 0, , .		2
156	Large-image CNN hardware processing using a time multiplexing scheme. , 0, , .		5
157	VLSI implementation of an extended Hamming neural network for non-binary pattern recognition. , 0, , .		0
158	A field programmable analog signal processing array. , 0, , .		8
159	Wavelets generation using Laguerre analog adaptive filter. , 0, , .		2
160	A classifier system with low sensitivity to pattern shifted position. , 0, , .		0
161	Building blocks for filter tuning system using analog VLSI fuzzy logic controller. , 0, , .		0
162	A very fast CMOS artificial cellular neural network. , 0, , .		1

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163	The limitation of CMRR in low voltage operational amplifier with N-P input pairs. , 0, , .		2
164	Monolithic mixed-mode implementation of sum-of-product arrays for performing binary morphological image processing. , 0, , .		1
165	A floating-gate MOSFET D/A converter. , 0, , .		27
166	A unified approach for a time-domain built-in self-test technique and fault detection. , 0, , .		2
167	A current-mode based field programmable analog array architecture for signal processing applications. , 0, , .		24
168	A low mismatch sensitivity fully-balanced current-mode integrator. , 0, , .		1
169	Optimal design of low power nested GM-C compensation amplifiers using a current-based MOS transistor model. , 0, , .		0
170	Simple CMOS low-voltage op amps with constant-g/sub m/ rail-to-rail input stage. , 0, , .		2
171	A parallel analog median filter. , 0, , .		5
172	Frequency-domain intrachip communication schemes for CNN. , 0, , .		3
173	Efficient clock recovery architecture. , 0, , .		0
174	A low voltage fully differential nested G/sub m/ capacitance compensation amplifier: analysis and design. , 0, , .		0
175	VLSI implementation of a neural network for solving linear second order parabolic PDE. , 0, , .		3
176	Current mirror based folding amplifier. , 0, , .		7
177	A fully parallel CMOS analog median filter. , 0, , .		8
178	Next generation wideband multi-standard digital receiver design. , 0, , .		1
179	Very linear ramp-generators for high resolution ADC BIST and calibration. , 0, , .		37
180	A programmable rail-to-rail constant-g/sub m/ input structure for LV amplifier. , 0, , .		5

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181	Different operational transconductance amplifier topologies for obtaining very small transconductances. , 0, , .		5
182	A comparative study of digital $\hat{\Sigma}^{\Delta}$ modulators for fractional-N synthesis. , 0, , .		14
183	1.5 V 5.0 MHz switched capacitor circuits in 1.2 $\hat{\mu}$ m CMOS without voltage bootstrapper. , 0, , .		2
184	Fully-integrated LC VCOs at RF on silicon. , 0, , .		1
185	A low voltage operational transconductance amplifier using common mode feedforward for high frequency switched capacitor circuits. , 0, , .		7
186	A monolithic CMOS low-IF Bluetooth receiver. , 0, , .		5
187	An auto-tuning structure for continuous time sigma-delta AD converter and high precision filters. , 0, , .		1
188	On-chip spectrum analyzer for built-in testing analog ICs. , 0, , .		20
189	High-selectivity SC filters with continuous digital Q-factor programmability. , 0, , .		4
190	A 2-V 11-bit incremental A/D converter using floating gate technique. , 0, , .		0
191	Design considerations of bandpass LC filters for RF applications. , 0, , .		5
192	Design trade-offs of a symmetric linearized CMOS LC VCO. , 0, , .		3
193	Extraction of electrical parameters of floating gate devices for circuit analysis, simulation, and design. , 0, , .		8
194	A 2.1-GHz monolithic frequency synthesizer with robust phase switching prescaler and loop capacitance scaling. , 0, , .		5
195	A mixed-mode IF GFSK demodulator for Bluetooth. , 0, , .		2
196	Floating gate analog implementation of the additive soft-input soft-output decoding algorithm. , 0, , .		4
197	Design tradeoffs of CMOS current mirrors using one-equation for all-region model. , 0, , .		5
198	An improved Q-tuning scheme and a fully symmetric OTA. , 0, , .		1

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199	Design considerations in a BiCMOS dual-modulus prescaler. , 0, , .		5
200	A 3 V, 0.35 μ m CMOS Bluetooth receiver IC. , 0, , .		1
201	A 2.1GHz 1.3V 5mW programmable Q-enhancement LC bandpass biquad in 0.35 μ m CMOS. , 0, , .		3
202	A 16mW, 2.23-2.45GHz fully integrated π PLL with novel prescaler and loop filter in 0.35 μ m CMOS. , 0, , .		0
203	A dual-mode low-pass filter for 802.11b/Bluetooth receiver. , 0, , .		7
204	Radio frequency wireless systems: basics and IC implementations. , 0, , .		0
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