Edgar SÃ¡nchez-Sinencio

List of Publications by Year
in descending order

215 papers	6,735 citations	57758 44 h-index	82547 72 g-index
215 all docs	215 docs citations	215 times ranked	3815 citing authors

#	Article	IF	Citations
1	Full On-Chip CMOS Low-Dropout Voltage Regulator. IEEE Transactions on Circuits and Systems I: Regular Papers, 2007, 54, 1879-1890.	5.4	420
2	CMOS transconductance multipliers: a tutorial. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1998, 45, 1550-1563.	2.2	231
3	High PSR Low Drop-Out Regulator With Feed-Forward Ripple Cancellation Technique. IEEE Journal of Solid-State Circuits, 2010, 45, 565-577.	5.4	211
4	Multistage amplifier topologies with nested G_m/C compensation. IEEE Journal of Solid-State Circuits, 1997, 32, 2000-2011.	5.4	206
5	A Continuous-Time>tex<$Sigma Delta $>/tex<Modulator With 88-dB Dynamic Range and 1.1-MHz Signal Bandwidth. IEEE Journal of Solid-State Circuits, 2004, 39, 75-86.	5.4	197
6	A 60-dB dynamic-range CMOS sixth-order 2.4-Hz low-pass filter for medical applications. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2000, 47, 1391-1398.	2.2	193
7	Transconductance amplifier structures with very small transconductances: a comparative design approach. IEEE Journal of Solid-State Circuits, 2002, 37, 770-775.	5.4	153
8	A 2.4-GHz monolithic fractional-N frequency synthesizer with robust phase-switching prescaler and loop capacitance multiplier. IEEE Journal of Solid-State Circuits, 2003, 38, 866-874.	5.4	142
9	A Noise Reduction and Linearity Improvement Technique for a Differential Cascode LNA. IEEE Journal of Solid-State Circuits, 2008, 43, 588-599.	5.4	140
10	A Micropower Low-Noise Neural Recording Front-End Circuit for Epileptic Seizure Detection. IEEE Journal of Solid-State Circuits, 2011, 46, 1392-1405.	5.4	128
11	A Highly Efficient Reconfigurable Charge Pump Energy Harvester With Wide Harvesting Range and Two-Dimensional MPPT for Internet of Things. IEEE Journal of Solid-State Circuits, 2016, 51, 1302-1312.	5.4	121
12	On-chip ramp generators for mixed-signal BIST and ADC self-test. IEEE Journal of Solid-State Circuits, 2003, 38, 263-273.	5.4	119
13	Boost Converter With Dynamic Input Impedance Matching for Energy Harvesting With Multi-Array Thermoelectric Generators. IEEE Transactions on Industrial Electronics, 2014, 61, 5345-5353.	7.9	109
14	A fully balanced pseudo-differential OTA with common-mode feedforward and inherent common-mode feedback detector. IEEE Journal of Solid-State Circuits, 2003, 38, 663-668.	5.4	108
15	A Fully Integrated Reconfigurable Self-Startup RF Energy-Harvesting System With Storage Capability. IEEE Journal of Solid-State Circuits, 2017, 52, 704-719.	5.4	99
16	Low-voltage class AB buffers with quiescent current control. IEEE Journal of Solid-State Circuits, 1998, 33, 915-920.	5.4	97
17	A Broadband CMOS Amplitude Detector for On-Chip RF Measurements. IEEE Transactions on Instrumentation and Measurement, 2008, 57, 1470-1477.	4.7	95
18	A Highly Linear Low-Noise Amplifier. IEEE Transactions on Microwave Theory and Techniques, 2006, 54, 4079-4085.	4.6	91

#	ARTICLE	IF	CITATIONS
19	A 2.8-mW Sub-2-dB Noise-Figure Inductorless Wideband CMOS LNA Employing Multiple Feedback. IEEE Transactions on Microwave Theory and Techniques, 2011, 59, 3154-3161.	4.6	85
20	A Low-Power High-PSRR Clock-Free Current-Controlled Class-D Audio Amplifier. IEEE Journal of Solid-State Circuits, 2011, 46, 1553-1561.	5.4	85
21	Attenuation-Predistortion Linearization of CMOS OTAs With Digital Correction of Process Variations in OTA-C Filter Applications. IEEE Journal of Solid-State Circuits, 2010, 45, 351-367.	5.4	84
22	An Autonomous Energy Harvesting Power Management Unit With Digital Regulation for IoT Applications. IEEE Journal of Solid-State Circuits, 2016, 51, 1457-1474.	5.4	84
23	Search for Optimal Pulse Charging Parameters for Li-Ion Polymer Batteries Using Taguchi Orthogonal Arrays. IEEE Transactions on Industrial Electronics, 2018, 65, 8982-8992.	7.9	83
24	A Highly Efficient Ultralow Photovoltaic Power Harvesting System With MPPT for Internet of Things Smart Nodes. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2015, 23, 3065-3075.	3.1	81
25	A 1-V +31 dBm IIP3, Reconfigurable, Continuously Tunable, Power-Adjustable Active-RC LPF. IEEE Journal of Solid-State Circuits, 2009, 44, 495-508.	5.4	76
26	An Inductor-Less Noise-Cancelling Broadband Low Noise Amplifier With Composite Transistor Pair in 90 nm CMOS Technology. IEEE Journal of Solid-State Circuits, 2011, 46, 1111-1122.	5.4	70
27	A CMOS transconductance amplifier architecture with wide tuning range for very low frequency applications. IEEE Journal of Solid-State Circuits, 2002, 37, 776-781.	5.4	67
28	An RC Time Constant Auto-Tuning Structure for High Linearity Continuous-Time<tex>$SigmaDelta$</tex>Modulators and Active Filters. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2004, 51, 2179-2188.	0.1	67
29	A CMOS Low-Noise Amplifier With Reconfigurable Input Matching Network. IEEE Transactions on Microwave Theory and Techniques, 2009, 57, 1054-1062.	4.6	65
30	A High Power Supply Rejection and Fast Settling Time Capacitor-Less LDO. IEEE Transactions on Power Electronics, 2019, 34, 474-484.	7.9	65
31	An accurate quality factor tuning scheme for IF and high-Q continuous-time filters. IEEE Journal of Solid-State Circuits, 1998, 33, 1970-1978.	5.4	64
32	A 3-V, 0.35-Î¼m CMOS Bluetooth receiver IC. IEEE Journal of Solid-State Circuits, 2003, 38, 30-42.	5.4	64
33	A CMOS RF RMS Detector for Built-in Testing of Wireless Transceivers. , 0, , .		63
34	Smart Soil Parameters Estimation System Using an Autonomous Wireless Sensor Network With Dynamic Power Management Strategy. IEEE Sensors Journal, 2018, 18, 8913-8923.	4.7	60
35	A 1.3-V 5-mW fully integrated tunable bandpass filter at 2.1 GHz in 0.35-Î¼m CMOS. IEEE Journal of Solid-State Circuits, 2003, 38, 918-928.	5.4	58
36	A Millimeter-Wave (23â€"32 GHz) Wideband BiCMOS Low-Noise Amplifier. IEEE Journal of Solid-State Circuits, 2010, 45, 289-299.	5.4	56

#	Article	IF	Citations
37	Constant-g/sub m/ rail-to-rail CMOS op-amp input stage with overlapped transition regions. IEEE Journal of Solid-State Circuits, 1999, 34, 148-156.	5.4	55
38	Design of Sub-Gigahertz Reconfigurable RF Energy Harvester From â˜²2 to 4 dBm With 99.8% Peak MPPT Power Efficiency. IEEE Journal of Solid-State Circuits, 2019, 54, 2601-2613.	5.4	55
39	A DCVSL Delay Cell for Fast Low Power Frequency Synthesis Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2011, 58, 1225-1238.	5.4	54
40	An Automatic Resonance Tracking Scheme With Maximum Power Transfer for Piezoelectric Transducers. IEEE Transactions on Industrial Electronics, 2015, 62, 7136-7145.	7.9	53
41	Robust highly linear high-frequency CMOS OTA with IM3 below - 70 dB at 26 MHz. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1433-1447.	0.1	52
42	A Low THD, Low Power, High Output-Swing Time-Mode-Based Tunable Oscillator Via Digital Harmonic-Cancellation Technique. IEEE Journal of Solid-State Circuits, 2010, 45, 1061-1071.	5.4	52
43	Am improved tail current source for low voltage applications. IEEE Journal of Solid-State Circuits, 1997, 32, 1173-1180.	5.4	51
44	Lorenz-based chaotic cryptosystem: a monolithic implementation. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2000, 47, 1243-1247.	0.1	51
45	The Impact of Pulse Charging Parameters on the Life Cycle of Lithium-Ion Polymer Batteries. Energies, 2018, 11, 2162.	3.1	51
46	CMOS RF receiver system design: a systematic approach. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2006, 53, 1023-1034.	0.1	49
47	Frequency planning and synthesizer architectures for multiband OFDM UWB radios. IEEE Transactions on Microwave Theory and Techniques, 2005, 53, 3744-3756.	4.6	48
48	Low Phase Noise Wide Tuning Range N-Push Cyclic-Coupled Ring Oscillators. IEEE Journal of Solid-State Circuits, 2012, 47, 1278-1294.	5.4	45
49	Analog fault diagnosis based on ramping power supply current signature clusters. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1996, 43, 703-712.	2.2	44
50	An enhanced adaptive Q-tuning scheme for a 100-MHz fully symmetric OTA-based bandpass filter. IEEE Journal of Solid-State Circuits, 2003, 38, 585-593.	5.4	42
51	An Efficient and Fast Li-Ion Battery Charging System Using Energy Harvesting or Conventional Sources. IEEE Transactions on Industrial Electronics, 2018, 65, 7383-7394.	7.9	42
52	Low-Input Power-Level CMOS RF Energy-Harvesting Front End. IEEE Transactions on Microwave Theory and Techniques, 2015, 63, 3794-3805.	4.6	41
53	An Area Efficient Thermal Energy Harvester With Reconfigurable Capacitor Charge Pump for IoT Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2018, 65, 1974-1978.	3.0	39
54	A Current Injection Built-In Test Technique for RF Low-Noise Amplifiers. IEEE Transactions on Circuits and Systems I: Regular Papers, 2008, 55, 1794-1804.	5.4	38

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55	A 0.8â€"1.2 V 10â€"50 MS/s 13-bit Subranging Pipelined-SAR ADC Using a Temperature-Insensitive Time-Based Amplifier. IEEE Journal of Solid-State Circuits, 2017, 52, 2991-3005.	5.4	38
56	Thwarting analog IC piracy via combinational locking. , 2017, , .		38
57	VERDI: an acoustically programmable and adjustable CMOS mixed-mode signal processor for hearing aid applications. IEEE Journal of Solid-State Circuits, 1996, 31, 634-645.	5.4	37
58	Very linear ramp-generators for high resolution ADC BIST and calibration. , 0, , .		37
59	A wide input bandwidth 7-bit 300-msample/s folding and current-mode interpolating adc. IEEE Journal of Solid-State Circuits, 2003, 38, 1405-1410.	5.4	37
60	An Energy-Efficient Time-Domain Asynchronous 2 b/Step SAR ADC With a Hybrid R-2R/C-3C DAC Structure. IEEE Journal of Solid-State Circuits, 2014, 49, 1383-1396.	5.4	37
61	A 4-D chaotic oscillator based on a differential hysteresis comparator. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1998, 45, 3-10.	0.1	34
62	An Integrated Frequency Response Characterization System With a Digital Interface for Analog Testing. IEEE Journal of Solid-State Circuits, 2006, 41, 2301-2313.	5.4	33
63	A Reconfigurable Rectifier With Optimal Loading Point Determination for RF Energy Harvesting From âˆ'22 dBm to âˆ'2 dBm. IEEE Transactions on Circuits and Systems II: Express Briefs, 2020, 67, 87-91.	3.0	33
64	A 100-MHz 8-mW ROM-less quadrature direct digital frequency synthesizer. IEEE Journal of Solid-State Circuits, 2002, 37, 1235-1243.	5.4	32
65	Towards provably-secure analog and mixed-signal locking against overproduction. , 2018, , .		31
66	150â€"850 MHz High-Linearity Sine-wave Synthesizer Architecture Based on FIR Filter Approach and SFDR Optimization. IEEE Transactions on Circuits and Systems I: Regular Papers, 2015, 62, 2227-2237.	5.4	30
67	A 10-bit 44-MS/s 20-mW Configurable Time-Interleaved Pipeline ADC for a Dual-Mode 802.11b/Bluetooth Receiver. IEEE Journal of Solid-State Circuits, 2006, 41, 530-539.	5.4	28
68	A 13.56-MHz CMOS Active Rectifier With a Voltage Mode Switched-Offset Comparator for Implantable Medical Devices. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 2050-2060.	3.1	28
69	A floating-gate MOSFET D/A converter. , 0, , .		27
70	Multiple Input Energy Harvesting Systems for Autonomous IoT End-Nodes. Journal of Low Power Electronics and Applications, 2018, 8, 6.	2.0	27
71	A 2.7-v 1.8-GHz fourth-order tunable LC bandpass filter based on emulation of magnetically coupled resonators. IEEE Journal of Solid-State Circuits, 2003, 38, 1172-1181.	5.4	26
72	A rail-to-rail amplifier input stage with /spl plusmn/0.35%g/sub m/ fluctuation. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2005, 52, 271-282.	0.1	26

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73	An Inductorless DCâ€"DC Converter for an Energy Aware Power Management Unit Aimed at Microbial Fuel Cell Arrays. IEEE Journal of Emerging and Selected Topics in Power Electronics, 2015, 3, 1109-1121.	5.4	26
74	21.1 A single-cycle MPPT charge-pump energy harvester using a thyristor-based VCO without storage capacitor. , 2016, , .		26
75	Time multiplexed color image processing based on a CNN with cell-state outputs. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 1998, 6, 314-322.	3.1	25
76	A current-mode based field programmable analog array architecture for signal processing applications. , 0, , .		24
77	20.7 A 0.45-to-3V reconfigurable charge-pump energy harvester with two-dimensional MPPT for Internet of Things. , 2015, , .		24
78	A Built-In Self-Test and <italic>In Situ</italic> Analog Circuit Optimization Platform. IEEE Transactions on Circuits and Systems I: Regular Papers, 2018, 65, 3445-3458.	5.4	24
79	A 0.6-V Power-Efficient Active-RC Analog Low-Pass Filter With Cutoff Frequency Selection. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1757-1769.	3.1	23
80	A Millimeter-Wave (24/31-GHz) Dual-Band Switchable Harmonic Receiver in 0.18-mum SiGe Process. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 2717-2730.	4.6	22
81	A nonlinear macromodel for CMOS OTAs. , 0, , .		21
82	Classification and Design Space Exploration of Low-Power Three-Stage Operational Transconductance Amplifier Architectures for Wide Load Ranges. Electronics (Switzerland), 2019, 8, 1268.	3.1	21
83	On-chip spectrum analyzer for built-in testing analog ICs. , 0, , .		20
84	A Fully Parallel CMOS Analog Median Filter. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2004, 51, 116-123.	2.2	20
85	A GSM LNA using mutual-coupled degeneration. IEEE Microwave and Wireless Components Letters, 2005, 15, 68-70.	3.2	20
86	A 20â€"32-GHz Wideband Mixer With 12-GHz IF bandwidth in 0.18-$mu{hbox {m}}$ SiGe Process. IEEE Transactions on Microwave Theory and Techniques, 2010, 58, 2731-2740.	4.6	20
87	A 175.2-mW 4-Stage OTA With Wide Load Range (400 pFâ€"12 nF) Using Active Parallel Compensation. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 1621-1629.	3.1	20
88	Power-Scaling Output-Compensated Three-Stage OTAs for Wide Load Range Applications. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 2180-2192.	5.4	20
89	A linearization technique for RF low noise amplifier. , 0, , .		19
90	A 2-GHz Highly Linear Efficient Dual-Mode BiCMOS Power Amplifier Using a Reconfigurable Matching Network. IEEE Journal of Solid-State Circuits, 2012, 47, 2385-2404.	5.4	19

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91	A Wideband Millimeter-Wave Frequency Synthesis Architecture Using Multi-Order Harmonic-Synthesis and Variable <formula formulatype="inline"> <tex Notation="TeX">N</tex></formula>-Push Frequency Multiplication. IEEE Journal of Solid-State Circuits, 2011, 46, 1265-1283.	5.4	18
92	A highly linear pseudo-differential transconductance [CMOS OTA]. , 0, , .		17
93	Applications of Multipath Transform-Domain Charge-Sampling Wide-Band Receivers. IEEE Transactions on Circuits and Systems II: Express Briefs, 2008, 55, 309-313.	3.0	17
94	Electromagnetic Interference Resisting <newline/>Operational Amplifier. IEEE Transactions on Circuits and Systems I: Regular Papers, 2014, 61, 1917-1927.	5.4	17
95	A Fully Integrated Maximum Power Tracking Combiner for Energy Harvesting IoT Applications. IEEE Transactions on Industrial Electronics, 2020, 67, 2744-2754.	7.9	17
96	A 1-nA 4.5-nW 289-ppm/Â°C Current Reference Using Automatic Calibration. IEEE Journal of Solid-State Circuits, 2020, 55, 2498-2512.	5.4	17
97	Characterization, evaluation, and comparison of laser-trimmed film resistors. IEEE Journal of Solid-State Circuits, 1987, 22, 1177-1189.	5.4	16
98	An Integrated Concurrent Multiple-Input Self-Startup Energy Harvesting Capacitive-Based DC Adder Combiner. IEEE Transactions on Industrial Electronics, 2018, 65, 6281-6290.	7.9	16
99	On the common mode rejection ratio in low voltage operational amplifiers with complementary N-P input pairs. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1997, 44, 678-683.	2.2	15
100	Nonlinear Shaping SC Oscillator With Enhanced Linearity. IEEE Journal of Solid-State Circuits, 2007, 42, 2421-2431.	5.4	15
101	A Feed-Forward Power-Supply Noise Cancellation Technique for Single-Ended Class-D Audio Amplifiers. IEEE Journal of Solid-State Circuits, 2014, 49, 718-728.	5.4	15
102	A High-Efficiency Self-Oscillating Class-D Amplifier for Piezoelectric Speakers. IEEE Transactions on Power Electronics, 2015, 30, 5125-5135.	7.9	15
103	Programmable time-multiplexed switched-capacitor variable equalizer for arbitrary frequency response realizations. IEEE Journal of Solid-State Circuits, 1997, 32, 274-278.	5.4	14
104	A comparative study of digital Î£Î" modulators for fractional-N synthesis. , 0, , .		14
105	A Low-Power Frequency Synthesizer with Quadrature Signal Generation for 2.4 GHz Zigbee Transceiver Applications. , 2007, , .		14
106	Power-Aware Multibandâ€"Multistandard CMOS Receiver System-Level Budgeting. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 570-574.	3.0	14
107	An On-Chip Loopback Block for RF Transceiver Built-In Test. IEEE Transactions on Circuits and Systems II: Express Briefs, 2009, 56, 444-448.	3.0	14
108	Current Reference Circuits: A Tutorial. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 830-836.	3.0	14

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109	Floating-gate analog implementation of the additive soft-input soft-output decoding algorithm. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2003, 50, 1256-1269.	0.1	13
110	Linearized CMOS OTA using active-error feedforward technique. , 0, , .		12
111	Multiple-Input Harvesting Power Management Unit With Enhanced Boosting Scheme for IoT Applications. IEEE Transactions on Industrial Electronics, 2020, 67, 3662-3672.	7.9	12
112	Title is missing!. Analog Integrated Circuits and Signal Processing, 2002, 32, 249-256.	1.4	11
113	Constant-g›inf‹m›/inf‹Techniques for Rail-to-Rail CMOS Amplifier Input Stages: A Comparative Study. , 0, , .		11
114	A Unified Amplifier-Based CC-CV Linear Charger for Energy-Constrained Low-Power Applications. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 377-381.	3.0	11
115	Breaking Analog Locking Techniques. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2020, 28, 2157-2170.	3.1	11
116	Synthesis of High-Order Continuously Tunable Low-Pass Active-R Filters. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 1841-1854.	5.4	11
117	Tradeoffs between passive sensitivity, output voltage swing, and total capacitance in biquadratic SC filters. IEEE Transactions on Circuits and Systems, 1984, 31, 984-987.	0.9	10
118	Corrections to "CMOS transconductance multipliers: a tutorial". IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 1999, 46, 660-660.	2.2	10
119	Switched-capacitor circuits with periodical nonuniform individual sampling. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2003, 50, 404-414.	2.2	10
120	Feedforward Reversed Nested Miller Compensation Techniques for Three-Stage Amplifiers. , 0, , .		10
121	Design and Fabrication of a 3-D Printed Concentrating Solar Thermoelectric Generator for Energy Harvesting Based Wireless Sensor Nodes. , 2019, 3, 1-4.		10
122	Gaussian-Process-Based Surrogate for Optimization-Aided and Process-Variations-Aware Analog Circuit Design. Electronics (Switzerland), 2020, 9, 685.	3.1	10
123	Built-In Self Optimization for Variation Resilience of Analog Filters. , 2015, , .		9
124	A Temperature Compensation Technique for a Dynamic Amplifier in Pipelined-SAR ADCs. IEEE Solid-State Circuits Letters, 2018, 1, 10-13.	2.0	9
125	Wien Oscillator Using Organic Enzymeâ€Chemiresistors for Fused Measurement of Glucose and Lactate. Advanced Intelligent Systems, 2020, 2, 2000004.	6.1	9
126	Excess phase jitter cancellation method for SC relaxation oscillators. IEEE Transactions on Circuits and Systems, 1987, 34, 695-700.	0.9	8

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127	A field programmable analog signal processing array. , 0, , .		8
128	A Modular Analog NLMS Structure for Adaptive Filtering. Analog Integrated Circuits and Signal Processing, 1999, 21, 127-142.	1.4	8
129	A fully parallel CMOS analog median filter. , 0, , .		8
130	Extraction of electrical parameters of floating gate devices for circuit analysis, simulation, and design. , 0, , .		8
131	A Constant-g›inf‹m›/inf‹Rail-to-Rail Op Amp Input Stage Using Dynamic Current Scaling Technique. , 0, , .		8
132	Second order Dynamic element matching technique for low Oversampling Delta Sigma ADC. , 0, , .		8
133	A switched mode Li-ion battery charger with multiple energy harvesting systems simultaneously used as input sources. , 2014, , .		8
134	A Time-Interleave-Based Power Management System with Maximum Power Extraction and Health Protection Algorithm for Multiple Microbial Fuel Cells for Internet of Things Smart Nodes. Applied Sciences (Switzerland), 2018, 8, 2404.	2.5	8
135	Current mirror based folding amplifier. , 0, , .		7
136	A low voltage operational transconductance amplifier using common mode feedforward for high frequency switched capacitor circuits. , 0, , .		7
137	High-Selectivity Switched-Capacitor Bandpass Filter with Quasi-Continuous Quality Factor Tunability. Analog Integrated Circuits and Signal Processing, 2002, 33, 117-126.	1.4	7
138	A dual-mode low-pass filter for 802.11b/Bluetooth receiver. , 0, , .		7
139	A Carrier Frequency Generator for Multi-Band UWB Radios. , 0, , .		7
140	An Accurate Automatic Quality-Factor Tuning Scheme for Second-Order <emphasis emphasistype="italic">LC</emphasis> Filters. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 2007, 54, 745-756.	0.1	7
141	A 140mA 90nm CMOS low drop-out regulator with &#x2212;56dB power supply rejection at 10MHz. , 2010, , .		7
142	Current-Reused 2.4-GHz Direct-Modulation Transmitter With On-Chip Automatic Tuning. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2013, 21, 732-746.	3.1	7
143	Reconfigurable System for Electromagnetic Energy Harvesting With Inherent Activity Sensing Capabilities for Wearable Technology. IEEE Transactions on Circuits and Systems II: Express Briefs, 2019, 66, 1302-1306.	3.0	7
144	A CMOS Four Quadrant Current/Transconductance Multiplier. Analog Integrated Circuits and Signal Processing, 1999, 19, 163-168.	1.4	6

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145	THD+Noise Estimation in Class-D Amplifiers. , 2007, , .		6
146	Biquadratic programmable sc filters with additional flexibility and reduced total capacitance. International Journal of Circuit Theory and Applications, 1989, 17, 241-248.	2.0	5
147	Large-image CNN hardware processing using a time multiplexing scheme. , 0, , .		5
148	A parallel analog median filter. , 0, , .		5
149	A programmable rail-to-rail constant-g/sub m/ input structure for LV amplifier. , 0, , .		5
150	Different operational transconductance amplifier topologies for obtaining very small transconductances. , 0, , .		5
151	A monolithic CMOS low-IF Bluetooth receiver. , 0, , .		5
152	Design considerations of bandpass LC filters for RF applications. , 0, , .		5
153	A 2.1-GHz monolithic frequency synthesizer with robust phase switching prescaler and loop capacitance scaling. , 0, , .		5
154	Design tradeoffs of CMOS current mirrors using one-equation for all-region model. , 0, , .		5
155	Design considerations in a BiCMOS dual-modulus prescaler. , 0, , .		5
156	A 2â€"1100 MHz wideband low noise amplifier with 1.43 dB minimum noise figure. , 2010, , .		5
157	A PVT-Resilient, Highly-Linear Fifth-Order Ring-Oscillator-Based Filter. IEEE Transactions on Circuits and Systems I: Regular Papers, 2020, 67, 4295-4308.	5.4	5
158	Analog/RF IP Protection: Attack Models, Defense Techniques, and Challenges. IEEE Transactions on Circuits and Systems II: Express Briefs, 2021, 68, 36-41.	3.0	5
159	A flexible and expendable neuroimage processor architecture. IEEE Transactions on Circuits and Systems Part 1: Regular Papers, 1999, 46, 1055-1063.	0.1	4
160	High-selectivity SC filters with continuous digital Q-factor programmability. , 0, , .		4
161	Floating gate analog implementation of the additive soft-input soft-output decoding algorithm. , 0, , .		4
162	A universal fast battery charging and management solution for stand-alone solar photovoltaic home systems in Sub-Saharan Africa. , 2017, , .		4

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163	Efficient use of gain-bandwidth product in active filters: Gm-C and Active-R alternatives. , 2017, , .		4
164	Schmitt Trigger-Based Key Provisioning for Locking Analog/RF Integrated Circuits. , 2020, , .		4
165	Frequency-domain intrachip communication schemes for CNN. , 0, , .		3
166	VLSI implementation of a neural network for solving linear second order parabolic PDE. , 0, , .		3
167	Analog implementation of an active noise controller system for portable audio applications. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2001, 48, 400-404.	2.2	3
168	Design trade-offs of a symmetric linearized CMOS LC VCO. , 0, , .		3
169	A 2.1GHz 1.3V 5mW programmable Q-enhancement LC bandpass biquad in 0.35Î¼m CMOS. , 0, , .		3
170	Series/Parallel Time-Multiplexed Switched-Capacitor Filters with Programmability Based on Non-Uniform Sampling. Analog Integrated Circuits and Signal Processing, 2006, 46, 241-252.	1.4	3
171	An ultra-low power power management unit with &#x2212;40dB switching-noise-suppression for a 3&#x00D7;3 thermoelectric generator array with 57% maximum end-to-end efficiency. , 2014, , .		3
172	Taming the Stability-Constrained Performance Optimization Challenge of Distributed On-Chip Voltage Regulation. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2019, 38, 1571-1584.	2.7	3
173	Optimal manufacturable CNN array size for time multiplexing schemes. , 0, , .		2
174	Wavelets generation using Laguerre analog adaptive filter. , 0, , .		2
175	The limitation of CMRR in low voltage operational amplifier with N-P input pairs. , 0, , .		2
176	A unified approach for a time-domain built-in self-test technique and fault detection. , 0, , .		2
177	Simple CMOS low-voltage op amps with constant-g/sub m/ rail-to-rail input stage. , 0, , .		2
178	1.5 V 5.0 MHz switched capacitor circuits in 1.2 Î¼m CMOS without voltage bootstrapper. , 0, , .		2
179	A mixed-mode IF GFSK demodulator for Bluetooth. , 0, , .		2
180	State space approach to design of continuous time sigma delta ADC with delay in feedback path. Midwest Symposium on Circuits and Systems, 2006, , .	1.0	2

#	Article	IF	Citations
181	An 11-Band 3.4 to 10.3 GHz MB-OFDM UWB Receiver in 0.25Î¼m SiGe BiCMOS. , 0, , .		2
182	New applications and technology scaling driving next generation A/D converters. , 2009, , .		2
183	Low power complementary metalâ€oxide semiconductor classâ€G audio amplifier with gradual power supply switching. IET Circuits, Devices and Systems, 2015, 9, 256-264.	1.4	2
184	A biopotential amplifier with dynamic capacitor matching for improved CMRR. Analog Integrated Circuits and Signal Processing, 2015, 82, 47-55.	1.4	2
185	Design Trade-Offs in Common-Mode Feedback Implementations for Highly Linear Three-Stage Operational Transconductance Amplifiers. Electronics (Switzerland), 2021, 10, 991.	3.1	2
186	A CMOS Energy Harvesting Interface Circuit With Cycle-to-Cycle Frequency-to-Amplitude Conversion MPPT for Centimeter-Scale Wind Turbine. IEEE Transactions on Circuits and Systems I: Regular Papers, 2021, 68, 3587-3597.	5.4	2
187	Fully integrated MOSFET-C variable equalizer circuit with on-chip automatic tuning. , 0, , .		1
188	A very fast CMOS artificial cellular neural network. , 0, , .		1
189	Monolithic mixed-mode implementation of sum-of-product arrays for performing binary morphological image processing. , 0, , .		1
190	A low mismatch sensitivity fully-balanced current-mode integrator. , 0, , .		1
191	Next generation wideband multi-standard digital receiver design. , 0, , .		1
192	Fully-integrated LC VCOs at RF on silicon. , 0, , .		1
193	An auto-tuning structure for continuous time sigma-delta AD converter and high precision filters. , 0, , .		1
194	An improved Q-tuning scheme and a fully symmetric OTA. , 0, , .		1
195	A 3 V, 0.35 Î¼m CMOS Bluetooth receiver IC. , 0, , .		1
196	RF bandpass filter design using capacitive degeneration. , 2005, , .		1
197	A systematic system level design methodology for dual band CMOS RF receivers. Midwest Symposium on Circuits and Systems, 2007, , .	1.0	1
198	A low power 1.3GHz dual-path current mode Gm-C filter. , 2008, , .		1

#	Article	IF	Citations
199	A 470&#x03BC;W clock-free current-controlled class D amplifier with 0.02% THD&#x002B;N and 82dB PSRR. , 2010, , .		1
200	A 0.6-to-200MSPS speed reconfigurable and 1.9-to-27mW power scalable 10bit ADC. , 2011, , .		1
201	A biopotential amplifier with improved common mode gain. , 2013, , .		1
202	Noise-sensitive feedback loop identification in linear time-varying analog circuits. , 2017, , .		1
203	An On-Chip Built-in Linearity Estimation Methodology and Hardware Implementation. IEEE Transactions on Circuits and Systems I: Regular Papers, 2019, 66, 897-908.	5.4	1
204	Finite gain - bandwidth product effects on a pair of pseudo-N-path SC filters. IEEE Transactions on Circuits and Systems, 1984, 31, 583-584.	0.9	0
205	Low voltage current-mode filters: high performance and limitations. , 0, , .		0
206	VLSI implementation of an extended Hamming neural network for non-binary pattern recognition. , 0, , .		0
207	A classifier system with low sensitivity to pattern shifted position. , 0, , .		0
208	Building blocks for filter tuning system using analog VLSI fuzzy logic controller. , 0, , .		0
209	Optimal design of low power nested GM-C compensation amplifiers using a current-based MOS transistor model. , 0, , .		0
210	Efficient clock recovery architecture. , 0, , .		0
211	A low voltage fully differential nested G/sub m/ capacitance compensation amplifier: analysis and design. , 0, , .		0
212	A 2-V 11-bit incremental A/D converter using floating gate technique. , 0, , .		0
213	A 16mW, 2.23-2.45GHz fully integrated Î£Î” PLL with novel prescaler and loop filter in 0.35Î¼m CMOS. , 0, , .		0
214	Radio frequency wireless systems: basics and IC implementations. , 0, , .		0
215	On the monolithic design of hysteretic chaotic oscillators. , 0, , .		0