

# Tei-Wei Kuo

## List of Publications by Year in descending order

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269  
papers

4,060  
citations

331670

21  
h-index

254184

43  
g-index

273  
all docs

273  
docs citations

273  
times ranked

1619  
citing authors

#	ARTICLE	IF	CITATIONS
1	MVLevelDB: Using Log-Structured Tree to Support Temporal Queries in IoT. IEEE Internet of Things Journal, 2022, 9, 7815-7825.	8.7	0
2	Online Rare Category Identification and Data Diversification for Edge Computing. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2022, 41, 1290-1301.	2.7	0
3	SEOF-Net: Compression and Acceleration of Deep Neural Networks for Speech Enhancement Using Sign-Exponent-Only Floating-Points. IEEE/ACM Transactions on Audio Speech and Language Processing, 2022, 30, 1016-1031.	5.8	3
4	TRACE: A Fast Transformer-based General-Purpose Lossless Compressor. , 2022, , .		1
5	RM-SSD: In-Storage Computing for Large-Scale Recommendation Inference. , 2022, , .		6
6	Read-Ahead Efficiency on Mobile Devices: Observation, Characterization, and Optimization. IEEE Transactions on Computers, 2021, 70, 99-110.	3.4	2
7	Accelerating data filtering for database using FPGA. Journal of Systems Architecture, 2021, 114, 101908.	4.3	6
8	Future Computing Platform Design: A Cross-Layer Design Approach. , 2021, , .		6
9	Space-efficient Graph Data Placement to Save Energy of ReRAM Crossbar. , 2021, , .		2
10	A Write-friendly Arithmetic Coding Scheme for Achieving Energy-Efficient Non-Volatile Memory Systems. , 2021, , .		8
11	Scheduling-Aware Prefetching: Enabling the PCIe SSD to Extend the Global Memory of GPU Device. , 2021, , .		1
12	Joint Management of CPU and NVDIMM for Breaking Down the Great Memory Wall. IEEE Transactions on Computers, 2020, 69, 722-733.	3.4	15
13	When Storage Response Time Catches Up With Overall Context Switch Overhead, What Is Next?. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2020, 39, 4266-4277.	2.7	11
14	Request Flow Coordination for Growing-Scale Solid-State Drives. IEEE Transactions on Computers, 2020, 69, 832-843.	3.4	0
15	Overheating-Avoidance Remapping Scheme for Reliability Enhancement of 3D PCM Storage Systems. , 2020, , .		0
16	On Improving the Write Responsiveness for Host-Aware SMR Drives. IEEE Transactions on Computers, 2019, 68, 111-124.	3.4	13
17	Enabling Failure-resilient Intermittently-powered Systems Without Runtime Checkpointing. , 2019, , .		15
18	Multiversion Concurrency Control on Intermittent Systems. , 2019, , .		12

#	ARTICLE	IF	CITATIONS
19	Fast Frequent Pattern Mining without Candidate Generations on GPU by Low Latency Memory Allocation. , 2019, , .		4
20	Boosting NVDIMM Performance With a Lightweight Caching Algorithm. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2018, 26, 1518-1530.	3.1	5
21	An SLC-Like Programming Scheme for MLC Flash Memory. ACM Transactions on Storage, 2018, 14, 1-26.	2.1	2
22	Real-Time Computing and the Evolution of Embedded System Designs. , 2018, , .		4
23	Duet: An OLED & GPU Co-management Scheme for Dynamic Resolution Adaptation. , 2018, , .		4
24	Hot-Spot Suppression for Resource-Constrained Image Recognition Devices With Nonvolatile Memory. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 2567-2577.	2.7	18
25	Proactive Channel Adjustment to Improve Polar Code Capability for Flash Storage Devices. , 2018, , .		2
26	Scrubbing-Aware Secure Deletion for 3-D NAND Flash. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2018, 37, 2790-2801.	2.7	21
27	Boosting the performance with a data-backup-free programming scheme for TLC-based SSDs. , 2018, , .		2
28	Oasis: A Mobile Cyber-Physical System for Accessible Location Exploration. Proceedings of the IEEE, 2018, 106, 1744-1759.	21.3	4
29	Improving PCM Endurance with a Constant-Cost Wear Leveling Design. ACM Transactions on Design Automation of Electronic Systems, 2017, 22, 1-27.	2.6	21
30	Write-aware memory management for hybrid SLC-MLC PCM memory systems. ACM SIGAPP Applied Computing Review: A Publication of the Special Interest Group on Applied Computing, 2017, 17, 16-26.	0.9	7
31	Antiwear Leveling Design for SSDs With Hybrid ECC Capability. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2017, 25, 488-501.	3.1	10
32	A fast non-volatile memory aware algorithm for generating random scale-free networks. , 2017, , .		0
33	Memory Bank Partitioning for Fixed-Priority Tasks in a Multi-core System. , 2017, , .		7
34	Distillation: A light-weight data separation design to boost performance of NVDIMM main memory. , 2017, , .		1
35	Virtual persistent cache: Remedy the long latency behavior of host-aware shingled magnetic recording drives. , 2017, , .		12
36	How to enable software isolation and boost system performance with sub-block erase over 3D flash memory. , 2016, , .		14

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37	Designing CPS/IoT applications for smart buildings and cities. IET Cyber-Physical Systems: Theory and Applications, 2016, 1, 3-12.	3.3	87
38	Enabling Hybrid PCM Memory System with Inherent Memory Management. , 2016, , .		2
39	Framework designs to enhance reliable and timely services of disaster management systems. , 2016, , .		3
40	Reducing Data Migration Overheads of Flash Wear Leveling in a Progressive Way. IEEE Transactions on Very Large Scale Integration (VLSI) Systems, 2016, 24, 1808-1820.	3.1	15
41	A semantics-aware design for mounting remote sensors on mobile systems. , 2016, , .		7
42	Pattern-aware write-back strategy to minimize energy consumption of PCM-based storage systems. , 2016, , .		2
43	A disturbance-aware sub-block design to improve reliability of 3D MLC flash memory. , 2016, , .		5
44	Many-Core Real-Time Task Scheduling with Scratchpad Memory. IEEE Transactions on Parallel and Distributed Systems, 2016, 27, 2953-2966.	5.6	7
45	Virtual Flash Chips: Reinforcing the Hardware Abstraction Layer to Improve Data Recoverability of Flash Devices. IEEE Transactions on Computers, 2016, 65, 2872-2883.	3.4	5
46	User-Centric Scheduling and Governing on Mobile Devices with big.LITTLE Processors. Transactions on Embedded Computing Systems, 2016, 15, 1-22.	2.9	14
47	Disturbance Relaxation for 3D Flash Memory. IEEE Transactions on Computers, 2016, 65, 1467-1483.	3.4	11
48	Capacity-Independent Address Mapping for Flash Storage Devices with Explosively Growing Capacity. IEEE Transactions on Computers, 2016, 65, 448-465.	3.4	7
49	On relaxing page program disturbance over 3D MLC flash memory. , 2015, , .		9
50	Rethinking I/O request management over eMMC-based solid-state drives. , 2015, , .		4
51	A user-centric CPU-GPU governing framework for 3D games on mobile devices. , 2015, , .		15
52	A light-weighted software-controlled cache for PCM-based main memory systems. , 2015, , .		6
53	Access pattern reshaping for eMMC-enabled SSDs. , 2015, , .		3
54	How to improve the space utilization of dedup-based PCM storage devices?. , 2015, , .		1

#	ARTICLE	IF	CITATIONS
55	Marching-Based Wear-Leveling for PCM-Based Storage Systems. ACM Transactions on Design Automation of Electronic Systems, 2015, 20, 1-22.	2.6	22
56	Reliability-aware striping with minimized performance overheads for flash-based storage devices. , 2015, , .		0
57	Achieving SLC performance with MLC flash memory. , 2015, , .		13
58	Virtual flash chips. , 2015, , .		7
59	Real-Time Task Scheduling on Island-Based Multi-Core Platforms. IEEE Transactions on Parallel and Distributed Systems, 2015, 26, 538-550.	5.6	12
60	PWL: A Progressive Wear Leveling to Minimize Data Migration Overheads for NAND Flash Devices. , 2015, , .		9
61	An Adaptive Endurance-Aware $\{B^+\}$ -Tree for Flash Memory Storage Systems. IEEE Transactions on Computers, 2014, 63, 2661-2673.	3.4	18
62	Application behavior analysis in resource consumption for mobile devices. , 2014, , .		5
63	A PCM translation layer for integrated memory and storage management. , 2014, , .		15
64	User-Centric Energy-Efficient Scheduling on Multi-Core Mobile Devices. , 2014, , .		24
65	Garbage collection and wear leveling for flash memory: Past and future. , 2014, , .		57
66	Computation Offloading by Using Timing Unreliable Components in Real-Time Systems. , 2014, , .		19
67	Adaptive range-based address mapping for the flash storage devices with explosive capacity. , 2014, , .		0
68	On Trading Wear-leveling with Heal-leveling. , 2014, , .		16
69	Endurance-aware clustering-based mining algorithm for non-volatile phase-change memory. , 2014, , .		2
70	The acceleration of pipeline workloads under the FPGA area and bandwidth constraints. , 2014, , .		1
71	Current-aware scheduling for flash storage devices. , 2014, , .		3
72	Profit-aware base station operation for green cellular networks. , 2014, , .		4

#	ARTICLE	IF	CITATIONS
73	Accelerator-Aware Task Synchronization for Real-Time Systems. , 2014, , .		2
74	Deadline-aware load balancing for MapReduce. , 2014, , .		0
75	Energy-aware data placement strategy for SSD-assisted streaming video servers. , 2014, , .		2
76	Endurance-Aware Flash-Cache Management for Storage Servers. IEEE Transactions on Computers, 2014, 63, 2416-2430.	3.4	8
77	Bootling Time Minimization for Real-Time Embedded Systems with Non-Volatile Memory. IEEE Transactions on Computers, 2014, 63, 847-859.	3.4	18
78	A classification-based prefetching scheme for hibernation of embedded computing systems. , 2014, , .		1
79	On trading wear-leveling with heal-leveling. , 2014, , .		0
80	Real-time process synchronization for systems with accelerators. , 2014, , .		0
81	Real-time partitioned scheduling on multi-core systems with local and global memories. , 2013, , .		2
82	A disturb-alleviation scheme for 3D flash memory. , 2013, , .		19
83	A DRAM-flash index for native flash file systems. , 2013, , .		0
84	An efficient B+-tree design for main-memory database systems with strong access locality. Information Sciences, 2013, 232, 325-345.	6.9	5
85	Reliability Enhancement of Flash-Memory Storage Systems: An Efficient Version-Based Design. IEEE Transactions on Computers, 2013, 62, 2503-2515.	3.4	16
86	A reliability enhancement design under the flash translation layer for MLC-based flash-memory storage systems. Transactions on Embedded Computing Systems, 2013, 13, 1-28.	2.9	17
87	Messages from the conference chairs. , 2013, , .		0
88	MLC-flash-friendly logging and recovery for databases. , 2013, , .		2
89	An index-based management scheme with adaptive caching for huge-scale low-cost embedded flash storages. ACM Transactions on Design Automation of Electronic Systems, 2013, 18, 1-26.	2.6	6
90	Joint management of performance-predictable virtualized storage devices with hard disk and flash memory. , 2013, , .		0

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91	Joint management of RAM and flash memory with access pattern considerations. , 2012, , .		13
92	Working-set-based address mapping for ultra-large-scaled flash devices. , 2012, , .		13
93	An adaptive file-system-oriented FTL mechanism for flash-memory storage systems. Transactions on Embedded Computing Systems, 2012, 11, 1-19.	2.9	23
94	Age-based PCM wear leveling with nearly zero search cost. , 2012, , .		78
95	System-wide profiling and optimization with virtual machines. , 2012, , .		9
96	Multilayer Bus Optimization for Real-Time Embedded Systems. IEEE Transactions on Computers, 2012, 61, 1638-1650.	3.4	9
97	A real-time, energy-efficient system software suite for heterogeneous multicore platforms. , 2012, , .		5
98	Partitioned scheduling for real-time tasks on multiprocessor embedded systems with programmable shared srams. , 2012, , .		4
99	Advanced Issues of Operating Systems for Reliable Distributed Sensor Networks: Aim and Scope. International Journal of Distributed Sensor Networks, 2012, 8, 394-197.	2.2	0
100	Power management strategies in data transmission. , 2011, , .		0
101	A flash-friendly B&sup>&#x002B;&lt;/sup>-tree with endurance-awareness. , 2011, , .		0
102	A QoS Guaranteed Cache Design for Environment Friendly Computing. , 2011, , .		0
103	A Management Strategy for the Reliability and Performance Improvement of MLC-Based Flash-Memory Storage Systems. IEEE Transactions on Computers, 2011, 60, 305-320.	3.4	23
104	An efficient fault detection algorithm for NAND flash memory. ACM SIGAPP Applied Computing Review: A Publication of the Special Interest Group on Applied Computing, 2011, 11, 8-16.	0.9	7
105	Data Transmission with the Battery Utilization Maximization. Journal of Computer Science and Technology, 2011, 26, 392-404.	1.5	2
106	A driver-layer caching policy for removable storage devices. ACM Transactions on Storage, 2011, 7, 1-23.	2.1	7
107	Memory controllers for high-performance and real-time MPSoCs. , 2011, , .		16
108	A Run-Time Page Selection Methodology for Efficient Quality-Based Resuming. , 2011, , .		6

#	ARTICLE	IF	CITATIONS
109	Task synchronization and allocation for many-core real-time systems. , 2011, , .		25
110	Guest Editorial &lt;newline/&gt;Special Section on Real-Time Applications and Tools Design. IEEE Transactions on Industrial Informatics, 2010, 6, 592-593.	11.3	1
111	Processing element allocation and dynamic scheduling codesign for multi-function SoCs. Real-Time Systems, 2010, 44, 72-104.	1.3	2
112	Guest Editorial: Special Issue on EUC 2007. Journal of Signal Processing Systems, 2010, 59, 1-2.	2.1	3
113	Maximum-residual multicasting and aggregating in wireless ad hoc networks. Wireless Networks, 2010, 16, 701-711.	3.0	0
114	A reliable MTD design for MLC flash-memory storage systems. , 2010, , .		24
115	A strategy to emulate NOR flash with NAND flash. ACM Transactions on Storage, 2010, 6, 1-23.	2.1	89
116	An Efficient FTL Design for Multi-chipped Solid-State Drives. , 2010, , .		7
117	Improving Flash Wear-Leveling by Proactively Moving Static Data. IEEE Transactions on Computers, 2010, 59, 53-65.	3.4	101
118	An Adaptive Flash Translation Layer for High-Performance Storage Systems. IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, 2010, 29, 953-965.	2.7	18
119	Support of Android lab modules for embedded system curriculum. , 2010, , .		7
120	Energy-efficient real-time scheduling of multimedia tasks on multi-core processors. , 2010, , .		20
121	Energy-Efficient Task Synchronization for Real-Time Systems. IEEE Transactions on Industrial Informatics, 2010, 6, 287-301.	11.3	15
122	Energy-efficient real-time task scheduling with temperature-dependent leakage. , 2010, , .		0
123	Energy-Efficient Mapping Technique for Virtual Cores. , 2010, , .		3
124	Multi-layer Bus Optimization for Real-Time Task Scheduling with Chain-Based Precedence Constraints. , 2009, , .		2
125	Energy reduction techniques for systems with non-DVS components. , 2009, , .		3
126	QoS for storage subsystems using IEEE-1394. ACM Transactions on Storage, 2009, 4, 1-17.	2.1	3



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127	A commitment-based management strategy for the performance and reliability enhancement of flash-memory storage systems. , 2009, , .		41
128	A DVS-assisted hard real-time I/O device scheduling algorithm. Real-Time Systems, 2009, 41, 222-255.	1.8	2
129	An anomaly prevention approach for real-time task scheduling. Journal of Systems and Software, 2009, 82, 144-154.	4.5	8
130	A file-system-aware FTL design for flash-memory storage systems. , 2009, , .		0
131	An approximation scheme for energy-efficient scheduling of real-time tasks in heterogeneous multiprocessor systems. , 2009, , .		10
132	The Speech Quality Analysis of Push-to-Talk Services. , 2009, , .		0
133	Managing Imprecise Worst Case Execution Times on DVFS Platforms. , 2009, , .		3
134	A Maximum-Residual Multicast Protocol for Large-Scale Mobile Ad Hoc Networks. IEEE Transactions on Mobile Computing, 2009, 8, 1441-1453.	5.8	9
135	A Reconfigurable Virtual Storage Device. , 2009, , .		4
136	Energy-efficiency for multiframe real-time tasks on a dynamic voltage scaling processor. , 2009, , .		7
137	Guest Editorial: Special Issue on Real-Time Systemsâ€™Part II. IEEE Transactions on Industrial Informatics, 2009, 5, 1-2.	11.3	2
138	Configurable Flash-Memory Management: Performance versus Overheads. IEEE Transactions on Computers, 2008, 57, 1571-1583.	3.4	17
139	The Behavior Analysis of Flash-Memory Storage Systems. , 2008, , .		24
140	Special Issues in Flash. , 2008, , .		34
141	Approximation Algorithms for Multiprocessor Energy-Efficient Scheduling of Periodic Real-Time Tasks with Uncertain Task Execution Time. , 2008, , .		16
142	Special track on Adaptive Techniques in Operating Systems. , 2008, , .		0
143	An Integrated Deployment Tool for ZigBee-Based Wireless Sensor Networks. , 2008, , .		7
144	Multiprocessor frequency locking for real-time task synchronization. , 2008, , .		4

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145	The minimization of hardware size in reconfigurable embedded platforms. , 2008, , .		0
146	A Secure Routing Protocol for Wireless Embedded Networks. , 2008, , .		7
147	Energy-Efficient Real-Time Co-scheduling of Multimedia DSP Jobs. , 2008, , .		3
148	Component Model and Architecture of Smart Devices for Elderly. , 2008, , .		5
149	Discrete Frequency Selection of Frame-Based Stochastic Real-Time Tasks. , 2008, , .		2
150	A Signature-based Grid Index Design for RFID Main-Memory Databases. , 2008, , .		2
151	A NOR Emulation Strategy over NAND Flash Memory. Gifted and Talented International, 2007, , .	0.8	21
152	Energy-efficient and performance-enhanced disks using flash-memory cache. , 2007, , .		16
153	Preemption Control for Energy-Efficient Task Scheduling in Systems with a DVS Processor and Non-DVS Devices. Gifted and Talented International, 2007, , .	0.8	12
154	Energy-Efficient Real-Time Task Scheduling with Task Rejection. , 2007, , .		12
155	Search-Oriented Deployment Strategies for Wireless Sensor Networks. , 2007, , .		23
156	Real-Time Task Replication for Fault Tolerance in Identical Multiprocessor Systems. Real Time and Embedded Technology and Applications Symposium (RTAS), IEEE, 2007, , .	0.0	22
157	Time-Division-Based Cyclic Scheduling for UMTS High-Speed Downlink Shared-Channels. IEEE Transactions on Vehicular Technology, 2007, 56, 2086-2094.	6.3	3
158	On the Minimization fo the Instantaneous Temperature for Periodic Real-Time Tasks. , 2007, , .		44
159	System-Level Energy-Efficiency for Real-Time Tasks. , 2007, , .		15
160	Flow Time Minimization under Energy Constraints. , 2007, , .		0
161	FL-PCP: Frequency Locking for Energy-Efficient Real-Time Task Synchronization. , 2007, , .		6
162	Procrastination determination for periodic real-time tasks in leakage-aware dynamic voltage scaling systems.. IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2007, , .	0.0	15

#	ARTICLE	IF	CITATIONS
163	3D Video Applications and Intelligent Video Surveillance Camera and its VLSI Design. , 2007, , .		0
164	Endurance enhancement of flash-memory storage systems. Proceedings - Design Automation Conference, 2007, , .	0.0	142
165	An efficient B-tree layer implementation for flash-memory storage systems. Transactions on Embedded Computing Systems, 2007, 6, 19.	2.9	139
166	Energy-Efficient Real-Time Task Scheduling in Multiprocessor DVS Systems. , 2007, , .		37
167	Dynamic Task Scheduling and Processing Element Allocation for Multi-Function SoCs. Real Time and Embedded Technology and Applications Symposium (RTAS), IEEE, 2007, , .	0.0	6
168	Scheduling for time-division based shared channel allocation for UMTS. Wireless Networks, 2007, 13, 189-202.	3.0	7
169	Endurance Enhancement of Flash-Memory Storage, Systems: An Efficient Static Wear Leveling Design. Proceedings - Design Automation Conference, 2007, , .	0.0	10
170	Energy-Efficiency on a Variable-Bitrate Device. , 2007, , 604-616.		0
171	Allocation Cost Minimization for Periodic Hard Real-Time Tasks in Energy-Constrained DVS Systems. IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006, , .	0.0	5
172	Component-oriented radars with probabilistic timing guarantees. IEEE Transactions on Parallel and Distributed Systems, 2006, 17, 723-735.	5.6	0
173	Energy-Efficient Real-Time Task Scheduling for a DVS System with a Non-DVS Processing Element. , 2006, , .		53
174	AGA: Adaptive GTS Allocation with Low Latency and Fairness Considerations for IEEE 802.15.4. , 2006, , .		16
175	Multiprocessor Synthesis for Periodic Hard Real-Time Tasks under a Given Energy Constraint. , 2006, , .		7
176	Procrastination for leakage-aware rate-monotonic scheduling on a dynamic voltage scaling processor. ACM SIGPLAN Notices, 2006, 41, 153-162.	0.2	10
177	Procrastination for leakage-aware rate-monotonic scheduling on a dynamic voltage scaling processor. , 2006, , .		42
178	A faster exact schedulability analysis for fixed-priority scheduling. Journal of Systems and Software, 2006, 79, 1744-1753.	4.5	9
179	Resource allocation framework for distributed real-time end-to-end tasks. , 2006, , .		2
180	Threat-Based Configuration Architecture for Security Gateways. , 2006, , .		1

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181	The Design of efficient initialization and crash recovery for log-based file systems over flash memory. ACM Transactions on Storage, 2006, 2, 449-467.	2.1	37
182	Efficient initialization and crash recovery for log-based file systems over flash memory. , 2006, , .		17
183	An adaptive two-level management for the flash translation layer in embedded systems. IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006, , .	0.0	92
184	Efficient identification of hot data for flash memory storage systems. ACM Transactions on Storage, 2006, 2, 22-40.	2.1	163
185	Dynamic Load Balancing for Multiple Processors. , 2006, , .		2
186	An Adaptive Two-Level Management for the Flash Translation Layer in Embedded Systems. IEEE/ACM International Conference on Computer-Aided Design, Digest of Technical Papers, 2006, , .	0.0	9
187	Multi-disk scheduling for time-constrained requests in RAID-0 devices. Journal of Systems and Software, 2005, 76, 237-250.	4.5	2
188	$1 + \hat{\mu}$ approximation clock rate assignment for periodic real-time tasks on a voltage-scaling processor. , 2005, , .		36
189	Efficient management for large-scale flash-memory storage systems with resource conservation. ACM Transactions on Storage, 2005, 1, 381-418.	2.1	105
190	Real-time task scheduling anomaly. , 2005, , .		8
191	Efficient on-line identification of hot data for flash-memory management. , 2005, , .		46
192	Real-time dwell scheduling of component-oriented phased array radars. IEEE Transactions on Computers, 2005, 54, 47-60.	3.4	29
193	Dual power assignment for network connectivity in wireless sensor networks. , 2005, , .		5
194	Power-Saving Scheduling for Weakly Dynamic Voltage Scaling Devices. Lecture Notes in Computer Science, 2005, , 338-349.	1.3	27
195	An efficient management scheme for large-scale flash-memory storage systems. , 2004, , .		75
196	Profit-driven uniprocessor scheduling with energy and timing constraints. , 2004, , .		17
197	Time division based shared channel allocation algorithm for UMTS. , 2004, , .		1
198	Real-time garbage collection for flash-memory storage systems of real-time embedded systems. Transactions on Embedded Computing Systems, 2004, 3, 837-863.	2.9	200

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199	Location management in cellular mobile computing systems with dynamic hierarchical location databases. Journal of Systems and Software, 2004, 69, 159-171.	4.5	7
200	Efficient validation of mobile transactions in wireless environments. Journal of Systems and Software, 2004, 69, 183-193.	4.5	32
201	Efficient location area planning for cellular networks with hierarchical location databases. Computer Networks, 2004, 45, 715-730.	5.1	10
202	Probabilistic real-time guarantees for component-oriented phased array radars. , 2004, , .		1
203	Resource Reservation and Enforcement for Framebuffer-Based Devices. Lecture Notes in Computer Science, 2004, , 398-408.	1.3	1
204	An Efficient B-Tree Layer for Flash-Memory Storage Systems. Lecture Notes in Computer Science, 2004, , 409-430.	1.3	44
205	Utilization bound revisited. IEEE Transactions on Computers, 2003, 52, 351-361.	3.4	32
206	Two-version based concurrency control and recovery in real-time client/server databases. IEEE Transactions on Computers, 2003, 52, 506-524.	3.4	12
207	Schedulability and performance analysis of the similarity stack protocol. IEEE Transactions on Computers, 2003, 52, 658-669.	3.4	0
208	A quantification of aborting effect for real-time data accesses. IEEE Transactions on Computers, 2003, 52, 670-675.	3.4	3
209	Efficient online schedulability tests for real-time systems. IEEE Transactions on Software Engineering, 2003, 29, 734-751.	5.6	23
210	Real-time digital signal processing of phased array radars. IEEE Transactions on Parallel and Distributed Systems, 2003, 14, 433-446.	5.6	3
211	An efficient r-tree implementation over flash-memory storage systems. , 2003, , .		74
212	Real-time concurrency control in a multiprocessor environment. IEEE Transactions on Parallel and Distributed Systems, 2002, 13, 659-671.	5.6	11
213	A class of rate-based real-time scheduling algorithms. IEEE Transactions on Computers, 2002, 51, 708-720.	3.4	10
214	Strategies for resolving inter-class data conflicts in mixed real-time database systems. Journal of Systems and Software, 2002, 61, 1-14.	4.5	4
215	Evaluation of concurrency control strategies for mixed soft real-time database systems. Information Systems, 2002, 27, 123-149.	3.6	16
216	Abort-oriented concurrency control for real-time databases. IEEE Transactions on Computers, 2001, 50, 660-673.	3.4	7

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217	RETINA. SIGMOD Record, 2001, 30, 615.	1.2	4
218	A multi-version data model for executing real-time transactions in a mobile environment. , 2001, , .		10
219	Concurrency control in mobile distributed real-time database systems. Information Systems, 2000, 25, 261-286.	3.6	44
220	Real-Time Access Control and Reservation on B-Tree Indexed Data. Real-Time Systems, 2000, 19, 245-281.	1.3	1
221	A Software-Reuse Approach to Build Monitor Programs for Soft Real-Time Applications. Real-Time Systems, 2000, 19, 123-148.	1.3	1
222	The Reduced Ceiling Protocol for Concurrency Control in Real-time Databases with Mixed Transactions. Computer Journal, 2000, 43, 65-80.	2.4	3
223	Similarity-based load adjustment for static real-time transaction systems. IEEE Transactions on Computers, 2000, 49, 112-126.	3.4	10
224	Real-time data semantics and similarity-based concurrency control. IEEE Transactions on Computers, 2000, 49, 1241-1254.	3.4	21
225	A similarity-based protocol for concurrency control in mobile distributed real-time database systems. Lecture Notes in Computer Science, 1999, , 329-338.	1.3	3
226	Real-time data access control on B-tree index structures. , 1999, , .		2
227	Incremental reconfiguration and load adjustment in adaptive real time systems. IEEE Transactions on Computers, 1997, 46, 1313-1324.	3.4	36
228	Real-time database " similarity and resource scheduling. SIGMOD Record, 1996, 25, 18-22.	1.2	16
229	The design and implementation of a Real-Time Object Management Interface. , 0, , .		1
230	BAP: a class of abort-oriented protocols based on the notion of compatibility. , 0, , .		3
231	Error propagation analysis of real-time data-intensive applications. , 0, , .		3
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