

Anirban Ganguly

List of Publications by Year in descending order

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3
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2682572

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2917675

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citing authors

#	ARTICLE	IF	CITATIONS
1	VLSI architecture for analog radix-4 DFT front-end in QAM-OFDM receiver. Analog Integrated Circuits and Signal Processing, 2020, 102, 169-179.	1.4	3
2	Precise realization of one-staged 2-D DCT using analog current mode architecture in compressed sensing front-end. Microelectronics Journal, 2021, 115, 105184.	2.0	2
3	A Novel Reconfigurable Analog VLSI Architecture of M-point DFT Using Complex Matrix Multiplier and Graph-Based Signal Routing Method. Circuits, Systems, and Signal Processing, 0, , .	2.0	0