

J W Bruce

List of Publications by Year in descending order

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Version: 2024-02-01

15
papers

234
citations

1478505

6
h-index

1588992

8
g-index

15
all docs

15
docs citations

15
times ranked

138
citing authors

#	ARTICLE	IF	CITATIONS
1	Efficient adder circuits based on a conservative reversible logic gate. , 0, , .		91
2	Cooperative and Progressive Design Experience for Embedded Systems. IEEE Transactions on Education, 2004, 47, 83-92.	2.4	56
3	The CRL gateway. IEEE Potentials, 2004, 23, 8-11.	0.3	14
4	Clustering algorithm for improved network lifetime of mobile wireless sensor networks. , 2017, , .		14
5	Personal digital assistant (PDA) based I2C bus analysis. IEEE Transactions on Consumer Electronics, 2003, 49, 1482-1487.	3.6	12
6	Nyquist-rate digital-to-analog converter architectures. IEEE Potentials, 2001, 20, 24-28.	0.3	10
7	An analysis of dynamic element matching flash digital-to-analog converters. IEEE Transactions on Circuits and Systems Part 2: Express Briefs, 2001, 48, 205-213.	2.2	10
8	Unsupervised Machine Learning Techniques for Detecting PLC Process Control Anomalies. Journal of Cybersecurity and Privacy, 2022, 2, 220-244.	3.9	6
9	CMOS current mode flash analog to digital converter. , 0, , .		5
10	A comparison of hardware efficient dynamic element matching networks for digital to analog converters. , 0, , .		4
11	Maintenance and monitoring object models for high-availability network appliances. IEEE Transactions on Consumer Electronics, 2004, 50, 472-477.	3.6	4
12	Meeting the analog world challenge. Nyquist-rate analog-to-digital converter architectures. IEEE Potentials, 1999, 17, 36-39.	0.3	3
13	CMOS current mode interpolating flash analog to digital converter. , 0, , .		3
14	An analysis of flash dynamic element matching analog to digital converters. , 0, , .		2
15	Adaptive design method for efficient direct digital synthesis. , 0, , .		0